



# SPEC® CINT2006 Result

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## Bull SAS

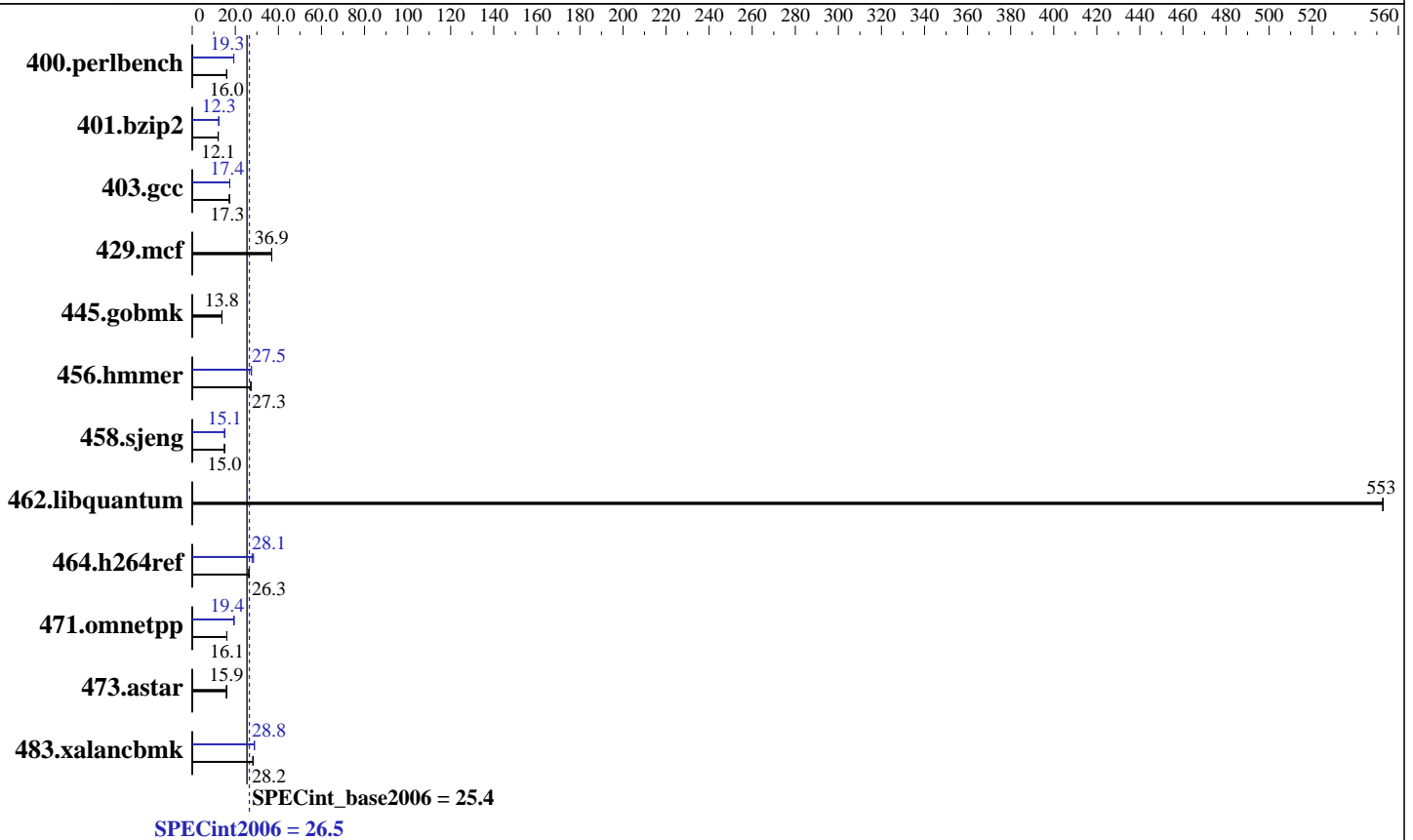
**SPECint®2006 = 26.5**

NovaScale T820 F3 (Intel Xeon E5-2403, 1.80 GHz)

**SPECint\_base2006 = 25.4**

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: Dell Inc.

Test date: Jun-2012  
Hardware Availability: Jun-2012  
Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2403  
 CPU Characteristics: 1800  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1067 MHz)  
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP2(x86\_64) 3.0.13-0.27-default  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	610	16.0	<b>610</b>	<b>16.0</b>	609	16.0	507	19.3	<b>506</b>	<b>19.3</b>	506	19.3
401.bzip2	800	12.1	801	12.1	<b>801</b>	<b>12.1</b>	782	12.3	784	12.3	<b>783</b>	<b>12.3</b>
403.gcc	<b>466</b>	<b>17.3</b>	465	17.3	467	17.2	462	17.4	461	17.4	<b>462</b>	<b>17.4</b>
429.mcf	247	36.9	<b>247</b>	<b>36.9</b>	247	36.9	247	36.9	<b>247</b>	<b>36.9</b>	247	36.9
445.gobmk	<b>760</b>	<b>13.8</b>	761	13.8	760	13.8	<b>760</b>	<b>13.8</b>	761	13.8	760	13.8
456.hammer	344	27.1	<b>342</b>	<b>27.3</b>	342	27.3	340	27.5	<b>340</b>	<b>27.5</b>	340	27.5
458.sjeng	<b>808</b>	<b>15.0</b>	809	15.0	807	15.0	804	15.1	804	15.0	<b>804</b>	<b>15.1</b>
462.libquantum	<b>37.5</b>	<b>553</b>	37.5	553	37.5	553	<b>37.5</b>	<b>553</b>	37.5	553	37.5	553
464.h264ref	<b>841</b>	<b>26.3</b>	843	26.3	841	26.3	777	28.5	<b>787</b>	<b>28.1</b>	796	27.8
471.omnetpp	390	16.0	<b>389</b>	<b>16.1</b>	388	16.1	324	19.3	<b>322</b>	<b>19.4</b>	321	19.5
473.astar	442	15.9	<b>441</b>	<b>15.9</b>	438	16.0	442	15.9	<b>441</b>	<b>15.9</b>	438	16.0
483.xalancbmk	245	28.2	244	28.3	<b>245</b>	<b>28.2</b>	239	28.9	<b>239</b>	<b>28.8</b>	239	28.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```

CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on linux-sxkz Fri Jun 22 14:00:11 2012

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2403 0 @ 1.80GHz
1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
cache size : 10240 KB

```

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### Platform Notes (Continued)

From /proc/meminfo

MemTotal: 49348896 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

SUSE Linux Enterprise Server 11 (x86\_64)

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 11 (x86\_64)

VERSION = 11

PATCHLEVEL = 2

uname -a:

Linux linux-sxkz 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012  
(d73692b) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jun 21 09:41 last=S

SPEC is set to: /root/CPU2006-1.2

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	ext3	271G	40G	218G	16%	/

Additional information from dmidecode:

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"

OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

The Dell PowerEdge T320 and

the Bull NovaScale T820 F3 models are electronically equivalent.

The results have been measured on a Dell PowerEdge T320 model



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## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
  
C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

445.gobmk: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-ansi-alias`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
-ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias`

458.sjeng: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4`

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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