SPEC® CFP2006 Result

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Huawei

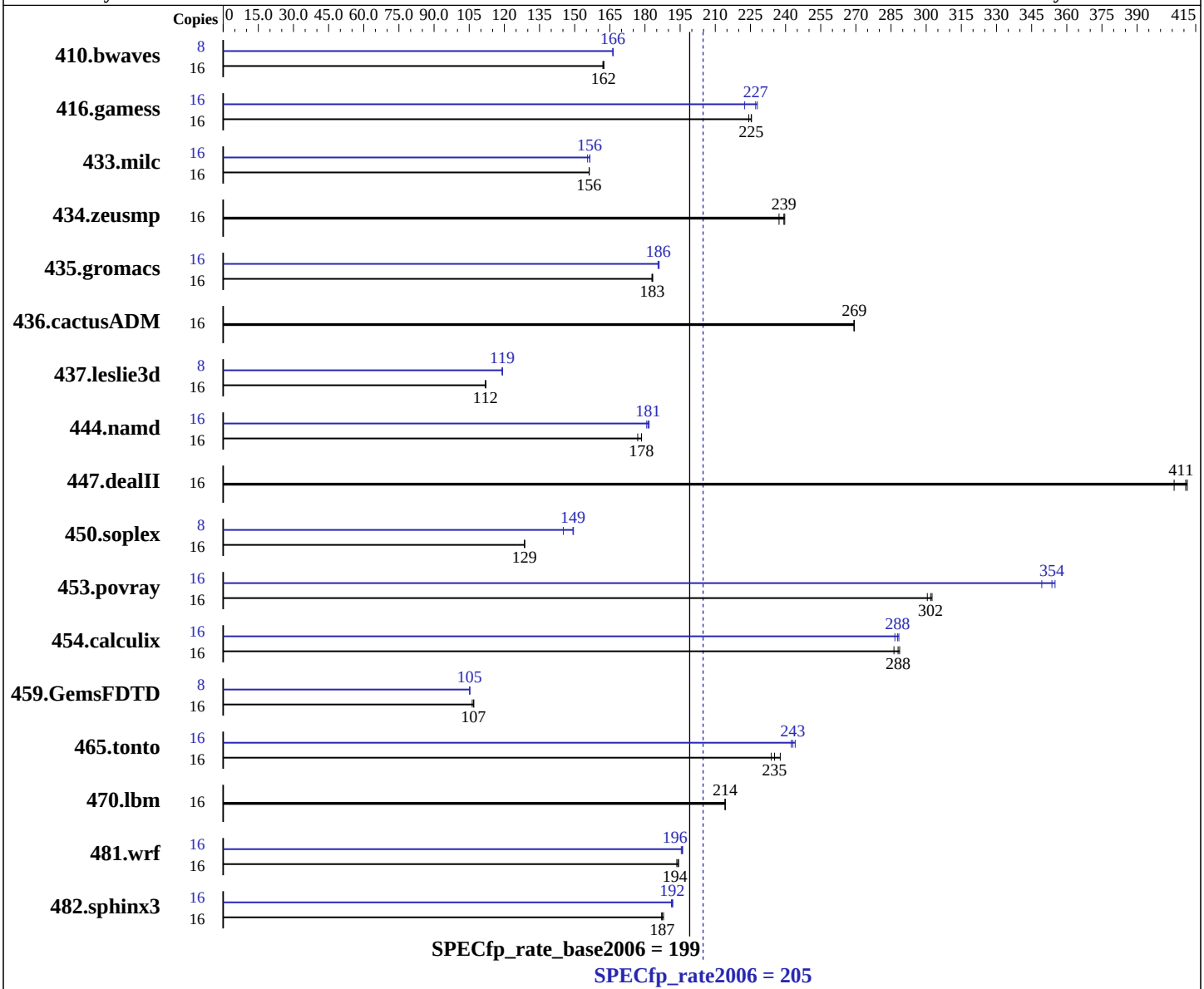
Huawei RH2285 V2 (Intel Xeon E5-2450)

SPECfp®_rate2006 = 205

SPECfp_rate_base2006 = 199

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Jul-2012
Hardware Availability: May-2012
Software Availability: Dec-2011



Hardware

CPU Name:	Intel Xeon E5-2450
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	8 cores, 1 chip, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 300 GB SAS, 10K RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1338	162	1342	162	**1340**	**162**	8	654	166	653	166	**654**	**166**
416.gamess	16	**1390**	**225**	1389	225	1397	224	16	**1379**	**227**	1408	223	1374	228
433.milc	16	940	156	941	156	**941**	**156**	16	**939**	**156**	939	156	945	155
434.zeusmp	16	608	240	**608**	**239**	614	237	16	608	240	**608**	**239**	614	237
435.gromacs	16	623	183	**624**	**183**	624	183	16	616	186	614	186	**615**	**186**
436.cactusADM	16	710	269	**710**	**269**	710	269	16	710	269	**710**	**269**	710	269
437.leslie3d	16	**1344**	**112**	1341	112	1344	112	8	632	119	**631**	**119**	631	119
444.namd	16	719	179	725	177	**719**	**178**	16	710	181	**708**	**181**	706	182
447.dealII	16	451	406	**446**	**411**	445	411	16	451	406	**446**	**411**	445	411
450.soplex	16	1037	129	1038	129	**1038**	**129**	8	460	145	**447**	**149**	446	150
453.povray	16	283	300	281	303	**282**	**302**	16	**241**	**354**	244	349	240	355
454.calculix	16	461	286	**458**	**288**	457	289	16	458	288	461	287	**459**	**288**
459.GemsFDTD	16	1597	106	1587	107	**1588**	**107**	8	807	105	807	105	**807**	**105**
465.tonto	16	673	234	662	238	**669**	**235**	16	**648**	**243**	650	242	645	244
470.lbm	16	1026	214	1026	214	**1026**	**214**	16	1026	214	1026	214	**1026**	**214**
481.wrf	16	923	194	919	194	**921**	**194**	16	**912**	**196**	911	196	914	195
482.sphinx3	16	1667	187	**1665**	**187**	1659	188	16	1630	191	**1627**	**192**	1625	192

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Select only test related files when installing the operating system
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on RH62-yjp2 Sat Jul 14 06:47:32 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2450 0 @ 2.10GHz
      1 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       49402980 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux RH62-yjp2 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 13 17:29

SPEC is set to: /spec
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/sda1      ext3    270G   46G  211G  18% /

Additional information from dmidecode:
  Memory:
   6x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"

 Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory
 using RHEL 6.1
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -static -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -opt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo -O3 -no-prec-div -prof-use(pass 2) -xSSE4.2 -opt-prefetch -static -auto-ilp32 -opt-mem-layout-trans=3

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Peak Optimization Flags (Continued)

436.cactusADM: `basepeak = yes`

454.calculix: `-xAVX -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-mem-layout-trans=3`

481.wrf: `Same as 454.calculix`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20120703.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20120703.xml`

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