SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

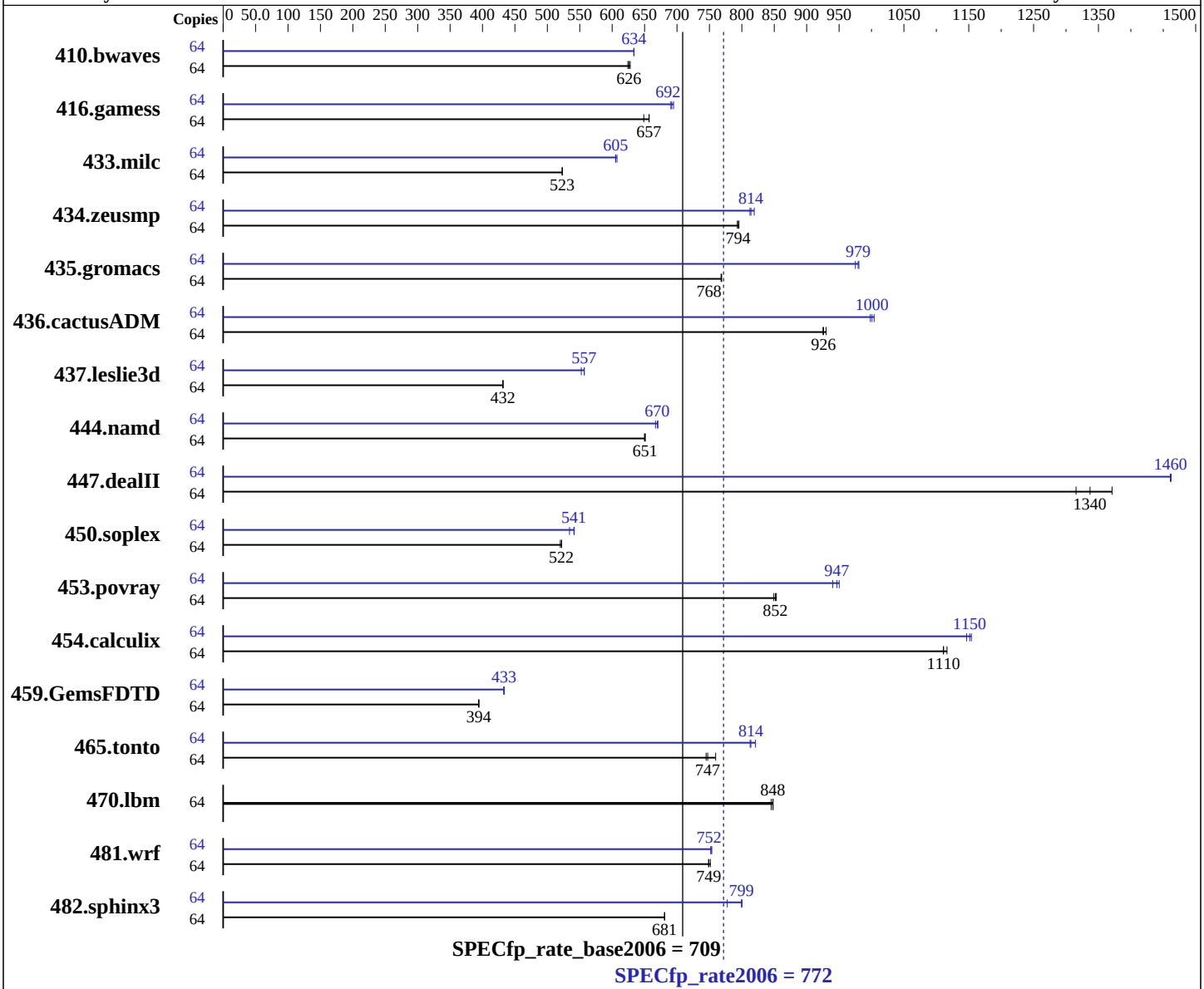
PowerEdge R815 (AMD Opteron 6278, 2.40 GHz)

SPECfp®_rate2006 = 772

SPECfp_rate_base2006 = 709

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2012
Hardware Availability: Jul-2012
Software Availability: Jul-2011



Hardware

CPU Name:	AMD Opteron 6278
CPU Characteristics:	AMD Turbo CORE technology up to 3.30 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	64 cores, 4 chips, 16 cores/chip
CPU(s) orderable:	2,4 chips

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.27-default
Compiler:	C/C++/Fortran: Version 4.5.1 of x86 Open64 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R815 (AMD Opteron 6278, 2.40 GHz)

SPECfp_rate2006 = 772

SPECfp_rate_base2006 = 709

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2012
Hardware Availability: Jul-2012
Software Availability: Jul-2011

Primary Cache: 512 KB I on chip per chip, 64 KB I shared / 2 cores; 16 KB D on chip per core
Secondary Cache: 16 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 8 cores
Other Cache: None
Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 3 x 146 GB SAS, 15000 RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	1393	624	**1389**	**626**	1385	628	64	**1373**	**634**	1372	634	1373	633
416.gamess	64	1907	657	**1908**	**657**	1932	649	64	1815	691	**1811**	**692**	1804	695
433.milc	64	**1123**	**523**	1124	523	1122	523	64	971	605	**970**	**605**	967	607
434.zeusmp	64	**733**	**794**	734	793	732	796	64	717	813	711	819	**715**	**814**
435.gromacs	64	**595**	**768**	595	768	594	769	64	469	975	**467**	**979**	466	981
436.cactusADM	64	827	925	**826**	**926**	822	930	64	762	1000	766	998	**764**	**1000**
437.leslie3d	64	1394	432	**1394**	**432**	1394	431	64	**1080**	**557**	1089	552	1080	557
444.namd	64	**789**	**651**	788	651	790	650	64	770	667	765	671	**766**	**670**
447.dealII	64	534	1370	**548**	**1340**	557	1320	64	501	1460	**501**	**1460**	501	1460
450.soplex	64	1026	520	**1023**	**522**	1023	522	64	999	534	986	542	**987**	**541**
453.povray	64	399	853	401	849	**400**	**852**	64	362	940	358	950	**360**	**947**
454.calculix	64	475	1110	473	1120	**475**	**1110**	64	460	1150	**459**	**1150**	458	1150
459.GemsFDTD	64	**1723**	**394**	1721	395	1723	394	64	**1568**	**433**	1566	434	1569	433
465.tonto	64	845	745	**843**	**747**	829	760	64	**774**	**814**	775	813	767	821
470.lbm	64	1037	848	**1037**	**848**	1040	845	64	1037	848	**1037**	**848**	1040	845
481.wrf	64	951	751	955	749	**954**	**749**	64	**950**	**752**	949	754	950	752
482.sphinx3	64	1832	681	**1832**	**681**	1832	681	64	1559	800	1604	778	**1560**	**799**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set transparent_hugepage=never as a boot parameter in /boot/grub/menu.lst
```

Continued on next page

Operating System Notes (Continued)

```
Set vm/nr_hugepages=57344 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/root/cpu2006/amd1104-rate-libs-revC/32:/root/cpu2006/amd1104-rate-libs-revC/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64

Binaries were compiled on a system with 2x AMD Opteron 6274 chips + 64GB Memory using RHEL 6.1
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64

Continued on next page

Base Portability Flags (Continued)

481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64 -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso

C++ benchmarks:
-march=bdver1 -Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on -HP:bd=2m:heap=2m -D__OPEN64_FAST_SET

Fortran benchmarks:
-march=bdver1 -Ofast -LNO:blocking=off -OPT:rsqrt=2 -OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso

Benchmarks using both Fortran and C:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso -LNO:blocking=off -OPT:rsqrt=2 -OPT:unroll_size=256

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -march=bdver1 -Ofast -CG:movnti=1 -CG:locs_best=on
-HP:bdt=2m:heap=2m -IPA:plimit=7000 -IPA:callee_limit=1200
-OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso

470.lbm: basepeak = yes

482.sphinx3: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -IPA:plimit=1000
-OPT:malloc_alg=2 -CG:cmp_peep=on -CG:local_sched_alg=2
-CG:p2align=0 -INLINE:aggressive=on -LNO:prefetch=2
-LNO:prefetch_ahead=4 -mso

C++ benchmarks:

444.namd: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -IPA:plimit=3000
-LNO:ignore_feedback=off -CG:local_sched_alg=2
-CG:load_exe=0 -OPT:unroll_size=256 -fno-exceptions
-HP:bdt=2m:heap=2m

447.dealII: -march=bdver1 -Ofast -D__OPEN64_FAST_SET -static
-INLINE:aggressive=on -LNO:opt=0 -LNO:simd=0
-fno-emit-exceptions -m32 -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m
-GRA:unspill=on -CG:cmp_peep=on -CG:movext_icmp=off
-TENV:frame_pointer=off

450.soplex: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:ignore_feedback=off
-INLINE:aggressive=on -OPT:RO=1 -OPT:IEEE_arith=3
-OPT:IEEE_NaN_Inf=off -OPT:fold_unsigned_relops=on
-fno-exceptions -CG:p2align=0 -m32 -HP:bdt=2m:heap=2m

Continued on next page

Peak Optimization Flags (Continued)

450.soplex (continued):
-WOPT:sib=on

453.povray: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -CG:pre_local_sched=off
-CG:p2align=0 -CG:p2align_split=on -CG:dsched=on
-INLINE:aggressive=on -HP:bd=2m:heap=2m -OPT:transform=2
-OPT:alias=disjoint -WOPT:aggcm=0

Fortran benchmarks:

410.bwaves: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:ignore_feedback=off -LNO:fu=4
-LNO:loop_model_simd=on -LNO:simd_rm_unity_remainder=on
-WOPT:aggstr=0 -HP:bdt=2m:heap=2m -CG:cmp_peep=on

416.gamess: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:simd=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-OPT:unroll_times_max=2 -CG:local_sched_alg=1
-HP:bdt=2m:heap=2m -WOPT:sib=on

434.zeusmp: -march=bdver1 -Ofast -LNO:blocking=off -LNO:interchange=off
-IPA:plimit=1500 -HP:bdt=2m:heap=2m

437.leslie3d: -march=bdver1 -Ofast -CG:pre_minreg_level=2 -LNO:simd=0
-LNO:fusion=2 -HP:bdt=2m:heap=2m -mso

459.GemsFDTD: -march=bdver1 -Ofast -IPA:plimit=1500 -OPT:unroll_size=0
-LNO:fission=2 -CG:load_exe=0 -CG:local_sched_alg=2 -HP

465.tonto: -march=bdver1 -Ofast -OPT:alias=no_f90_pointer_alias
-LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525
-HP:bdt=2m:heap=2m

Benchmarks using both Fortran and C:

435.gromacs: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:rsqrt=2
-HP:bdt=2m:heap=2m -CG:local_sched_alg=2 -GRA:unspill=ON
-CG:load_exe=3 -LNO:simd=3

436.cactusADM: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:blocking=off
-LNO:prefetch=2 -HP -CG:locs_shallow_depth=1 -CG:load_exe=0
-CG:dsched=on -WOPT:sib=on

454.calculix: -march=bdver1 -Ofast -OPT:unroll_size=256
-GRA:optimize_boundary=on -CG:dsched=on -HP:bdt=2m:heap=2m

Continued on next page

Peak Optimization Flags (Continued)

481.wrf: `-march=bdver1 -Ofast -LNO:blocking=off -LANG:copyinout=off -IPA:callee_limit=5000 -GRA:prioritize_by_density=on -CG:load_exe=1 -HP -WOPT:sib=on`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/amd-platform-speed-revA-I.html`
`http://www.spec.org/cpu2006/flags/x86-open64-451-flags-rate-revC.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/amd-platform-speed-revA-I.xml`
`http://www.spec.org/cpu2006/flags/x86-open64-451-flags-rate-revC.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 11:25:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 31 July 2012.