SPEC® CFP2006 Result

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Huawei

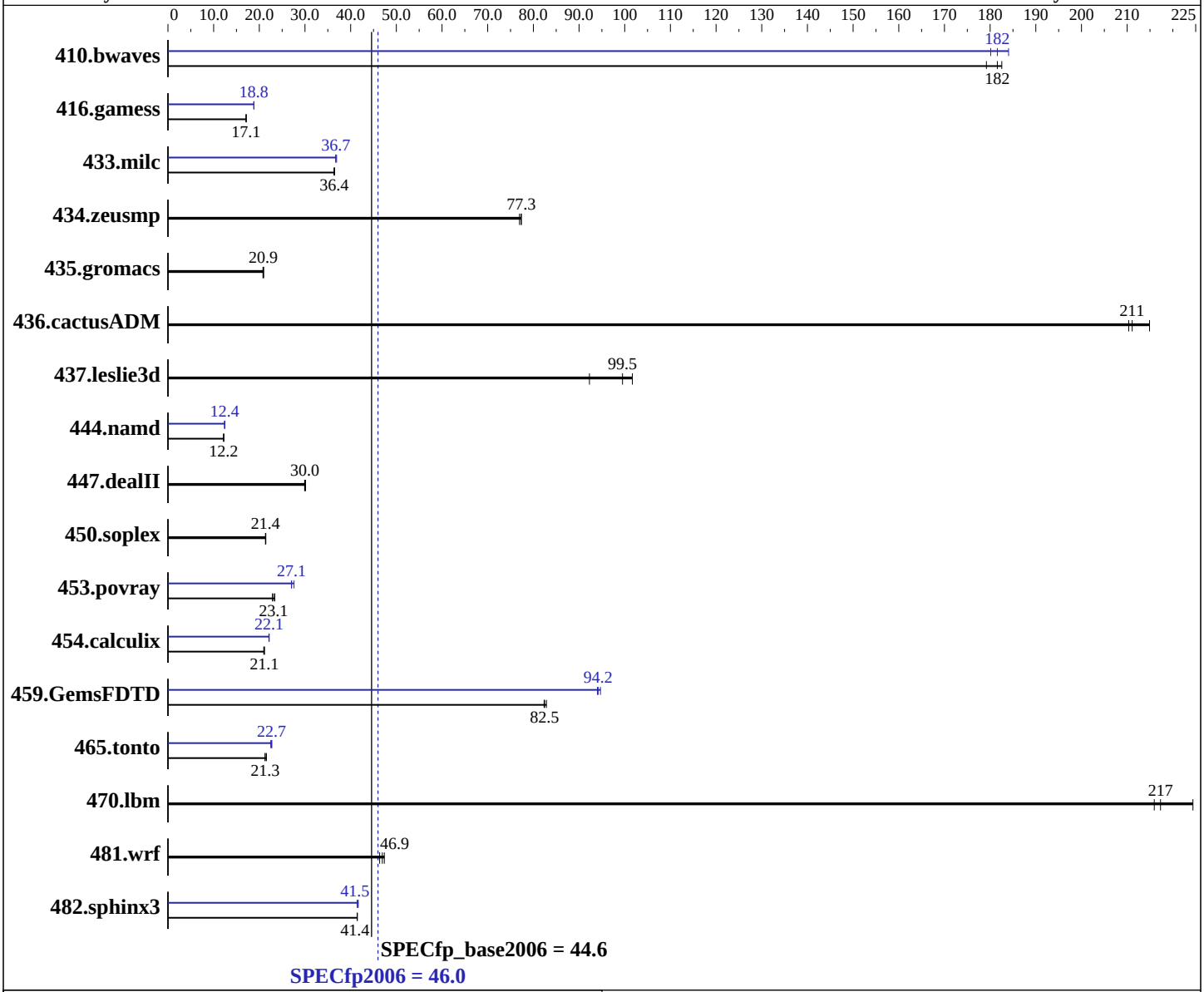
Huawei BH621 V2 (Intel Xeon E5-2403)

SPECfp®2006 = 46.0

SPECfp_base2006 = 44.6

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Oct-2012
Hardware Availability: May-2012
Software Availability: Dec-2011



Hardware

CPU Name:	Intel Xeon E5-2403
CPU Characteristics:	
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 1Rx4 PC3-10600R-11, ECC, running at 1067 MHz and CL7)
Disk Subsystem: 1 x 300 GB SAS, 10K RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**74.8**	**182**	74.4	183	75.8	179	73.8	184	**74.8**	**182**	75.4	180
416.gamess	1142	17.1	1145	17.1	**1145**	**17.1**	1041	18.8	**1041**	**18.8**	1040	18.8
433.milc	252	36.4	252	36.5	**252**	**36.4**	249	36.9	**250**	**36.7**	250	36.7
434.zeusmp	118	77.3	118	77.0	**118**	**77.3**	118	77.3	118	77.0	**118**	**77.3**
435.gromacs	**342**	**20.9**	341	21.0	343	20.8	**342**	**20.9**	341	21.0	343	20.8
436.cactusADM	56.8	210	**56.6**	**211**	55.6	215	56.8	210	**56.6**	**211**	55.6	215
437.leslie3d	102	92.3	**94.5**	**99.5**	92.5	102	102	92.3	**94.5**	**99.5**	92.5	102
444.namd	657	12.2	**657**	**12.2**	657	12.2	646	12.4	646	12.4	**646**	**12.4**
447.dealII	**382**	**30.0**	380	30.1	382	29.9	**382**	**30.0**	380	30.1	382	29.9
450.soplex	**390**	**21.4**	390	21.4	389	21.4	**390**	**21.4**	390	21.4	389	21.4
453.povray	**230**	**23.1**	227	23.4	233	22.9	193	27.6	**197**	**27.1**	197	27.1
454.calculix	**391**	**21.1**	391	21.1	393	21.0	373	22.1	372	22.1	**373**	**22.1**
459.GemsFDTD	129	82.3	**129**	**82.5**	128	82.9	**113**	**94.2**	113	94.0	112	94.7
465.tonto	456	21.6	**462**	**21.3**	463	21.3	437	22.5	433	22.7	**434**	**22.7**
470.lbm	**63.2**	**217**	63.6	216	61.2	224	**63.2**	**217**	63.6	216	61.2	224
481.wrf	**238**	**46.9**	241	46.3	236	47.4	**238**	**46.9**	241	46.3	236	47.4
482.sphinx3	470	41.4	470	41.4	**470**	**41.4**	471	41.4	468	41.7	**469**	**41.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Select only test related files when installing the operating system
```

Platform Notes

```
BIOS configuration:
Intel Hyper-Threading set to Disabled
Set Power Efficiency Mode to Performance
Sysinfo program /spec/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on RH62-yjp2 Mon Oct 15 08:48:03 2012
```

Continued on next page

Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2403 0 @ 1.80GHz
      2 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 0 1 2 3
      physical 1: cores 0 1 2 3
   cache size : 10240 KB

From /proc/meminfo
   MemTotal:       49403792 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux RH62-yjp2 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 15 08:41

SPEC is set to: /spec
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/sda1      ext3    270G   44G  212G  18% /

Additional information from dmidecode:
  Memory:
   12x Samsu M393B 4 GB 1333 MHz 1 rank

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "8"
```

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General Notes (Continued)

```
Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory
using RHEL 6.1
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Continued on next page

Base Optimization Flags (Continued)

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel`

C++ benchmarks:

444.namd: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.xml

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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 13:05:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 November 2012.