SPEC® CINT2006 Result

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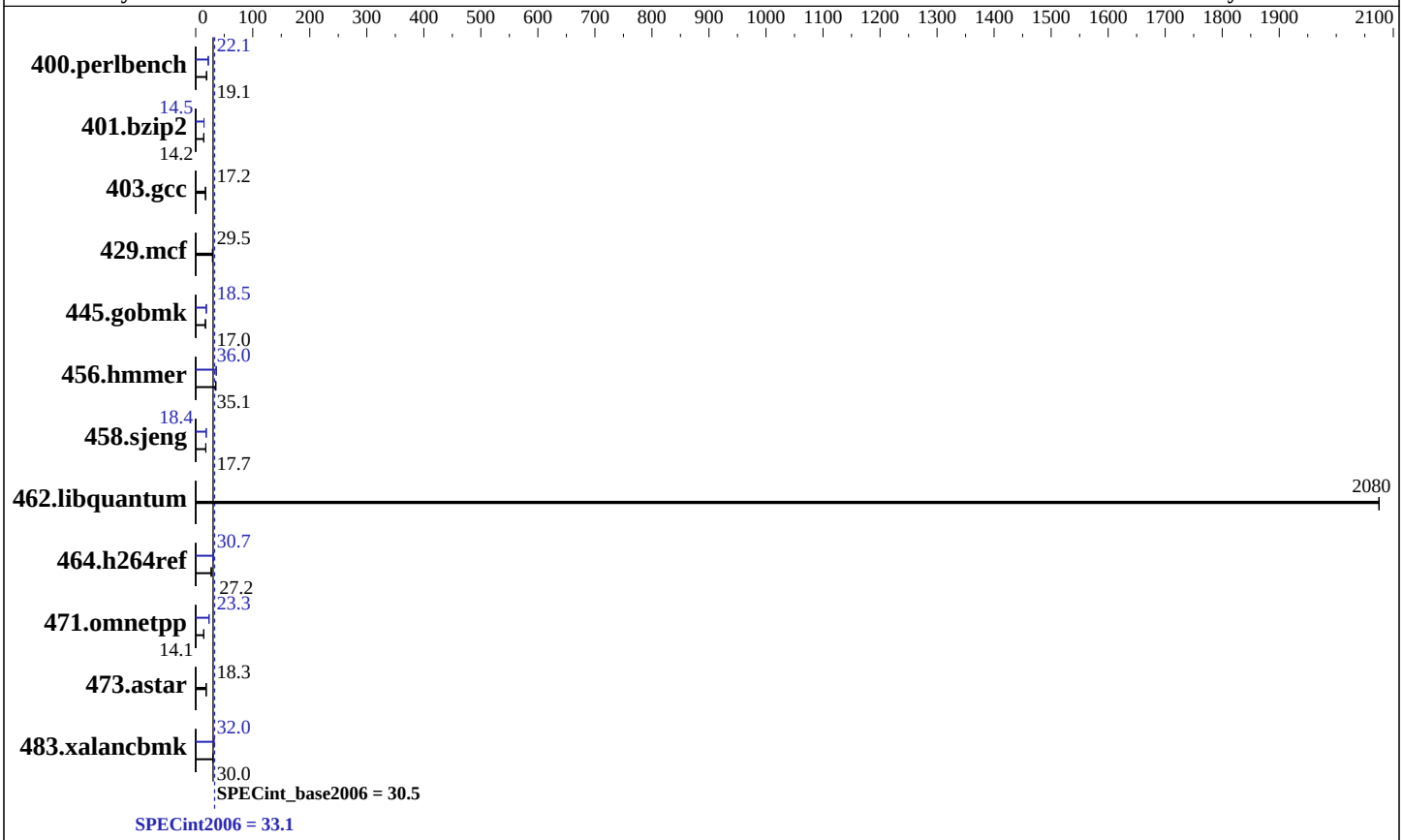
Huawei
Tecal RH5885 V2

SPECint®2006 = 33.1

SPECint_base2006 = 30.5

CPU2006 license: 13
Test sponsor: Huawei
Tested by: Huawei

Test date: Oct-2012
Hardware Availability: Oct-2012
Software Availability: Oct-2012



Hardware

CPU Name:	Intel Xeon E7-4830
CPU Characteristics:	Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (64 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem:	100 GB SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 13.0.0.079 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (add definition here)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	511	19.1	**512**	**19.1**	512	19.1	**443**	**22.1**	443	22.1	442	22.1
401.bzip2	680	14.2	680	14.2	**680**	**14.2**	665	14.5	**664**	**14.5**	664	14.5
403.gcc	**468**	**17.2**	468	17.2	468	17.2	**468**	**17.2**	468	17.2	468	17.2
429.mcf	**309**	**29.5**	307	29.7	310	29.4	**309**	**29.5**	307	29.7	310	29.4
445.gobmk	617	17.0	**617**	**17.0**	617	17.0	567	18.5	**567**	**18.5**	567	18.5
456.hmmer	265	35.2	266	35.1	**265**	**35.1**	**259**	**36.0**	259	36.0	260	35.9
458.sjeng	684	17.7	**684**	**17.7**	684	17.7	659	18.4	659	18.4	**659**	**18.4**
462.libquantum	9.98	2080	9.99	2070	**9.99**	**2080**	9.98	2080	9.99	2070	**9.99**	**2080**
464.h264ref	812	27.2	**812**	**27.2**	813	27.2	722	30.6	**722**	**30.7**	721	30.7
471.omnetpp	440	14.2	**443**	**14.1**	444	14.1	269	23.2	268	23.3	**269**	**23.3**
473.astar	**383**	**18.3**	381	18.4	386	18.2	**383**	**18.3**	381	18.4	386	18.2
483.xalancbmk	**230**	**30.0**	230	30.0	230	30.0	**216**	**32.0**	216	31.9	215	32.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Intel Hyper-Threading set to Disabled
Sysinfo program /home/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on Huawei-RH5885 Wed Oct 10 17:18:22 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7- 4830 @ 2.13GHz
      4 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 8
      physical 0: cores 0 1 2 8 17 18 24 25
      physical 1: cores 0 1 2 8 17 18 24 25
      physical 2: cores 0 1 2 8 17 18 24 25
      physical 3: cores 0 1 2 8 17 18 24 25
```

Continued on next page

Platform Notes (Continued)

```
      cache size : 24576 KB

   From /proc/meminfo
      MemTotal:       529114324 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      Red Hat Enterprise Linux Server release 6.2 (Santiago)

   From /etc/*release* /etc/*version*
      redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
      system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

   uname -a:
      Linux Huawei-RH5885 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Oct 10 16:50

   SPEC is set to: /home/cpu2006
      Filesystem     Type    Size  Used Avail Use% Mounted on
      /dev/mapper/vg_huaweirh5885-lv_home
                     ext4     80G   20G   56G  27% /home

   Additional information from dmidecode:
     Memory:
      32x Samsung M393B1K70CH0-CH9 8 GB 1067 MHz 2 rank
      32x Samsung M393B1K70DH0-YH9 8 GB 1067 MHz 2 rank

   (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,compact,1,0"
 LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
 OMP_NUM_THREADS = "32"

  Binaries compiled on a system with 4xE7-4807 CPU + 512GB
  memory using RHEL6.2
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/home/cpu2006/smartheap -lsmartheap64
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m64
```

Continued on next page

Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2) `-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/home/cpu2006/smartheap -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/home/cpu2006/smartheap -lsmartheap`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`

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