SPEC® CFP2006 Result

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Supermicro

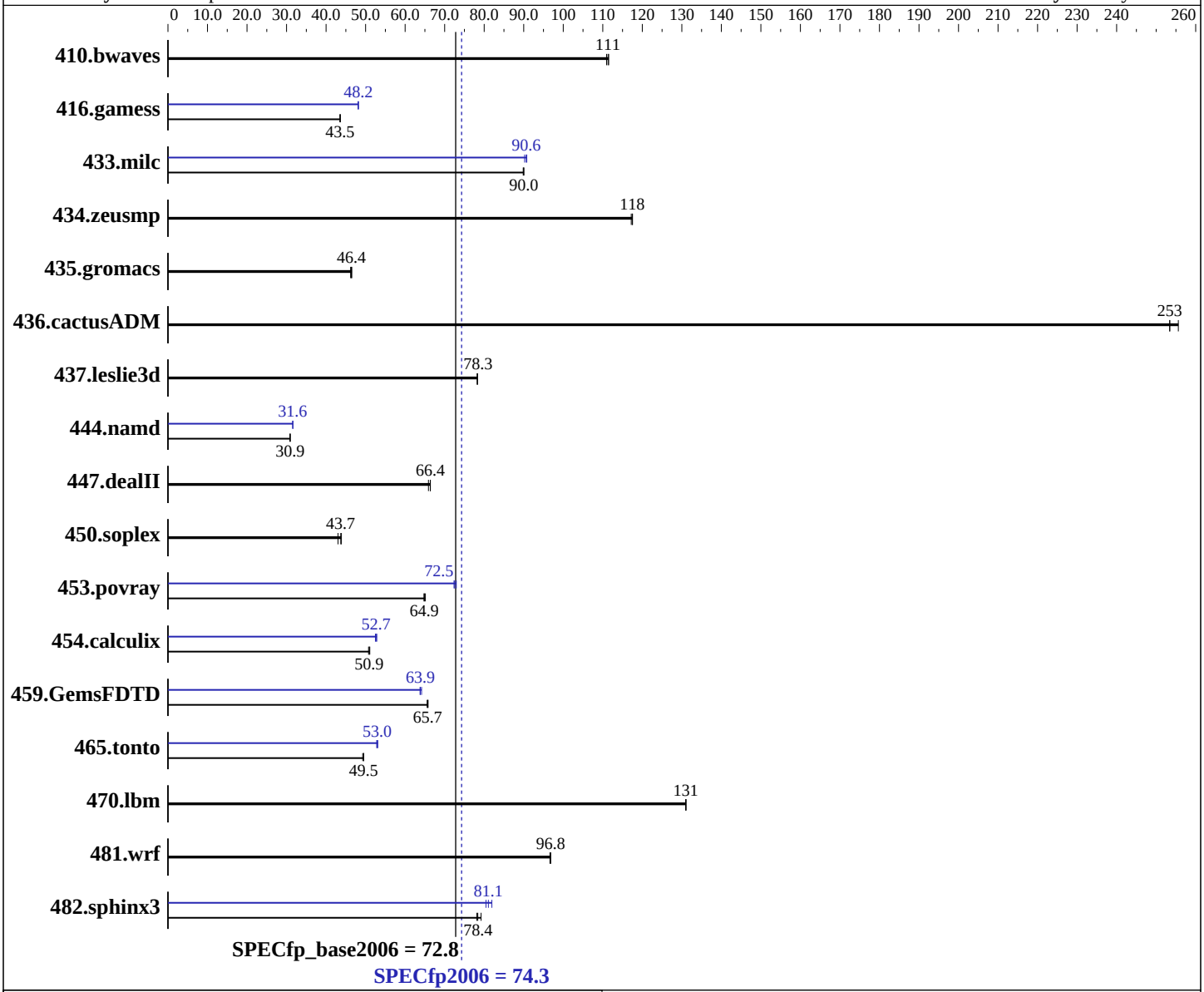
SuperWorkstation 5038A-iL (X10SAE, Intel Core i7-4770, 3.40 GHz)

SPECfp®2006 = 74.3

SPECfp_base2006 = 72.8

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2013
Hardware Availability: Jun-2013
Software Availability: May-2013



Hardware

CPU Name:	Intel Core i7-4770
CPU Characteristics:	Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.4, Kernel 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 13.1.1.163 of Intel C++ Studio XE for Linux; Fortran: Version 13.1.1.163 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB 2Rx4 PC3-12800U-11)
Disk Subsystem: 1 x 1000 GB SATA III, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	122	112	122	111	**122**	**111**	122	112	122	111	**122**	**111**
416.gamess	450	43.5	**450**	**43.5**	449	43.6	407	48.1	406	48.2	**407**	**48.2**
433.milc	102	90.0	**102**	**90.0**	102	89.9	101	90.8	**101**	**90.6**	102	90.2
434.zeusmp	**77.4**	**118**	77.6	117	77.4	118	**77.4**	**118**	77.6	117	77.4	118
435.gromacs	155	46.2	154	46.5	**154**	**46.4**	155	46.2	154	46.5	**154**	**46.4**
436.cactusADM	46.8	256	**47.2**	**253**	47.2	253	46.8	256	**47.2**	**253**	47.2	253
437.leslie3d	**120**	**78.3**	120	78.3	120	78.3	**120**	**78.3**	120	78.3	120	78.3
444.namd	260	30.9	259	30.9	**259**	**30.9**	**254**	**31.6**	254	31.6	254	31.6
447.dealII	174	65.9	**172**	**66.4**	172	66.4	174	65.9	**172**	**66.4**	172	66.4
450.soplex	190	43.9	194	43.0	**191**	**43.7**	190	43.9	194	43.0	**191**	**43.7**
453.povray	81.7	65.1	**82.0**	**64.9**	82.1	64.8	73.5	72.4	**73.3**	**72.5**	72.9	72.9
454.calculix	162	51.1	163	50.7	**162**	**50.9**	156	52.8	**157**	**52.7**	157	52.5
459.GemsFDTD	161	65.8	162	65.6	**161**	**65.7**	**166**	**63.9**	165	64.2	166	63.8
465.tonto	200	49.3	199	49.5	**199**	**49.5**	186	52.8	**186**	**53.0**	185	53.1
470.lbm	105	131	105	131	**105**	**131**	105	131	105	131	**105**	**131**
481.wrf	115	96.8	116	96.6	**115**	**96.8**	115	96.8	116	96.6	**115**	**96.8**
482.sphinx3	249	78.1	246	79.2	**249**	**78.4**	238	81.9	**240**	**81.1**	242	80.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "4"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html
http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml
http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.xml

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For other inquiries, please contact webmaster@spec.org.

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