SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

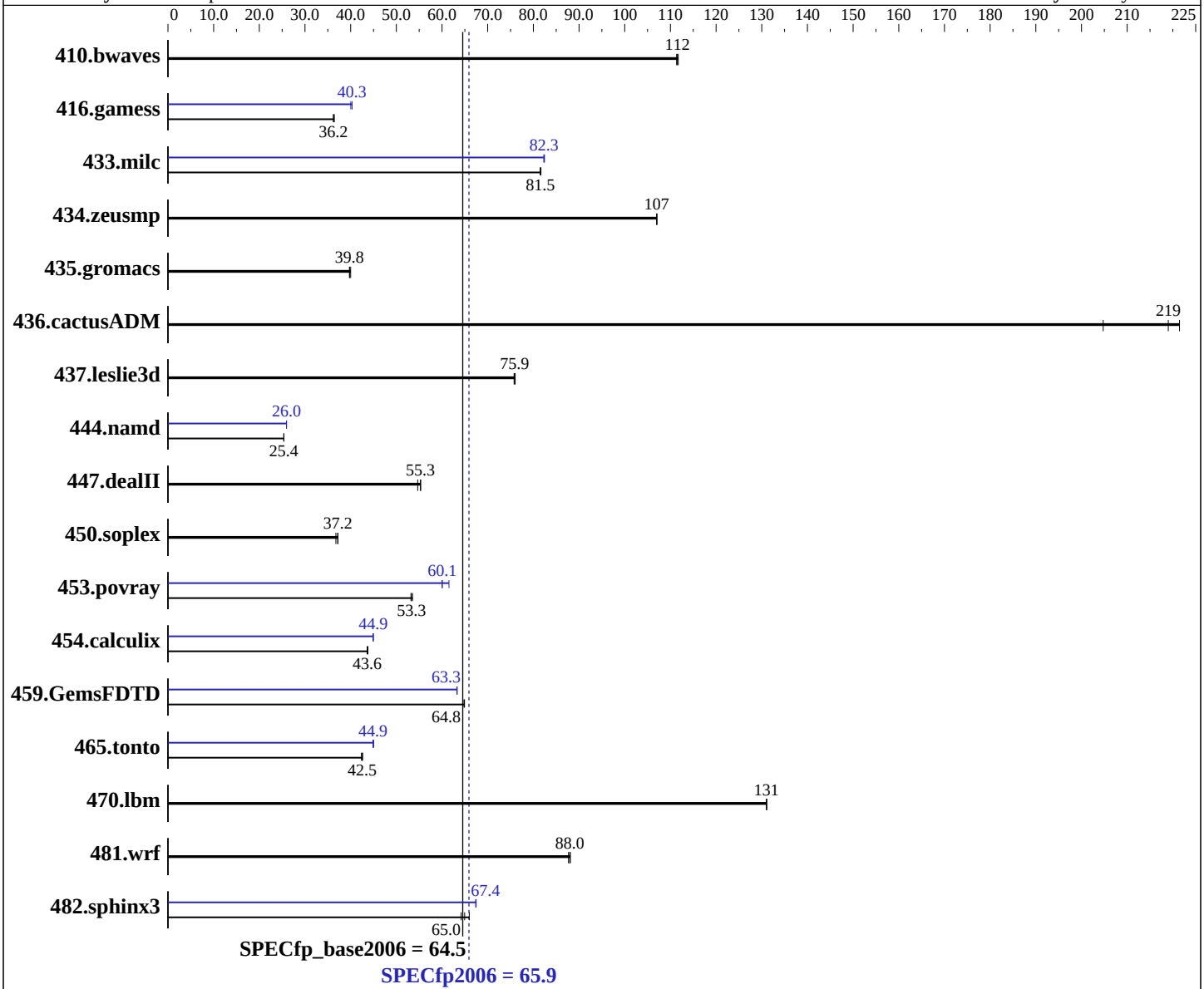
SuperWorkstation 5038A-iL (X10SAE, Intel Core i5-4430, 3.00 GHz)

SPECfp®2006 = 65.9

SPECfp_base2006 = 64.5

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2013
Hardware Availability: Jun-2013
Software Availability: May-2013



Hardware

CPU Name:	Intel Core i5-4430
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4, Kernel 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 13.1.1.163 of Intel C++ Studio XE for Linux; Fortran: Version 13.1.1.163 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperWorkstation 5038A-iL (X10SAE, Intel Core i5-4430, 3.00 GHz)

SPECfp2006 = 65.9

SPECfp_base2006 = 64.5

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2013
Hardware Availability: Jun-2013
Software Availability: May-2013

L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB 2Rx4 PC3-12800U-11)
Disk Subsystem: 1 x 1000 GB SATA II, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**122**	**112**	122	112	122	111	**122**	**112**	122	112	122	111
416.gamess	540	36.2	538	36.4	**540**	**36.2**	489	40.0	486	40.3	**486**	**40.3**
433.milc	112	81.7	**113**	**81.5**	113	81.5	112	82.3	**111**	**82.3**	111	82.4
434.zeusmp	**85.0**	**107**	85.0	107	85.0	107	**85.0**	**107**	85.0	107	85.0	107
435.gromacs	179	40.0	**180**	**39.8**	180	39.7	179	40.0	**180**	**39.8**	180	39.7
436.cactusADM	54.0	221	58.4	205	**54.6**	**219**	54.0	221	58.4	205	**54.6**	**219**
437.leslie3d	**124**	**75.9**	124	76.0	124	75.8	**124**	**75.9**	124	76.0	124	75.8
444.namd	316	25.4	**316**	**25.4**	316	25.4	309	26.0	**309**	**26.0**	309	25.9
447.dealII	209	54.7	207	55.3	**207**	**55.3**	209	54.7	207	55.3	**207**	**55.3**
450.soplex	224	37.2	**224**	**37.2**	227	36.8	224	37.2	**224**	**37.2**	227	36.8
453.povray	**99.8**	**53.3**	100	53.2	99.3	53.5	88.7	60.0	**88.6**	**60.1**	86.5	61.5
454.calculix	**189**	**43.6**	189	43.6	188	43.8	184	44.9	**184**	**44.9**	183	45.0
459.GemsFDTD	164	64.8	163	64.9	**164**	**64.8**	168	63.3	**168**	**63.3**	168	63.3
465.tonto	232	42.4	**231**	**42.5**	231	42.6	219	45.0	219	44.9	**219**	**44.9**
470.lbm	105	131	105	131	**105**	**131**	105	131	105	131	**105**	**131**
481.wrf	127	87.7	**127**	**88.0**	127	88.1	127	87.7	**127**	**88.0**	127	88.1
482.sphinx3	295	66.0	304	64.2	**300**	**65.0**	289	67.4	**289**	**67.4**	289	67.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "4"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html
http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml
http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 16:38:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 July 2013.