SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

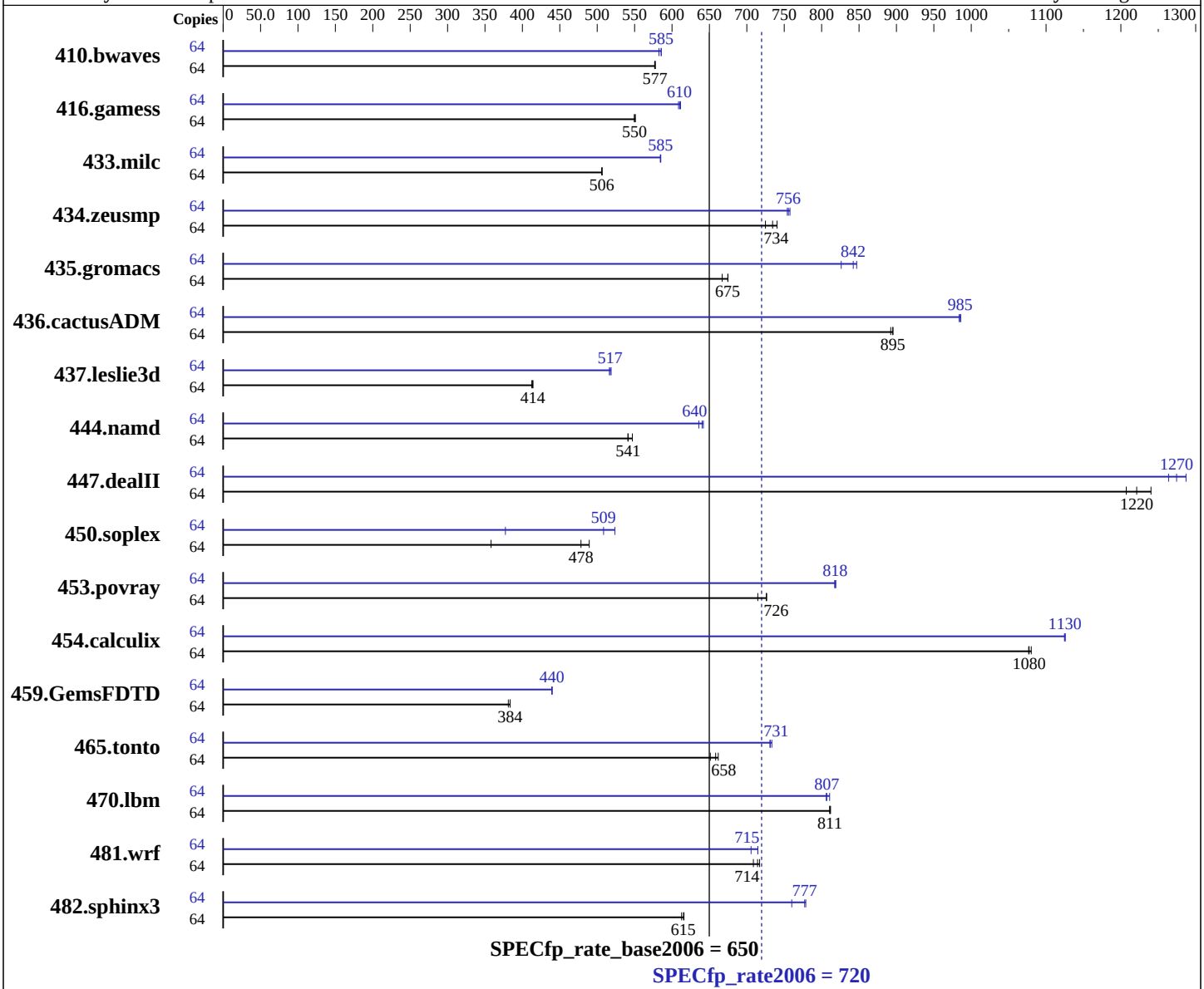
Supermicro A+ Server 1042G-LTF, (H8QGL-6F, Opteron 6370P)
AMD Opteron 6370P

SPECfp®_rate2006 = 720

SPECfp_rate_base2006 = 650

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Oct-2013
Hardware Availability: Jan-2014
Software Availability: Aug-2012



Hardware

CPU Name:	AMD Opteron 6370P
CPU Characteristics:	AMD Turbo CORE technology up to 2.50 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	64 cores, 4 chips, 16 cores/chip
CPU(s) orderable:	2,4 chips

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4, Kernel 2.6.32-358.el6.x86_64
Compiler:	C/C++/Fortran: Version 4.5.2 of x86 Open64 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Supermicro A+ Server 1042G-LTF, (H8QGL-6F, Opteron 6370P)
AMD Opteron 6370P

SPECfp_rate2006 = 720

SPECfp_rate_base2006 = 650

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Oct-2013
Hardware Availability: Jan-2014
Software Availability: Aug-2012

Primary Cache: 512 KB I on chip per chip, 64 KB I shared / 2 cores; 16 KB D on chip per core
Secondary Cache: 16 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 8 cores
Other Cache: None
Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	1506	578	**1507**	**577**	1508	577	64	1485	586	1493	583	**1486**	**585**
416.gamess	64	2274	551	2280	550	**2279**	**550**	64	**2054**	**610**	2059	609	2050	611
433.milc	64	1161	506	**1160**	**506**	1160	506	64	1005	584	**1005**	**585**	1004	585
434.zeusmp	64	787	740	803	725	**793**	**734**	64	769	758	**771**	**756**	772	754
435.gromacs	64	**677**	**675**	677	675	685	667	64	553	826	540	847	**543**	**842**
436.cactusADM	64	**854**	**895**	854	895	857	892	64	**776**	**985**	777	984	776	986
437.leslie3d	64	**1454**	**414**	1453	414	1458	413	64	**1163**	**517**	1165	516	1160	519
444.namd	64	**948**	**541**	938	547	949	541	64	807	636	**802**	**640**	800	642
447.dealII	64	**600**	**1220**	590	1240	607	1210	64	579	1260	**574**	**1270**	569	1290
450.soplex	64	1490	358	**1116**	**478**	1091	489	64	1415	377	**1050**	**509**	1019	524
453.povray	64	476	715	469	727	**469**	**726**	64	417	817	**416**	**818**	416	819
454.calculix	64	490	1080	489	1080	**490**	**1080**	64	**469**	**1130**	470	1120	469	1130
459.GemsFDTD	64	1781	381	1770	384	**1770**	**384**	64	**1544**	**440**	1544	440	1546	439
465.tonto	64	952	662	**957**	**658**	967	651	64	862	731	**861**	**731**	859	733
470.lbm	64	1083	812	**1084**	**811**	1085	811	64	1085	811	**1090**	**807**	1091	806
481.wrf	64	1009	709	**1001**	**714**	997	717	64	1000	715	1013	706	**1000**	**715**
482.sphinx3	64	2025	616	**2027**	**615**	2035	613	64	1641	760	**1605**	**777**	1602	779

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set transparent_hugepage=never as a boot parameter in /boot/grub/menu.lst
```

Continued on next page

Operating System Notes (Continued)

```
Set vm/nr_hugepages=57344 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

Platform Notes

```
C State mode set to disabled under Processor & Clock Options in BIOS.
HPC Mode set to enabled under Processor & Clock Options in BIOS.
C1E Support set to disabled under Processor & Clock Options in BIOS.
Power Down Enable set to disabled in Memory Configuration in BIOS.
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/home/spec/amd1206-rate-libs-revA/32:/home/spec/amd1206-rate-libs-revA/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64

Binaries were compiled on a system with 2x AMD Opteron 6386SE chips + 128GB Memory using RHEL 6.3
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64

Continued on next page

Base Portability Flags (Continued)

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64 -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso -march=bdver1

C++ benchmarks:
-Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on -HP:bd=2m:heap=2m -D__OPEN64_FAST_SET -march=bdver1

Fortran benchmarks:
-Ofast -LNO:blocking=off -LNO:simd_peel_align=on -OPT:rsqrt=2 -OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso -march=bdver1

Benchmarks using both Fortran and C:
-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso -march=bdver1 -LNO:blocking=off -LNO:simd_peel_align=on -OPT:rsqrt=2 -OPT:unroll_size=256

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64 -fno-second-underscore

Peak Optimization Flags

C benchmarks:

433.milc: -Ofast -CG:movnti=1 -CG:locs_best=on -HP:bdt=2m:heap=2m -IPA:plimit=7000 -IPA:callee_limit=1200 -OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso -march=bdver1

470.lbm: -Ofast -CG:cmp_peep=on -OPT:keep_ext=on -HP:bdt=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -march=bdver1 -mso

482.sphinx3: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast -m32 -IPA:plimit=1000 -OPT:malloc_alg=2 -CG:cmp_peep=on -CG:p2align=0 -CG:load_exe=1 -CG:dsched=on -INLINE:aggressive=on -LNO:prefetch=2 -LNO:prefetch_ahead=4 -mso -march=bdver2

C++ benchmarks:

444.namd: -Ofast -IPA:plimit=3000 -LNO:ignore_feedback=off -CG:local_sched_alg=0 -CG:load_exe=0 -OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m -LNO:if_select_conv=1 -OPT:alias=disjoint -LNO:psimd_iso_unroll=ON -march=bdver1

447.dealII: -Ofast -D__OPEN64_FAST_SET -static -INLINE:aggressive=on -LNO:opt=1 -LNO:simd=2 -fno-emit-exceptions -m32 -OPT:unroll_times_max=8 -OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on -CG:cmp_peep=on -CG:movext_icmp=off -TENV:frame_pointer=off -march=bdver1

Continued on next page

Peak Optimization Flags (Continued)

450.soplex: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-LNO:ignore_feedback=off -INLINE:aggressive=on -OPT:RO=1
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -fno-exceptions -CG:p2align=0
-m32 -mno-fma4 -HP:bdt=2m:heap=2m -WOPT:sib=on
-march=bdver1

453.povray: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-CG:pre_local_sched=off -CG:p2align=0 -CG:p2align_split=on
-CG:dsched=on -INLINE:aggressive=on -HP:bd=2m:heap=2m
-OPT:transform=2 -OPT:alias=disjoint -WOPT:aggcm=0
-march=bdver2

Fortran benchmarks:

410.bwaves: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:Ofast -OPT:treeheight=on -LNO:blocking=off
-LNO:ignore_feedback=off -LNO:fu=4 -LNO:loop_model_simd=on
-LNO:simd_rm_unity_remainder=on -WOPT:aggstr=0
-HP:bdt=2m:heap=2m -CG:cmp_peep=on -march=bdver1

416.gamess: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:fu=6 -LNO:blocking=0 -LNO:simd=2 -OPT:ro=3
-OPT:recip=on -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m
-WOPT:sib=on -march=bdver1

434.zeusmp: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:blocking=off -LNO:interchange=off -IPA:plimit=1500
-HP:bdt=2m:heap=2m -march=bdver1

437.leslie3d: -Ofast -CG:pre_minreg_level=2 -LNO:simd=0 -LNO:fusion=2
-HP:bdt=2m:heap=2m -mso -march=bdver1

459.GemsFDTD: -Ofast -IPA:plimit=1500 -OPT:unroll_size=1024
-OPT:unroll_times_max=16 -LNO:fission=2
-CG:local_sched_alg=2 -HP -march=bdver1

465.tonto: -Ofast -OPT:alias=no_f90_pointer_alias -LNO:blocking=off
-CG:load_exe=1 -CG:local_sched_alg=3 -IPA:plimit=525
-HP:bdt=2m:heap=2m -march=bdver1

Benchmarks using both Fortran and C:

435.gromacs: -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m
-CG:local_sched_alg=2 -CG:load_exe=3 -GRA:unspill=on
-march=bdver1 -LNO:simd=3

436.cactusADM: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:blocking=off -LNO:prefetch=2 -LNO:pf2=0
-LNO:prefetch_ahead=4 -HP -CG:locs_shallow_depth=1
-CG:load_exe=0 -CG:dsched=on -WOPT:sib=on -march=bdver1

Continued on next page

Peak Optimization Flags (Continued)

454.calculix: `-Ofast -OPT:unroll_size=256 -OPT:alias=disjoint -GRA:optimize_boundary=on -CG:dsched=on -HP:bdt=2m:heap=2m -march=bdver1`

481.wrf: `-Ofast -LNO:blocking=off -LANG:copyinout=off -IPA:callee_limit=5000 -GRA:prioritize_by_density=on -HP -WOPT:sib=on -march=bdver1`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-III.html
http://www.spec.org/cpu2006/flags/amd-Supermicro-platform-rate-revC-I.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-III.xml
http://www.spec.org/cpu2006/flags/amd-Supermicro-platform-rate-revC-I.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 22:11:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 February 2014.