SPEC® CINT2006 Result

Dell Inc.

PowerEdge R920 (Intel Xeon E7-4830 v2, 2.20 GHz)

SPECint_rate2006 = NC
SPECint_rate_base2006 = NC

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2014
Hardware Availability: Mar-2014
Software Availability: Mar-2014

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Hardware

| Copies |  
|--------|---|
| 400.perlbench |  
| 401.bzip2 |  
| 403.gcc |  
| 429.mcf |  
| 445.gobmk |  
| 456.hmmer |  
| 458.sjeng |  
| 462.libquantum |  
| 464.h264ref |  
| 471.omnetpp |  
| 473.astar |  
| 483 |  

Operating System:
SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default
Compiler:
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 32-bit

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/

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Results Table

<table>
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<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
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</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.
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Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
BIOS settings:
Virtualization Technology disabled
Execute Disable disabled
System Profile set to Custom
Memory Patrol Scrub set to disabled
Sysinfo program
/root/Desktop/Performance/ic14.0_Oct17_2013/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on slesperf1 Sun Feb 16 11:04:18 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4830 v2 @ 2.20GHz
  4 "physical id"s (chips)
  20 "processor"s
core  s: siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    10 cores  : 10
      siblings : 20
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
      physical 1: cores 0 1 2 3 4 8 9 10 11 12
      physical 2: cores 0 1 2 3 4 8 9 10 11 12
      physical 3: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 20480 KB

From /proc/meminfo
MemTotal: 1058789108 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

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Platform Notes (Continued)

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

uname -a:
Linux slesperf1 3.0.76-0.11-default SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 15 11:14 last=S

SPEC is set to: /root/Desktop/Performance/ic14.0_Oct17_2013

Additional information from dmidecode:
BIOS Dell Inc. 1.4 01/27/2014
Memory:
22x 00CE00B300CE M393B2G70BH0-YK0 16 GB 1333 MHz
10x 00CE00B300CE M393B2G70CB0-YK0 16 GB 1333 MHz
22x 00CE04B300CE M393B2G70CB0-YK0 16 GB 1333 MHz

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General Notes (Continued)

```
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
- icc  -m32

C++ benchmarks:
- icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
- x386_sse2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
- x386_sse2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
- Wl, -muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

Continued on next page
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Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc -m32
  400.perlbench: icc -m64
  401.bzip2: icc -m64
  456.hmmer: icc -m64
  458.sjeng: icc -m64

C++ benchmarks:
  icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64 -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page

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Peak Optimization Flags (Continued)

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -iop (pass 2)
   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
   -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
   -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes
429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
   -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
   -unroll2 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
   -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
   -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
   -L/sh -lsmartheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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