# SPEC® CFP2006 Result

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## Hewlett-Packard Company

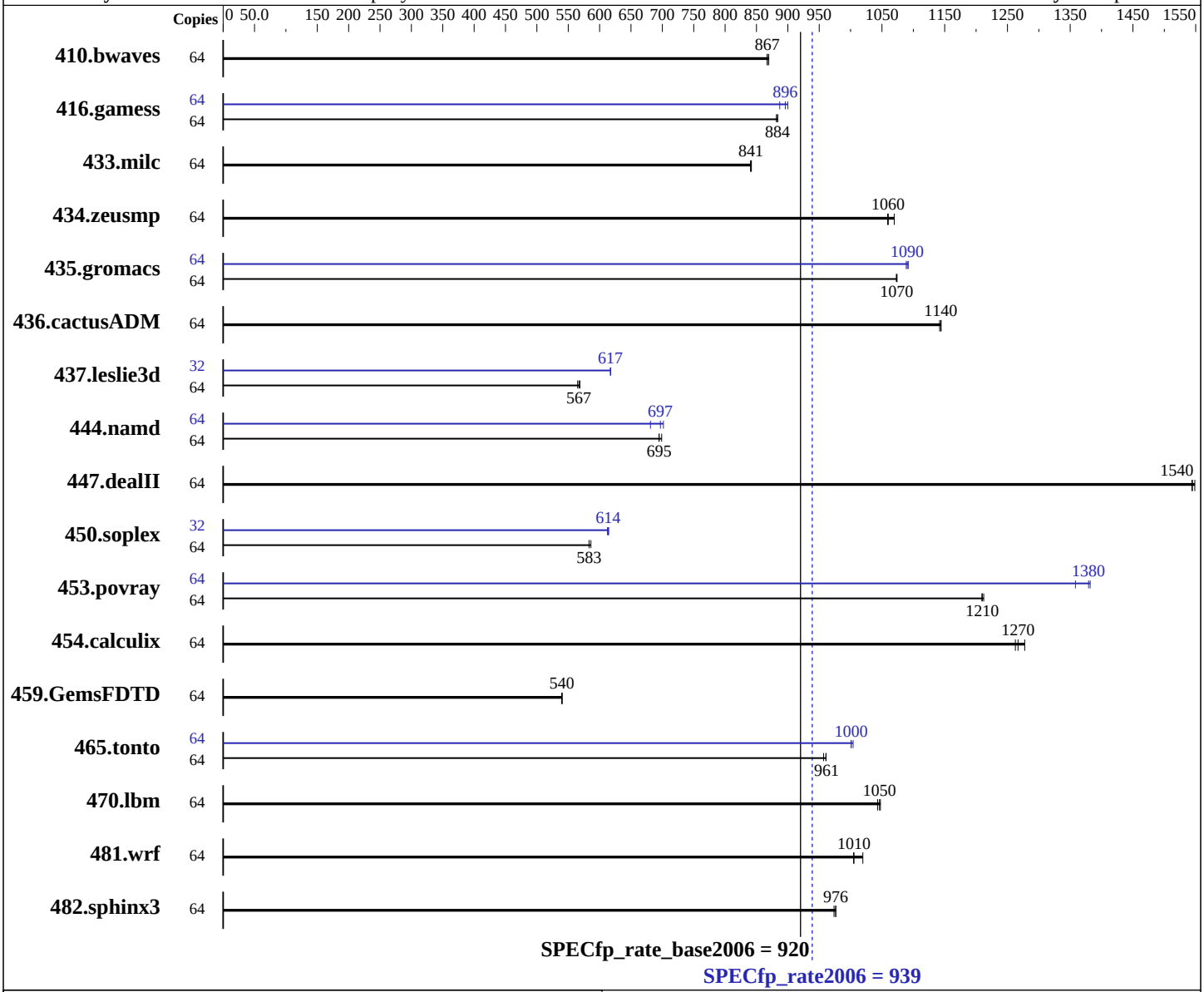
ProLiant DL560 Gen8
(2.30 GHz, Intel Xeon E5-4610 v2)

SPECfp®_rate2006 = 939

SPECfp_rate_base2006 = 920

**CPU2006 license:** 3
**Test sponsor:** Hewlett-Packard Company
**Tested by:** Hewlett-Packard Company

**Test date:** Feb-2014
**Hardware Availability:** Mar-2014
**Software Availability:** Sep-2013




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-4610 v2 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.70 GHz |
| CPU MHz: | 2300 |
| FPU: | Integrated |
| CPU(s) enabled: | 32 cores, 4 chips, 8 cores/chip, 2 threads/core |
| CPU(s) orderable: | 2,4 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.4, (Santiago)<br>Kernel 2.6.32-358.el6.x86_64 |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;<br>Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |

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L3 Cache: 16 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (32 x 8 GB 2Rx4 PC3-14900R-13, ECC, running at 1600 MHz and CL11)
Disk Subsystem: 1 x 900 GB 10 K SAS, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 64 | 1000 | 870 | **1003** | **867** | 1003 | 867 | 64 | 1000 | 870 | **1003** | **867** | 1003 | 867 |
| 416.gamess | 64 | 1418 | 884 | **1418** | **884** | 1421 | 882 | 64 | **1399** | **896** | 1392 | 900 | 1413 | 887 |
| 433.milc | 64 | 698 | 842 | **698** | **841** | 699 | 841 | 64 | 698 | 842 | **698** | **841** | 699 | 841 |
| 434.zeusmp | 64 | 550 | 1060 | **549** | **1060** | 545 | 1070 | 64 | 550 | 1060 | **549** | **1060** | 545 | 1070 |
| 435.gromacs | 64 | **426** | **1070** | 425 | 1070 | 426 | 1070 | 64 | **419** | **1090** | 418 | 1090 | 420 | 1090 |
| 436.cactusADM | 64 | **669** | **1140** | 668 | 1140 | 670 | 1140 | 64 | **669** | **1140** | 668 | 1140 | 670 | 1140 |
| 437.leslie3d | 64 | 1058 | 569 | **1061** | **567** | 1065 | 565 | 32 | 487 | 618 | 487 | 617 | **487** | **617** |
| 444.namd | 64 | 734 | 699 | 739 | 695 | **739** | **695** | 64 | 731 | 702 | 754 | 681 | **737** | **697** |
| 447.dealII | 64 | 474 | 1540 | **474** | **1540** | 473 | 1550 | 64 | 474 | 1540 | **474** | **1540** | 473 | 1550 |
| 450.soplex | 64 | 911 | 586 | 915 | 583 | **915** | **583** | 32 | 434 | 614 | **435** | **614** | 436 | 613 |
| 453.povray | 64 | 282 | 1210 | 281 | 1210 | **281** | **1210** | 64 | 251 | 1360 | 246 | 1380 | **247** | **1380** |
| 454.calculix | 64 | 413 | 1280 | **417** | **1270** | 418 | 1260 | 64 | 413 | 1280 | **417** | **1270** | 418 | 1260 |
| 459.GemsFDTD | 64 | 1259 | 539 | **1257** | **540** | 1257 | 540 | 64 | 1259 | 539 | **1257** | **540** | 1257 | 540 |
| 465.tonto | 64 | 655 | 961 | **656** | **961** | 658 | 957 | 64 | 629 | 1000 | **629** | **1000** | 627 | 1000 |
| 470.lbm | 64 | 839 | 1050 | **841** | **1050** | 843 | 1040 | 64 | 839 | 1050 | **841** | **1050** | 843 | 1040 |
| 481.wrf | 64 | 701 | 1020 | **711** | **1010** | 711 | 1000 | 64 | 701 | 1020 | **711** | **1010** | 711 | 1000 |
| 482.sphinx3 | 64 | **1278** | **976** | 1281 | 974 | 1278 | 976 | 64 | **1278** | **976** | 1281 | 974 | 1278 | 976 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

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## Operating System Notes (Continued)

```
Disabled unused Linux services through "stop_services.sh" before running.
```

## Platform Notes

```
BIOS Configuration:
 HP Power Profile set to Maximum Performance
 Memory Power Savings Mode set to Maximum Performance
 Collaborative Power Control set to Disabled
 Dynamic Power Capping Functionality set to Disabled
 Thermal Configuration set to Maximum Cooling
 Processor Power and Utilization Monitoring set to Disabled
 Memory Refresh Rate set to 1x

Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on DL560-Gen8 Wed Feb 12 06:05:10 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4610 v2 @ 2.30GHz
      4 "physical id"s (chips)
      64 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
      physical 2: cores 0 1 2 3 4 5 6 7
      physical 3: cores 0 1 2 3 4 5 6 7
   cache size : 16384 KB

From /proc/meminfo
   MemTotal:       264633628 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux DL560-Gen8 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
```

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## Platform Notes (Continued)

```
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Feb 11 16:21

   SPEC is set to: /cpu2006
      Filesystem     Type    Size  Used Avail Use% Mounted on
      /dev/sda3      ext4    824G   68G  714G   9% /

   Additional information from dmidecode:
     BIOS HP P77 12/20/2013
     Memory:
      32x HP 712382-071 8 GB 1600 MHz 2 rank
      16x UNKNOWN NOT AVAILABLE

   (End of data from sysinfo program)
   Regarding the sysinfo display about the memory installed, the correct amount of
   memory is 256 GB and the dmidecode description should have one line reading as:
      32x HP 712382-071 8 GB 1600 MHz 2 rank
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64

Continued on next page

## Base Portability Flags (Continued)

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Continued on next page

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Continued on next page

# Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml

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