# SPEC® CFP2006 Result

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## IBM Corporation

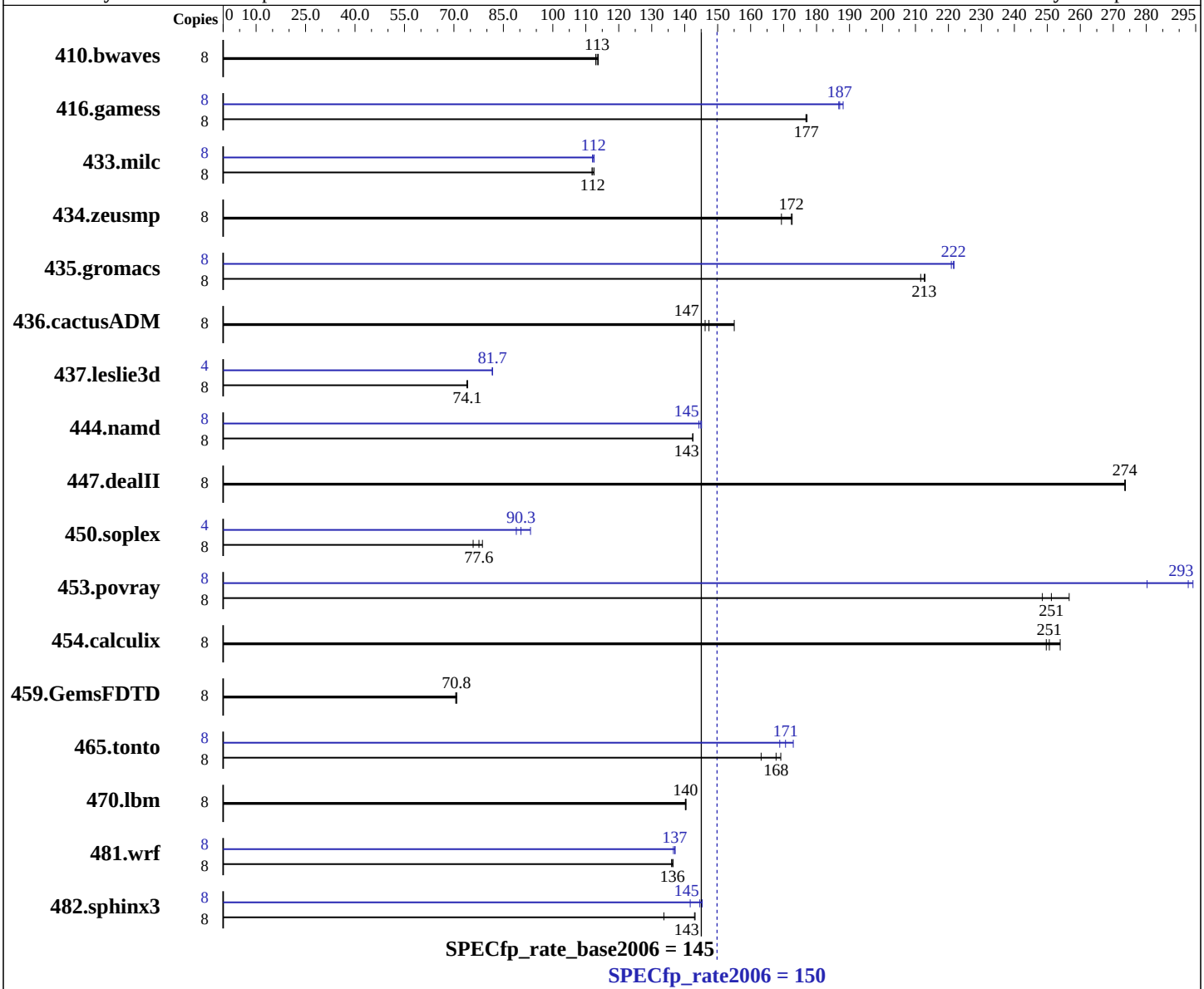
IBM System x3250 M5
(Intel Xeon E3-1230 v3, 3.30 GHz)

SPECfp®_rate2006 = 150

SPECfp_rate_base2006 = 145

**CPU2006 license:** 11
**Test sponsor:** IBM Corporation
**Tested by:** IBM Corporation

**Test date:** Mar-2014
**Hardware Availability:** Dec-2013
**Software Availability:** Sep-2013




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E3-1230 v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.70 GHz |
| CPU MHz: | 3300 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.4 (Santiago)<br>2.6.32-358.el6.x86_64 |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;<br>Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |

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| | |
|---|---|
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC) |
| Disk Subsystem: | 1 x 1 TB SATA, 7200 RPM |
| Other Hardware: | None |

| | |
|---|---|
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | None |

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 8 | 962 | 113 | **958** | **113** | 956 | 114 | 8 | 962 | 113 | **958** | **113** | 956 | 114 |
| 416.gamess | 8 | 886 | 177 | **886** | **177** | 884 | 177 | 8 | **838** | **187** | 833 | 188 | 839 | 187 |
| 433.milc | 8 | **655** | **112** | 656 | 112 | 653 | 113 | 8 | **654** | **112** | 653 | 113 | 656 | 112 |
| 434.zeusmp | 8 | 430 | 169 | 422 | 173 | **422** | **172** | 8 | 430 | 169 | 422 | 173 | **422** | **172** |
| 435.gromacs | 8 | 270 | 212 | 268 | 213 | **269** | **213** | 8 | 258 | 222 | **258** | **222** | 259 | 221 |
| 436.cactusADM | 8 | **649** | **147** | 617 | 155 | 654 | 146 | 8 | **649** | **147** | 617 | 155 | 654 | 146 |
| 437.leslie3d | 8 | 1017 | 73.9 | 1014 | 74.1 | **1015** | **74.1** | 4 | **460** | **81.7** | 460 | 81.7 | 460 | 81.7 |
| 444.namd | 8 | 450 | 143 | **450** | **143** | 451 | 142 | 8 | 442 | 145 | 445 | 144 | **443** | **145** |
| 447.dealII | 8 | 335 | 274 | **335** | **274** | 334 | 274 | 8 | 335 | 274 | **335** | **274** | 334 | 274 |
| 450.soplex | 8 | 880 | 75.8 | 848 | 78.6 | **860** | **77.6** | 4 | 375 | 88.9 | 358 | 93.2 | **369** | **90.3** |
| 453.povray | 8 | 166 | 257 | 171 | 248 | **169** | **251** | 8 | 152 | 280 | 145 | 294 | **145** | **293** |
| 454.calculix | 8 | 264 | 250 | **263** | **251** | 260 | 254 | 8 | 264 | 250 | **263** | **251** | 260 | 254 |
| 459.GemsFDTD | 8 | 1203 | 70.5 | **1200** | **70.8** | 1198 | 70.8 | 8 | 1203 | 70.5 | **1200** | **70.8** | 1198 | 70.8 |
| 465.tonto | 8 | 482 | 163 | **469** | **168** | 465 | 169 | 8 | **461** | **171** | 466 | 169 | 455 | 173 |
| 470.lbm | 8 | **784** | **140** | 784 | 140 | 783 | 140 | 8 | **784** | **140** | 784 | 140 | 783 | 140 |
| 481.wrf | 8 | 657 | 136 | **656** | **136** | 655 | 136 | 8 | 652 | 137 | **652** | **137** | 654 | 137 |
| 482.sphinx3 | 8 | **1091** | **143** | 1166 | 134 | 1089 | 143 | 8 | **1078** | **145** | 1101 | 142 | 1073 | 145 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS setting:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818
```

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## Platform Notes (Continued)

```
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on x3250M5 Thu Mar 13 01:13:53 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1230 v3 @ 3.30GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:       16299396 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux x3250M5 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 12 15:58

SPEC is set to: /home/SPECcpu-new
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/mapper/vg_x3250m5-lv_home
                  ext4    852G   21G  788G   3% /home

Additional information from dmidecode:
  BIOS IBM -[JUE109OUS-1.00]- 11/20/2013
  Memory:
   2x 0000    1600 MHz
   2x Micron 18KSF1G72AZ-1G6E1 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
"2x 0000    1600 MHz" memory information from dmidecode indicates unused DIMM slots.
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

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## Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

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## Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3 -unroll2

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

Continued on next page

## Peak Optimization Flags (Continued)

437.leslie3d: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3`

Benchmarks using both Fortran and C:

435.gromacs: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-opt-mem-layout-trans=3`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.xml


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