



# SPEC® CINT2006 Result

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## Sugon

**SPECint®2006 = 58.4**

Sugon I620-G20 (Intel Xeon E5-2670 v3)

**SPECint\_base2006 = 55.0**

CPU2006 license: 9046

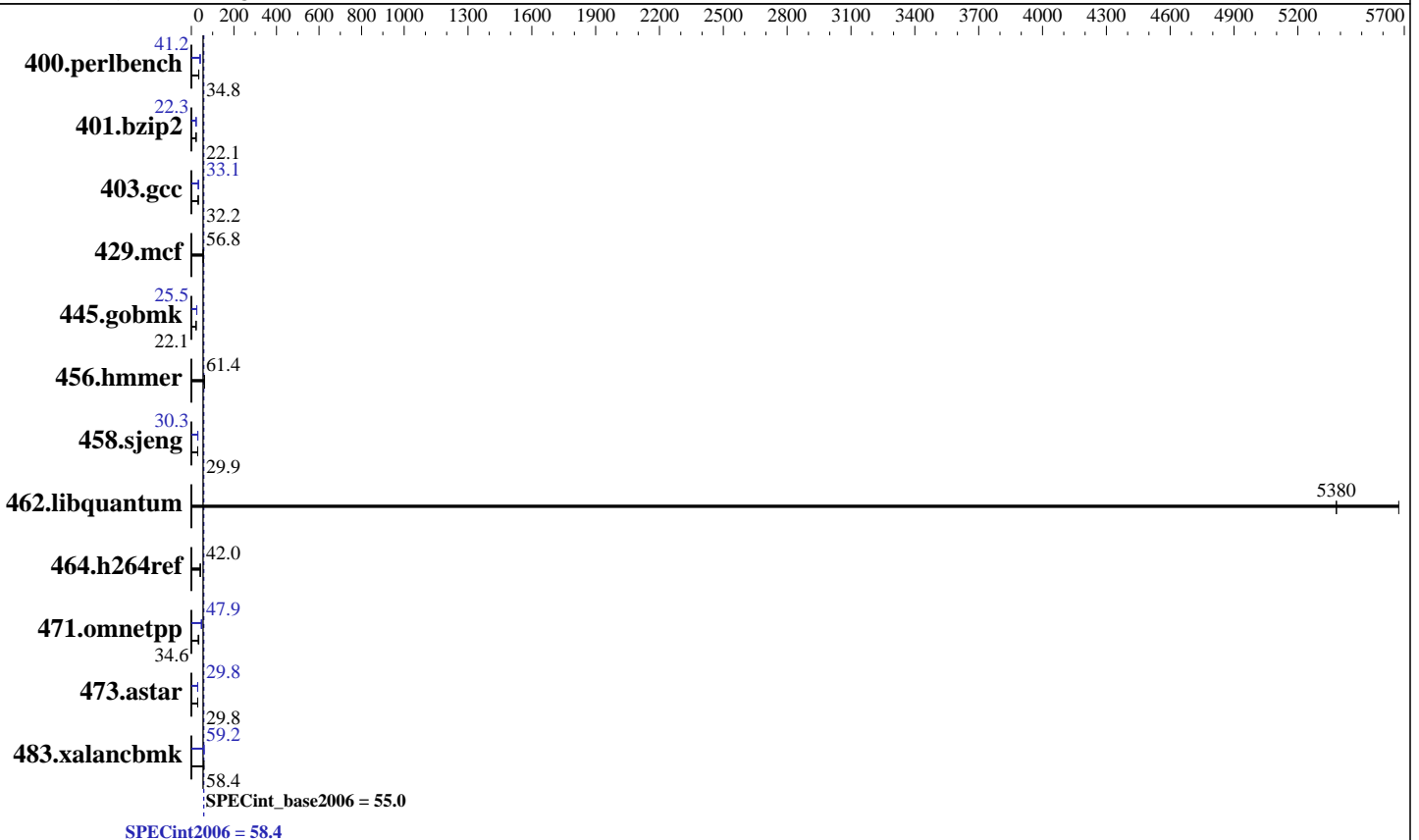
Test date: Oct-2014

Test sponsor: Sugon

Hardware Availability: Sep-2014

Tested by: Sugon

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2670 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 2.0 TB SATA 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	281	34.7	281	34.8	<b><u>281</u></b>	<b><u>34.8</u></b>	237	41.2	<b><u>237</u></b>	<b><u>41.2</u></b>	237	41.2
401.bzip2	436	22.1	436	22.1	<b><u>436</u></b>	<b><u>22.1</u></b>	433	22.3	433	22.3	<b><u>433</u></b>	<b><u>22.3</u></b>
403.gcc	250	32.2	251	32.1	<b><u>250</u></b>	<b><u>32.2</u></b>	246	32.7	<b><u>243</u></b>	<b><u>33.1</u></b>	243	33.2
429.mcf	166	55.0	161	56.8	<b><u>161</u></b>	<b><u>56.8</u></b>	166	55.0	161	56.8	<b><u>161</u></b>	<b><u>56.8</u></b>
445.gobmk	474	22.1	474	22.2	<b><u>474</u></b>	<b><u>22.1</u></b>	412	25.5	<b><u>411</u></b>	<b><u>25.5</u></b>	411	25.5
456.hammer	151	61.7	153	61.1	<b><u>152</u></b>	<b><u>61.4</u></b>	151	61.7	153	61.1	<b><u>152</u></b>	<b><u>61.4</u></b>
458.sjeng	405	29.9	405	29.8	<b><u>405</u></b>	<b><u>29.9</u></b>	399	30.3	400	30.3	<b><u>399</u></b>	<b><u>30.3</u></b>
462.libquantum	3.65	5670	3.85	5380	<b><u>3.85</u></b>	<b><u>5380</u></b>	3.65	5670	3.85	5380	<b><u>3.85</u></b>	<b><u>5380</u></b>
464.h264ref	526	42.1	<b><u>526</u></b>	<b><u>42.0</u></b>	529	41.8	526	42.1	<b><u>526</u></b>	<b><u>42.0</u></b>	529	41.8
471.omnetpp	197	31.7	179	35.0	<b><u>181</u></b>	<b><u>34.6</u></b>	<b><u>131</u></b>	<b><u>47.9</u></b>	133	46.9	131	47.9
473.astar	234	30.0	236	29.7	<b><u>236</u></b>	<b><u>29.8</u></b>	236	29.7	<b><u>236</u></b>	<b><u>29.8</u></b>	235	29.9
483.xalancbmk	<b><u>118</u></b>	<b><u>58.4</u></b>	118	58.6	118	58.3	<b><u>117</u></b>	<b><u>59.2</u></b>	117	59.2	116	59.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:

Enforce POR set to disabled

DDR Speed set to 2133

Early Snoop set to disabled

COD set to disable

Power Technology set to performance

Sysinfo program /home/cpu2006/config/sysinfo.rev6874

\$Rev: 6874 \$ \$Date:: 2013-11-20 #\$ 654bd3fcf53b06faef0efe54ed011998

running on localhost Thu Oct 30 09:45:15 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2670 v3 @ 2.30GHz

2 "physical id"s (chips)

48 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The

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<http://www.spec.org/>



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## Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 12
siblings  : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

From /proc/meminfo

```
MemTotal:      264477476 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 30 09:43

SPEC is set to: /home/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        ext4  1.8T  614G  1.1T  36% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 068 08/15/2014

Memory:

```
16x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz
8x NO DIMM NO DIMM
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "24"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

Submitted\_by: Tian Yuwan <tianyw@sugon.com>  
Submitted: Tue Nov 4 06:04:33 EST 2014  
Submission: cpu2006-20141104-32752.sub

Submitted\_by: Tian Yuwan <tianyw@sugon.com>  
Submitted: Sun Nov 23 21:26:15 EST 2014  
Submission: cpu2006-20141104-32752.sub

Submitted\_by: Tian Yuwan <tianyw@sugon.com>  
Submitted: Wed Nov 26 05:11:52 EST 2014  
Submission: cpu2006-20141104-32752.sub

Submitted\_by: Tian Yuwan <tianyw@sugon.com>  
Submitted: Thu Nov 27 00:57:34 EST 2014  
Submission: cpu2006-20141104-32752.sub

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzp2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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