SPEC® CFP2006 Result

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Huawei

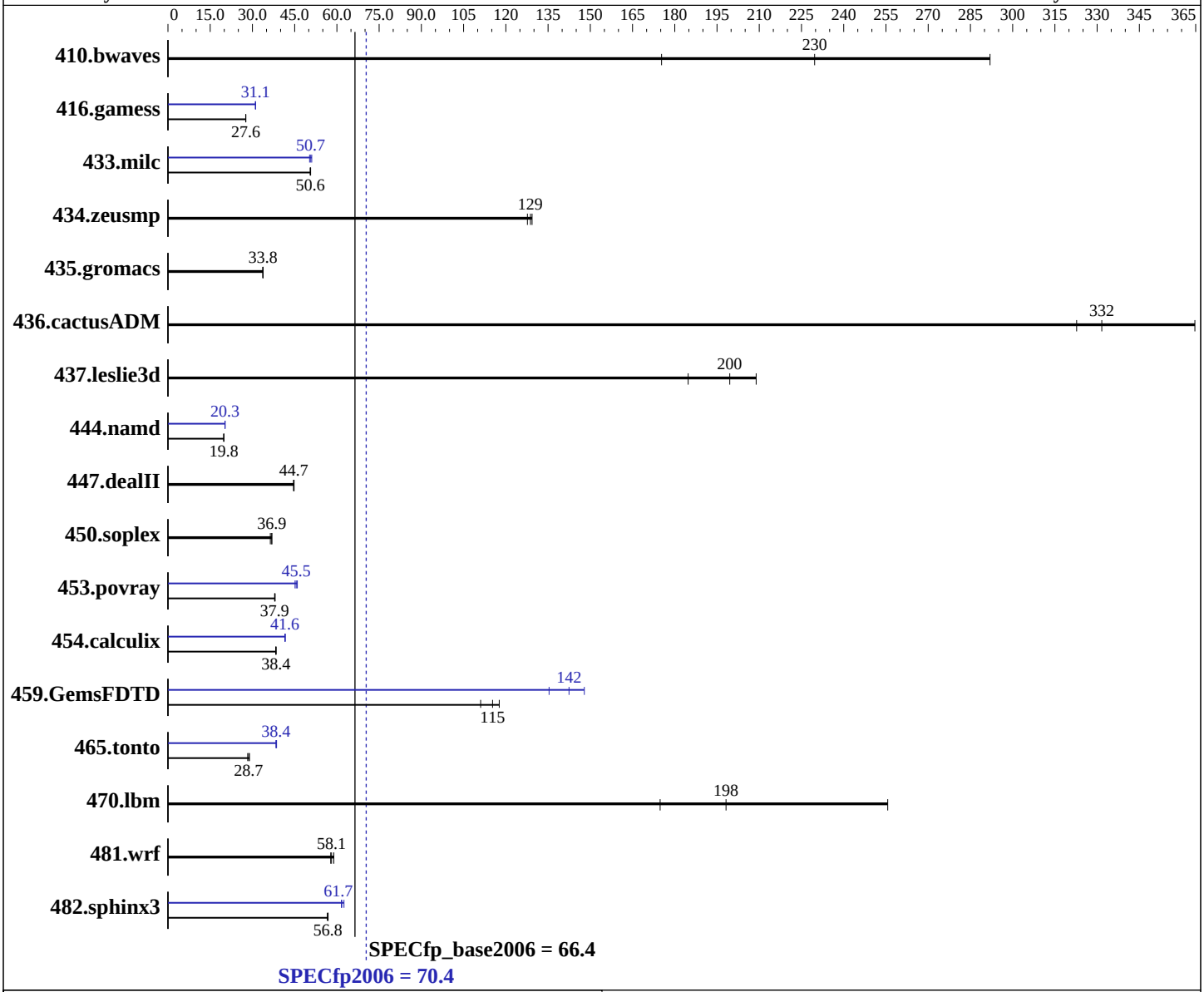
Huawei RH5885H V3 (Intel Xeon E7-4850 v2)

SPECfp®2006 = 70.4

SPECfp_base2006 = 66.4

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Oct-2014
Hardware Availability: Feb-2014
Software Availability: Jun-2014



Hardware

CPU Name:	Intel Xeon E7-4850 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	48 cores, 4 chips, 12 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz and CL7)
Disk Subsystem: 2 x 300 GB SAS, 10K RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	77.5	175	46.5	292	**59.2**	**230**	77.5	175	46.5	292	**59.2**	**230**
416.gamess	**708**	**27.6**	708	27.7	709	27.6	629	31.1	**630**	**31.1**	630	31.1
433.milc	181	50.7	**181**	**50.6**	182	50.5	**181**	**50.7**	183	50.3	180	51.1
434.zeusmp	71.3	128	**70.7**	**129**	70.4	129	71.3	128	**70.7**	**129**	70.4	129
435.gromacs	**211**	**33.8**	212	33.6	211	33.8	**211**	**33.8**	212	33.6	211	33.8
436.cactusADM	32.8	365	37.0	323	**36.0**	**332**	32.8	365	37.0	323	**36.0**	**332**
437.leslie3d	45.0	209	50.9	185	**47.1**	**200**	45.0	209	50.9	185	**47.1**	**200**
444.namd	404	19.9	405	19.8	**404**	**19.8**	**396**	**20.3**	396	20.2	395	20.3
447.dealII	**256**	**44.7**	255	44.8	257	44.6	**256**	**44.7**	255	44.8	257	44.6
450.soplex	**226**	**36.9**	226	36.9	229	36.4	**226**	**36.9**	226	36.9	229	36.4
453.povray	**140**	**37.9**	140	38.1	140	37.9	118	45.1	**117**	**45.5**	116	45.9
454.calculix	**215**	**38.4**	215	38.4	216	38.3	**198**	**41.6**	199	41.5	198	41.7
459.GemsFDTD	**92.0**	**115**	90.2	118	95.5	111	**74.5**	**142**	78.4	135	71.8	148
465.tonto	348	28.3	**343**	**28.7**	340	29.0	**256**	**38.4**	255	38.6	256	38.4
470.lbm	53.7	256	78.6	175	**69.3**	**198**	53.7	256	78.6	175	**69.3**	**198**
481.wrf	190	58.9	**192**	**58.1**	193	57.8	190	58.9	**192**	**58.1**	193	57.8
482.sphinx3	343	56.9	344	56.6	**343**	**56.8**	316	61.6	**316**	**61.7**	312	62.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set Intel Hyper Threading to disabled
Sysinfo program /spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Tue Oct 14 08:44:25 2014
```

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-4850 v2 @ 2.30GHz
      4 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 24576 KB

From /proc/meminfo
   MemTotal:       1056475676 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
   EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 14 01:52

SPEC is set to: /spec
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda3      ext4  349G   12G  320G   4% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
```

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Platform Notes (Continued)

```
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS American Megatrends Inc. BLISV395 07/25/2014
    Memory:
     61x Hynix HMT42GR7AFR4C-PB 16 GB 2 rank 1600 MHz, configured at 1066 MHz
     3x Hynix HMT42GR7MFR4C-PB 16 GB 2 rank 1600 MHz, configured at 1066 MHz
     32x NO DIMM NO DIMM

  (End of data from sysinfo program)
  Regarding the sysinfo display about the memory installed, the correct amount of
  memory is 1 TB and the dmidecode description should have two lines reading as:
     61x Hynix HMT42GR7AFR4C-PB 16 GB 2 rank 1600 MHz, configured at 1066 MHz
     3x Hynix HMT42GR7MFR4C-PB 16 GB 2 rank 1600 MHz, configured at 1066 MHz
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"
OMP_NUM_THREADS = "48"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64

Continued on next page

Base Portability Flags (Continued)

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

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