



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp®\_rate2006 = 574

Lenovo System x3650 M5  
(Intel Xeon E5-2630 v3, 2.40 GHz)

SPECfp\_rate\_base2006 = 560

CPU2006 license: 9017

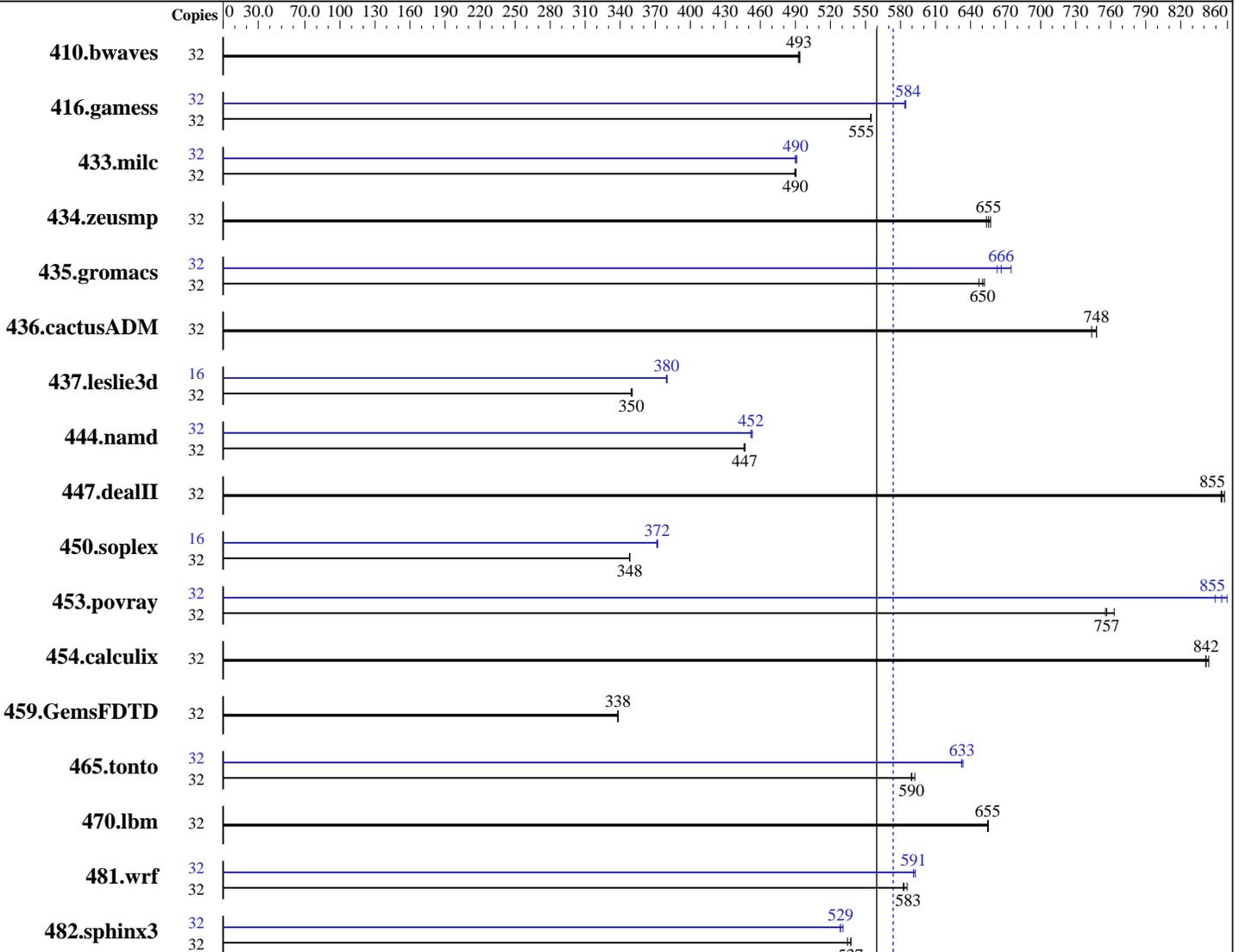
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Dec-2014

Hardware Availability: Oct-2014

Software Availability: Sep-2014



SPECfp\_rate\_base2006 = 560

SPECfp\_rate2006 = 574

### Hardware

CPU Name: Intel Xeon E5-2630 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp\_rate2006 = 574

Lenovo System x3650 M5  
(Intel Xeon E5-2630 v3, 2.40 GHz)

SPECfp\_rate\_base2006 = 560

CPU2006 license: 9017

Test date: Dec-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Oct-2014

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)  
Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	881	494	<b>882</b>	<b>493</b>	883	493	32	881	494	<b>882</b>	<b>493</b>	883	493
416.gamess	32	1129	555	1130	554	<b>1130</b>	<b>555</b>	32	<b>1073</b>	<b>584</b>	1072	585	1074	584
433.milc	32	<b>599</b>	<b>490</b>	599	491	600	489	32	598	491	<b>599</b>	<b>490</b>	600	490
434.zeusmp	32	<b>444</b>	<b>655</b>	443	657	446	654	32	<b>444</b>	<b>655</b>	443	657	446	654
435.gromacs	32	<b>351</b>	<b>650</b>	350	652	353	647	32	339	675	345	663	<b>343</b>	<b>666</b>
436.cactusADM	32	<b>511</b>	<b>748</b>	514	744	511	748	32	<b>511</b>	<b>748</b>	514	744	511	748
437.leslie3d	32	<b>860</b>	<b>350</b>	861	350	859	350	16	396	380	<b>396</b>	<b>380</b>	396	380
444.namd	32	574	447	575	446	<b>575</b>	<b>447</b>	32	568	452	566	453	<b>568</b>	<b>452</b>
447.dealII	32	427	857	428	855	<b>428</b>	<b>855</b>	32	427	857	428	855	<b>428</b>	<b>855</b>
450.soplex	32	766	348	<b>767</b>	<b>348</b>	767	348	16	<b>359</b>	<b>372</b>	359	372	359	371
453.povray	32	<b>225</b>	<b>757</b>	225	756	223	763	32	200	850	198	860	<b>199</b>	<b>855</b>
454.calculix	32	314	842	<b>314</b>	<b>842</b>	313	844	32	314	842	<b>314</b>	<b>842</b>	313	844
459.GemsFDTD	32	1004	338	1005	338	<b>1004</b>	<b>338</b>	32	1004	338	1005	338	<b>1004</b>	<b>338</b>
465.tonto	32	<b>534</b>	<b>590</b>	534	589	532	592	32	498	632	497	634	<b>498</b>	<b>633</b>
470.lbm	32	671	655	<b>672</b>	<b>655</b>	672	655	32	671	655	<b>672</b>	<b>655</b>	672	655
481.wrf	32	<b>613</b>	<b>583</b>	614	582	610	586	32	603	593	605	591	<b>605</b>	<b>591</b>
482.sphinx3	32	1166	535	1160	538	<b>1161</b>	<b>537</b>	32	1175	531	<b>1180</b>	<b>529</b>	1180	528

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp\_rate2006 = 574

Lenovo System x3650 M5  
(Intel Xeon E5-2630 v3, 2.40 GHz)

SPECfp\_rate\_base2006 = 560

CPU2006 license: 9017

Test date: Dec-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Oct-2014

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

### Platform Notes

BIOS setting:

Operating Mode set to "Efficiency-Favor Performance"

COD Preference set to Enable

Sysinfo program /home/SPEC/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Thu Dec 4 18:46:05 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2630 v3 @ 2.40GHz

2 "physical id"s (chips)

32 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 8

siblings : 16

physical 0: cores 0 1 2 3 4 5 6 7

physical 1: cores 0 1 2 3 4 5 6 7

cache size : 20480 KB

From /proc/meminfo

MemTotal: 263452696 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.0 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.0"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.0:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.0:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-123.el7.x86\_64 #1 SMP Mon May 5 11:16:57

EDT 2014 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Dec 4 18:42

SPEC is set to: /home/SPEC

Filesystem Type Size Used Avail Use% Mounted on

/dev/mapper/rhel-home xfs 877G 68G 809G 8% /home

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp\_rate2006 = 574**

Lenovo System x3650 M5  
(Intel Xeon E5-2630 v3, 2.40 GHz)

**SPECfp\_rate\_base2006 = 560**

**CPU2006 license:** 9017

**Test date:** Dec-2014

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Oct-2014

**Tested by:** Lenovo Group Limited

**Software Availability:** Sep-2014

## Platform Notes (Continued)

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TCE103EUS-1.01]- 10/21/2014

Memory:

16x Hynix 484D4134324752374D4652344E2D54465431 16 GB 2 rank 2133 MHz,  
configured at 1866 MHz  
8x NO DIMM Unknown

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/SPEC/libs/32:/home/SPEC/libs/64:/home/SPEC/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB  
memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECfp\_rate2006 = 574

Lenovo System x3650 M5  
(Intel Xeon E5-2630 v3, 2.40 GHz)

SPECfp\_rate\_base2006 = 560

CPU2006 license: 9017

Test date: Dec-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Oct-2014

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

## Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

```

Fortran benchmarks:

```

ifort -m64

```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp\_rate2006 = 574**

Lenovo System x3650 M5  
(Intel Xeon E5-2630 v3, 2.40 GHz)

**SPECfp\_rate\_base2006 = 560**

**CPU2006 license:** 9017

**Test date:** Dec-2014

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Oct-2014

**Tested by:** Lenovo Group Limited

**Software Availability:** Sep-2014

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -prof-gen(pass 1) -ipo -O3 -no-prec-div  
 -prof-use(pass 2) -unroll2

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-malloc-options=3

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp\_rate2006 = 574

Lenovo System x3650 M5  
(Intel Xeon E5-2630 v3, 2.40 GHz)

SPECfp\_rate\_base2006 = 560

CPU2006 license: 9017

Test date: Dec-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Oct-2014

Tested by: Lenovo Group Limited

Software Availability: Sep-2014

## Peak Optimization Flags (Continued)

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.xml>



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

Lenovo System x3650 M5  
(Intel Xeon E5-2630 v3, 2.40 GHz)

SPECfp\_rate2006 = 574

SPECfp\_rate\_base2006 = 560

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Group Limited

**Tested by:** Lenovo Group Limited

**Test date:** Dec-2014

**Hardware Availability:** Oct-2014

**Software Availability:** Sep-2014

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Tue Dec 30 16:13:43 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 30 December 2014.