



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant ML150 Gen9
(1.90 GHz, Intel Xeon E5-2609 v3)

SPECint®2006 = 35.1

SPECint_base2006 = 33.7

CPU2006 license: 3

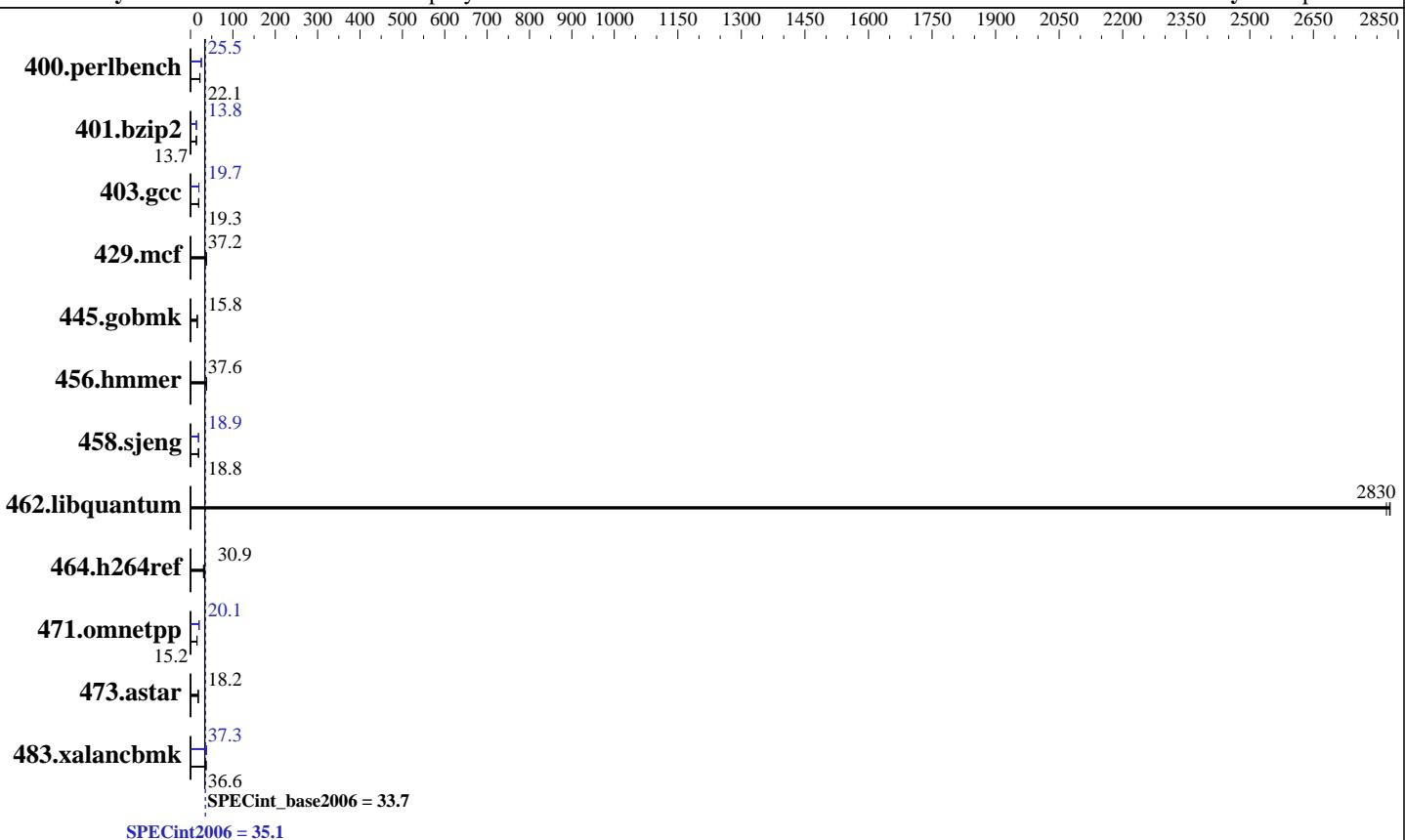
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Apr-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2609 v3
CPU Characteristics:
CPU MHz: 1900
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 1 x 400 SATA SSD, RAID 0
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: Kernel 3.10.0-123.el7.x86_64
Auto Parallel: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant ML150 Gen9
(1.90 GHz, Intel Xeon E5-2609 v3)

SPECint2006 = 35.1

SPECint_base2006 = 33.7

CPU2006 license: 3

Test date: Apr-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2014

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	442	22.1	442	22.1	442	22.1	384	25.5	384	25.5	384	25.5
401.bzip2	705	13.7	704	13.7	704	13.7	699	13.8	699	13.8	699	13.8
403.gcc	416	19.3	418	19.3	417	19.3	408	19.7	409	19.7	408	19.7
429.mcf	246	37.1	244	37.3	245	37.2	246	37.1	244	37.3	245	37.2
445.gobmk	665	15.8	664	15.8	664	15.8	665	15.8	664	15.8	664	15.8
456.hmmer	248	37.6	248	37.6	248	37.6	248	37.6	248	37.6	248	37.6
458.sjeng	643	18.8	643	18.8	643	18.8	641	18.9	641	18.9	641	18.9
462.libquantum	7.32	2830	7.34	2820	7.32	2830	7.32	2830	7.34	2820	7.32	2830
464.h264ref	715	30.9	715	30.9	717	30.8	715	30.9	715	30.9	717	30.8
471.omnetpp	412	15.2	412	15.2	412	15.2	311	20.1	311	20.1	310	20.2
473.astar	386	18.2	386	18.2	387	18.1	386	18.2	386	18.2	387	18.1
483.xalancbmk	189	36.6	188	36.7	189	36.4	184	37.4	187	36.8	185	37.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

BIOS Configuration:

Intel Hyperthreading Options set to Disabled

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core State set to C6 State

Minimum Processor Idle Power Package State set to No Package State

QPI Snoop Configuration set to Early Snoop

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Collaborative Power Control set to Disabled

Memory Double Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3ffb8667b5a285932ceab81e28219e1

running on ML150-Gen9 Fri Apr 17 00:31:50 2015

This section contains SUT (System Under Test) info as seen by
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant ML150 Gen9
(1.90 GHz, Intel Xeon E5-2609 v3)

SPECint2006 = 35.1

SPECint_base2006 = 33.7

CPU2006 license: 3

Test date: Apr-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2014

Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2609 v3 @ 1.90GHz
  2 "physical id"s (chips)
    12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      131604400 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux ML150-Gen9 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 16 23:58
```

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   369G   15G  355G   4% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P95 08/26/2014

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant ML150 Gen9
(1.90 GHz, Intel Xeon E5-2609 v3)

SPECint2006 = 35.1

SPECint_base2006 = 33.7

CPU2006 license: 3

Test date: Apr-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2014

Platform Notes (Continued)

Memory:

8x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz, configured at 1600 MHz
8x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 128 GB and the dmidecode description should have one line reading as:
8x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz, configured at 1600 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/cpu2006/lib32:32:/cpu2006/libs/64:/cpu2006/sh"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant ML150 Gen9
(1.90 GHz, Intel Xeon E5-2609 v3)

SPECint2006 = 35.1

SPECint_base2006 = 33.7

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Apr-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks (except as noted below):

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
403.gcc: -DSPEC_CPU_LP64  
429.mcf: -DSPEC_CPU_LP64  
445.gobmk: -DSPEC_CPU_LP64  
456.hmmr: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX  
464.h264ref: -DSPEC_CPU_LP64  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LINUX
```



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant ML150 Gen9
(1.90 GHz, Intel Xeon E5-2609 v3)

SPECint2006 = 35.1

SPECint_base2006 = 33.7

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Apr-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias
```

```
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
            -opt-prefetch -ansi-alias
```

```
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: basepeak = yes
```

```
456.hammer: basepeak = yes
```

```
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -unroll14
```

```
462.libquantum: basepeak = yes
```

```
464.h264ref: basepeak = yes
```

C++ benchmarks:

```
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block           -ansi-alias
              -Wl,-z,muldefs -L/sh -lsmartheap
```

```
473.astar: basepeak = yes
```

```
483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
                 -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant ML150 Gen9
(1.90 GHz, Intel Xeon E5-2609 v3)

SPECint2006 = 35.1

SPECint_base2006 = 33.7

CPU2006 license: 3

Test date: Apr-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2014

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>
<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue May 5 15:16:14 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 May 2015.