



SPEC[®] CFP2006 Result

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ZTE

SPECfp[®]2006 = **73.6**

ATCA SBCR (Intel Xeon E5-2628L v2)

SPECfp_base2006 = **69.7**

CPU2006 license: 3834

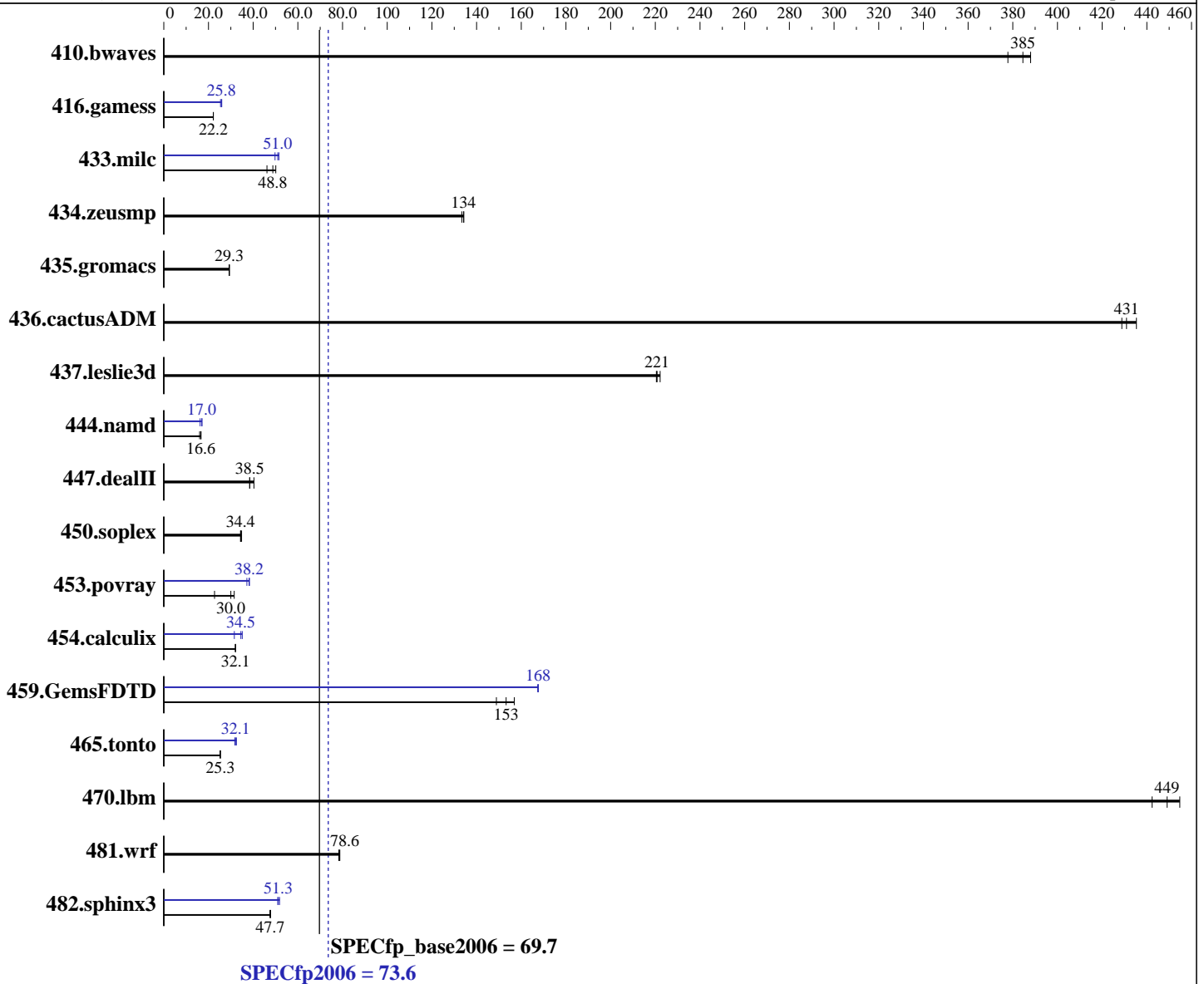
Test sponsor: ZTE

Tested by: ZTE

Test date: Aug-2015

Hardware Availability: Sep-2013

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2628L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz
 CPU MHz: 1900
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0(Maipo) 3.10.0-121.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (8 x 16 GB 2Rx4 PC3-10600R-9 ECC)
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>35.3</u>	<u>385</u>	35.0	388	36.0	378	<u>35.3</u>	<u>385</u>	35.0	388	36.0	378
416.gamess	<u>883</u>	<u>22.2</u>	883	22.2	884	22.1	771	25.4	755	25.9	<u>759</u>	<u>25.8</u>
433.milc	183	50.1	<u>188</u>	<u>48.8</u>	199	46.2	178	51.5	<u>180</u>	<u>51.0</u>	184	49.8
434.zeusmp	67.8	134	68.2	133	<u>67.8</u>	<u>134</u>	67.8	134	68.2	133	<u>67.8</u>	<u>134</u>
435.gromacs	244	29.3	<u>244</u>	<u>29.3</u>	244	29.3	244	29.3	<u>244</u>	<u>29.3</u>	244	29.3
436.cactusADM	<u>27.7</u>	<u>431</u>	27.9	429	27.4	435	<u>27.7</u>	<u>431</u>	27.9	429	27.4	435
437.leslie3d	42.3	222	<u>42.6</u>	<u>221</u>	42.6	220	42.3	222	<u>42.6</u>	<u>221</u>	42.6	220
444.namd	482	16.6	500	16.0	<u>483</u>	<u>16.6</u>	470	17.1	<u>471</u>	<u>17.0</u>	493	16.3
447.dealII	298	38.4	<u>297</u>	<u>38.5</u>	284	40.4	298	38.4	<u>297</u>	<u>38.5</u>	284	40.4
450.soplex	<u>242</u>	<u>34.4</u>	243	34.3	240	34.8	<u>242</u>	<u>34.4</u>	243	34.3	240	34.8
453.povray	234	22.7	<u>177</u>	<u>30.0</u>	169	31.5	<u>139</u>	<u>38.2</u>	139	38.3	143	37.2
454.calculix	<u>257</u>	<u>32.1</u>	257	32.1	257	32.1	261	31.6	235	35.1	<u>239</u>	<u>34.5</u>
459.GemsFDTD	71.3	149	<u>69.2</u>	<u>153</u>	67.6	157	63.4	167	63.3	168	<u>63.3</u>	<u>168</u>
465.tonto	390	25.3	<u>389</u>	<u>25.3</u>	389	25.3	303	32.5	<u>306</u>	<u>32.1</u>	310	31.8
470.lbm	30.2	455	<u>30.6</u>	<u>449</u>	31.1	442	30.2	455	<u>30.6</u>	<u>449</u>	31.1	442
481.wrf	<u>142</u>	<u>78.6</u>	143	78.3	142	78.8	<u>142</u>	<u>78.6</u>	143	78.3	142	78.8
482.sphinx3	408	47.8	410	47.5	<u>409</u>	<u>47.7</u>	382	51.0	<u>380</u>	<u>51.3</u>	376	51.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
 Turbo boost Technology enabled
 Virtualization Technology disabled
 Hyper Threading Technology enabled
 Sysinfo program /home/speccpu/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ 2b55956e7c0e338e808a36a21505f13a
 running on localhost.localdomain Sun Aug 9 13:20:12 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



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Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2628L v2 @ 1.90GHz

2 "physical id"s (chips)

32 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 8

siblings : 16

physical 0: cores 0 1 2 3 4 5 6 7

physical 1: cores 0 1 2 3 4 5 6 7

cache size : 20480 KB

From /proc/meminfo

MemTotal: 131796012 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.0 (Maipo)"

ID="rhel"

ID_LIKE="fedora"

VERSION_ID="7.0"

PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"

ANSI_COLOR="0;31"

CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-121.el7.x86_64 #1 SMP Tue Apr 8 10:48:19

EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 9 05:25

SPEC is set to: /home/speccpu

Filesystem Type Size Used Avail Use% Mounted on

/dev/mapper/rhel-home xfs 225G 78G 148G 35% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. CORE4.6.5_UBF3.10.49_SVN57833 05/06/2015

Memory:

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Platform Notes (Continued)

8x Micron 36KSF2G72PZ-1 16 GB 2 rank 1333 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/speccpu/libs/32:/home/speccpu/libs/64:/home/speccpu/sh"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64

Continued on next page



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Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias

Continued on next page



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Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ZTE-Platform-Flags-V2.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/ZTE-Platform-Flags-V2.0.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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