SPEC® CFP2006 Result

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Huawei

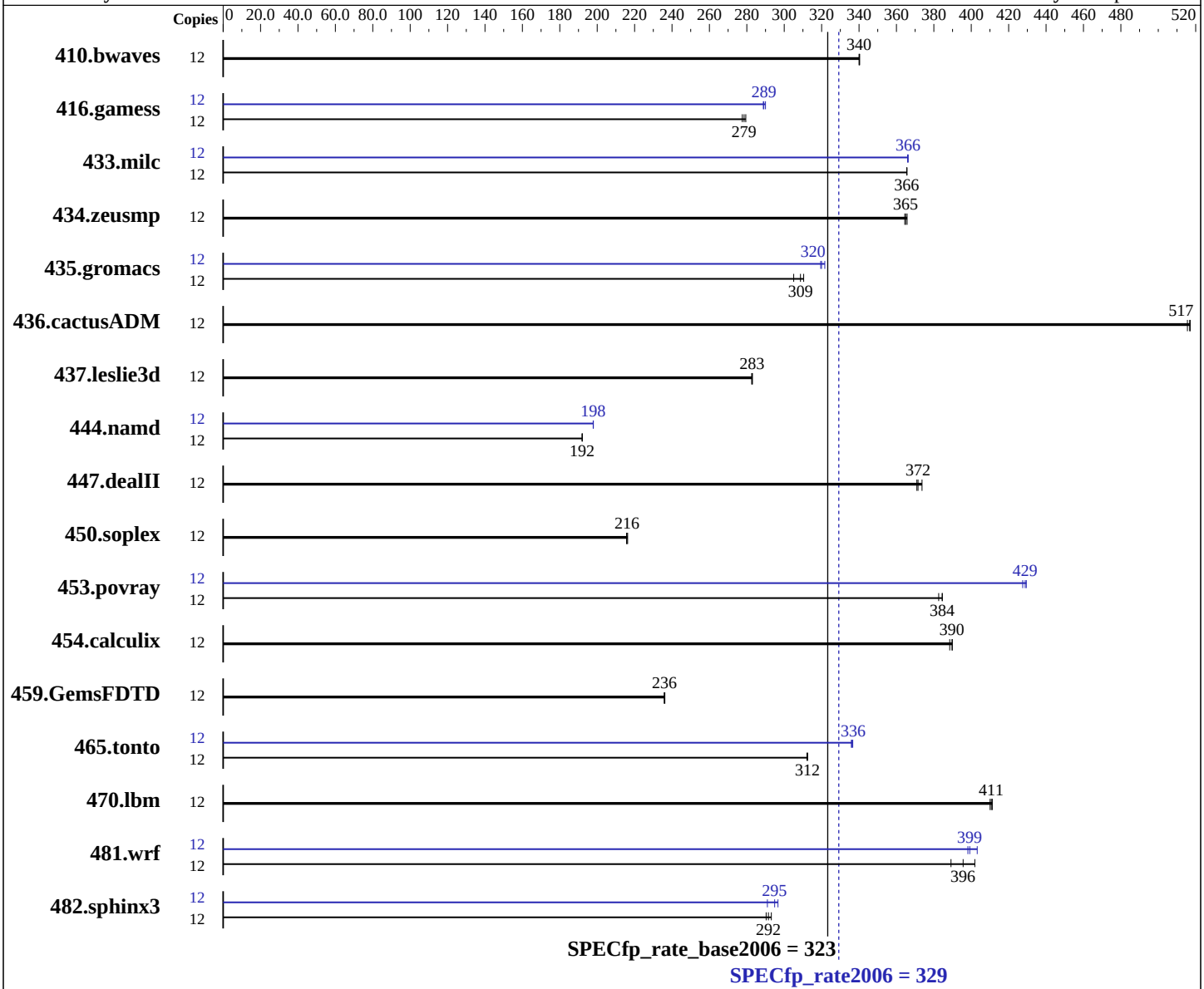
Huawei CH226 V3 (Intel Xeon E5-2609 v3)

SPECfp®_rate2006 = 329

SPECfp_rate_base2006 = 323

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Aug-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014



Hardware

CPU Name:	Intel Xeon E5-2609 v3
CPU Characteristics:	
CPU MHz:	1900
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	**480**	**340**	479	340	480	340	12	**480**	**340**	479	340	480	340
416.gamess	12	**844**	**279**	841	279	846	278	12	810	290	814	289	**813**	**289**
433.milc	12	301	365	301	366	**301**	**366**	12	301	366	301	366	**301**	**366**
434.zeusmp	12	**299**	**365**	299	366	300	364	12	**299**	**365**	299	366	300	364
435.gromacs	12	276	310	281	305	**278**	**309**	12	266	322	268	320	**268**	**320**
436.cactusADM	12	278	516	277	517	**277**	**517**	12	278	516	277	517	**277**	**517**
437.leslie3d	12	399	283	**399**	**283**	399	283	12	399	283	**399**	**283**	399	283
444.namd	12	501	192	**501**	**192**	501	192	12	**486**	**198**	486	198	486	198
447.dealII	12	370	371	367	374	**369**	**372**	12	370	371	367	374	**369**	**372**
450.soplex	12	**464**	**216**	463	216	464	216	12	**464**	**216**	463	216	464	216
453.povray	12	167	383	**166**	**384**	166	385	12	**149**	**429**	149	429	149	428
454.calculix	12	**254**	**390**	254	390	255	388	12	**254**	**390**	254	390	255	388
459.GemsFDTD	12	539	236	**539**	**236**	540	236	12	539	236	**539**	**236**	540	236
465.tonto	12	378	312	378	313	**378**	**312**	12	351	337	352	336	**351**	**336**
470.lbm	12	**401**	**411**	401	411	402	410	12	**401**	**411**	401	411	402	410
481.wrf	12	333	402	**339**	**396**	344	389	12	**336**	**399**	337	398	332	403
482.sphinx3	12	806	290	798	293	**802**	**292**	12	**793**	**295**	804	291	788	297

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to ES mode
```

Continued on next page

Platform Notes (Continued)

```
Set Patrol Scrub to Disable
Sysinfo program /spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Tue Aug 25 20:28:02 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2609 v3 @ 1.90GHz
      2 "physical id"s (chips)
      12 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 6
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       263579832 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
   EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 25 11:01

SPEC is set to: /spec
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda1      ext4  443G   19G  401G   5% /
Additional information from dmidecode:
```

Continued on next page

Platform Notes (Continued)

```
      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS Insyde Corp. 1.39 05/06/2015
    Memory:
     8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz, configured at 1600 MHz
     8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz
     8x NO DIMM NO DIMM   3 rank

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
```

Continued on next page

Base Portability Flags (Continued)

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3 -unroll2

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Continued on next page

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-opt-mem-layout-trans=3`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml`

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For other inquiries, please contact webmaster@spec.org.

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