SPEC® CINT2006 Result

SGI

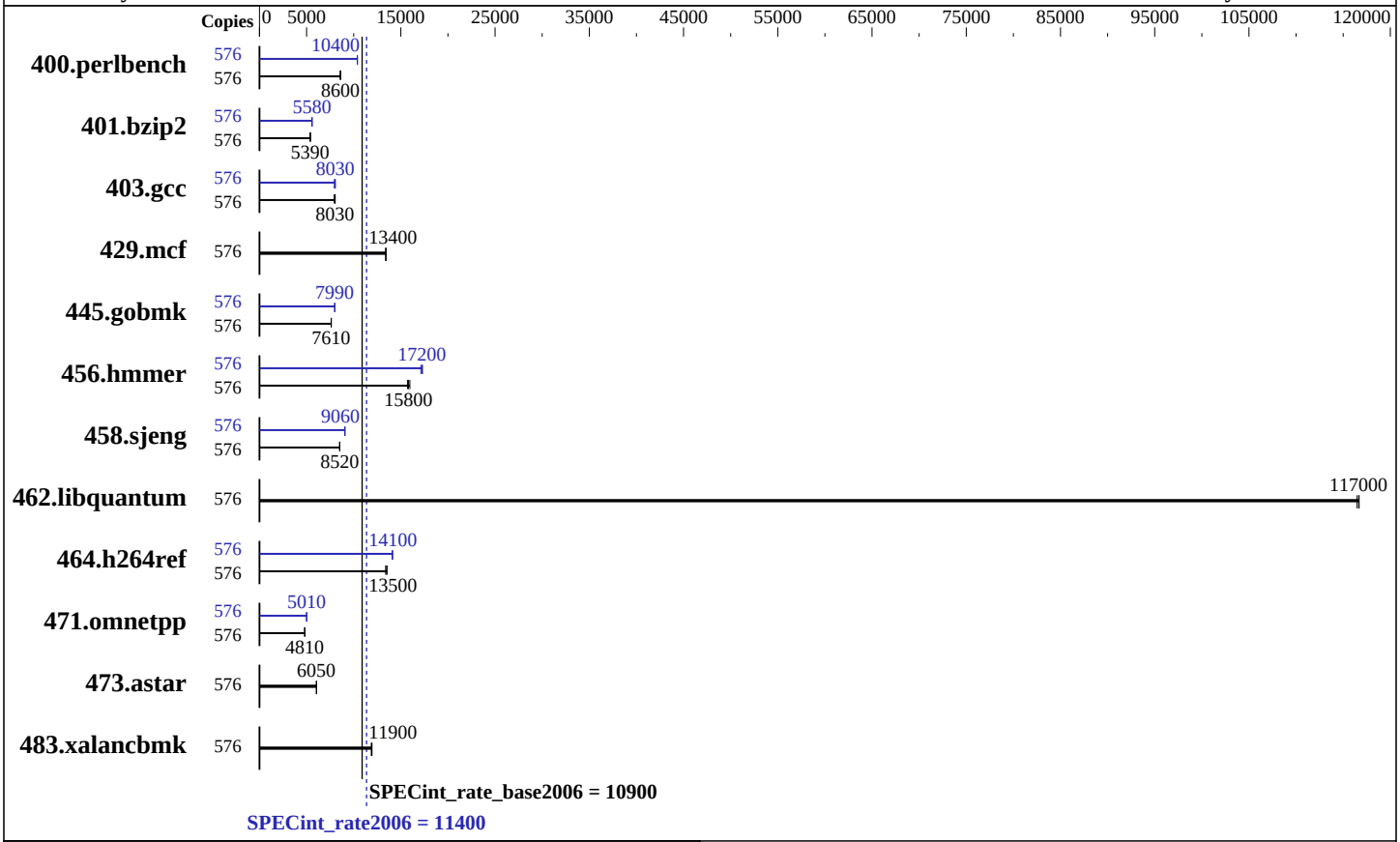
SGI UV 300 (Intel Xeon E7-8890 v3, 2.5 GHz)

SPECint®_rate2006 = 11400

SPECint_rate_base2006 = 10900

CPU2006 license: 4
Test sponsor: SGI
Tested by: SGI

Test date: Dec-2015
Hardware Availability: Sep-2015
Software Availability: Nov-2015



Hardware

CPU Name:	Intel Xeon E7-8890 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	288 cores, 16 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable:	4-64 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	45 MB I+D on chip per chip
Other Cache:	None
Memory:	8 TB (256 x 32 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	8 TB tmpfs
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP4, Kernel 3.0.101-65.1.9552.0.PTF-default
Compiler:	C/C++: Version 16.0.0.109 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	576	**654**	**8600**	658	8550	652	8630	576	542	10400	**542**	**10400**	542	10400
401.bzip2	576	1028	5410	1034	5380	**1031**	**5390**	576	995	5580	1001	5550	**996**	**5580**
403.gcc	576	578	8030	585	7930	**578**	**8030**	576	584	7940	**577**	**8030**	576	8040
429.mcf	576	392	13400	392	13400	**392**	**13400**	576	392	13400	392	13400	**392**	**13400**
445.gobmk	576	794	7610	793	7620	**794**	**7610**	576	756	7990	**756**	**7990**	757	7980
456.hmmer	576	337	16000	**340**	**15800**	342	15700	576	314	17100	**312**	**17200**	311	17300
458.sjeng	576	819	8510	**818**	**8520**	818	8520	576	**769**	**9060**	769	9060	769	9060
462.libquantum	576	102	116000	102	117000	**102**	**117000**	576	102	116000	102	117000	**102**	**117000**
464.h264ref	576	952	13400	**945**	**13500**	942	13500	576	905	14100	**903**	**14100**	903	14100
471.omnetpp	576	750	4800	748	4810	**749**	**4810**	576	720	5000	719	5010	**719**	**5010**
473.astar	576	669	6050	669	6050	**669**	**6050**	576	669	6050	669	6050	**669**	**6050**
483.xalancbmk	576	335	11900	**334**	**11900**	333	11900	576	335	11900	**334**	**11900**	333	11900

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"

Tmpfs filesystem set up with:
  mkdir -p /mnt/shm/cpu2006-ic16
  mount -t tmpfs -o size=8192G,rw tmpfs /mnt/shm/cpu2006-ic16

Turbo mode activated with:
  modprobe acpi_cpufreq
  cpupower frequency-set -u 3300MHz -d 3300MHz -g performance
```

Platform Notes

```
BT Mode set to Auto-select
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/mnt/shm/cpu2006-ic16/libs/32:/mnt/shm/cpu2006-ic16/libs/64:/mnt/shm/cpu2006-ic16/sh"

 Transparent Huge Pages enabled with:
```

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General Notes (Continued)

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/sw/sdev/intel/parallel_studio_xe_2016/compilers_and_libraries/linux/lib/ia32_lin
```

C++ benchmarks:

```
icpc -m32 -L/sw/sdev/intel/parallel_studio_xe_2016/compilers_and_libraries/linux/lib/ia32_lin
```

Base Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-D_FILE_OFFSET_BITS=64`
403.gcc: `-D_FILE_OFFSET_BITS=64`
429.mcf: `-D_FILE_OFFSET_BITS=64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-D_FILE_OFFSET_BITS=64`
458.sjeng: `-D_FILE_OFFSET_BITS=64`
462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
464.h264ref: `-D_FILE_OFFSET_BITS=64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-D_FILE_OFFSET_BITS=64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs
-L/root/cpu2006-ic16/sh -lsmartheap
```

Base Other Flags

C benchmarks:

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Base Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/sw/sdev/intel/parallel_studio_xe_2016/compilers_and_libraries/linux/lib/ia32_lin`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/sw/sdev/intel/parallel_studio_xe_2016/compilers_and_libraries/linux/lib/ia32_lin`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
403.gcc: `-D_FILE_OFFSET_BITS=64`
429.mcf: `-D_FILE_OFFSET_BITS=64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
458.sjeng: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
464.h264ref: `-D_FILE_OFFSET_BITS=64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-D_FILE_OFFSET_BITS=64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1)
`-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2)
`-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-auto-ilp32`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1)
`-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2)
`-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-opt-prefetch`
`-auto-ilp32 -ansi-alias`

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Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/root/cpu2006-ic16/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/SGI-UV300-Platform-Flags.20160112.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/SGI-UV300-Platform-Flags.20160112.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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