



# SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019S-M  
(X11SSH-F, Intel Xeon E3-1225 v5)

SPECint®2006 = 71.3

SPECint\_base2006 = 69.5

CPU2006 license: 001176

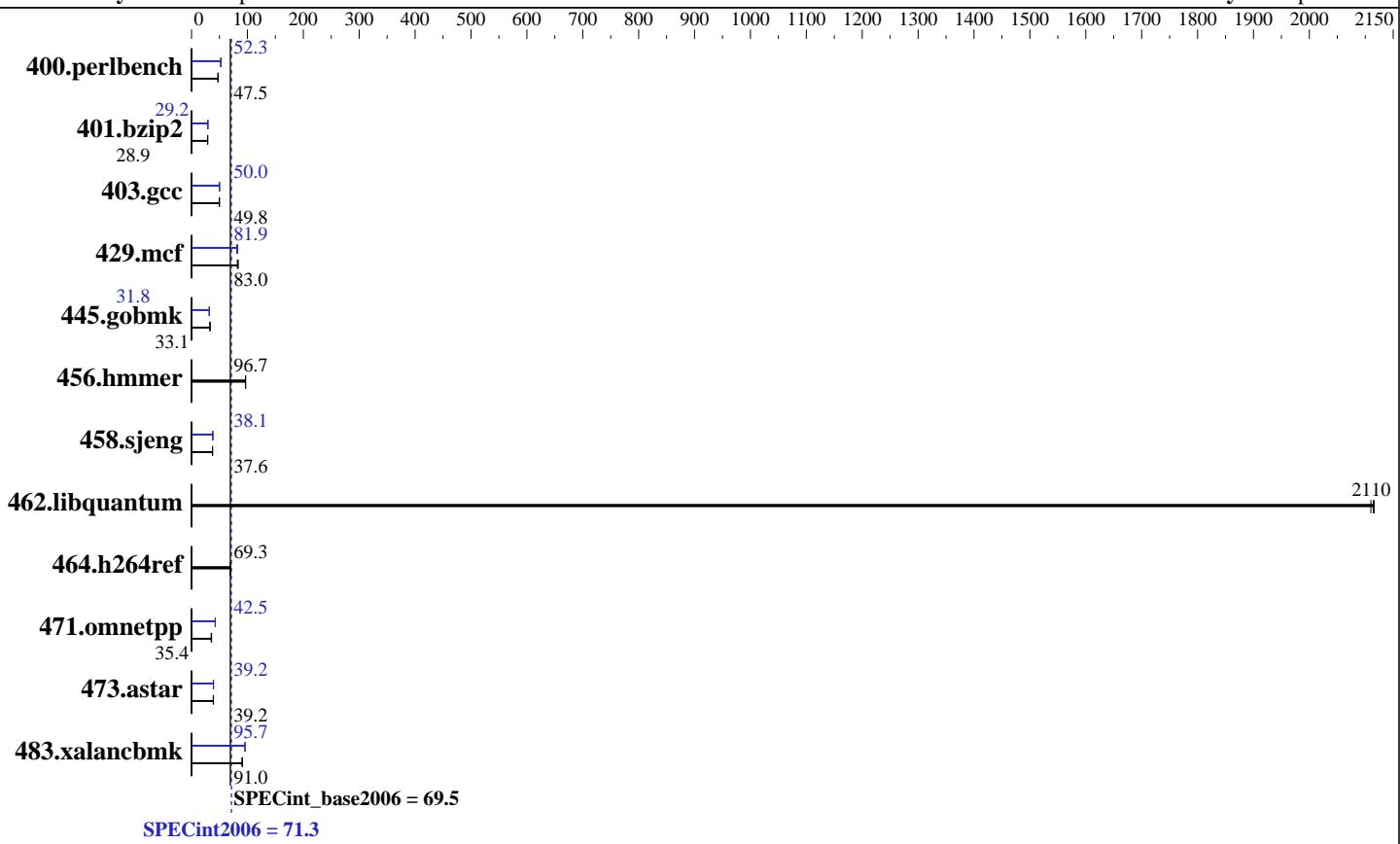
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Oct-2015

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Xeon E3-1225 v5  
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
CPU MHz: 3300  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-U)  
Disk Subsystem: 1 x 400 GB SATA III SSD  
Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2  
Compiler: Kernel 3.10.0-327.el7.x86\_64  
Auto Parallel: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
File System: Yes  
System State: xfs  
Base Pointers: Run level 3 (multi-user)  
Peak Pointers: 32/64-bit  
Other Software: 32/64-bit  
Microquill SmartHeap V10.2



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019S-M  
(X11SSH-F, Intel Xeon E3-1225 v5)

**SPECint2006 = 71.3**

**SPECint\_base2006 = 69.5**

CPU2006 license: 001176

Test date: Dec-2015

Test sponsor: Supermicro

Hardware Availability: Oct-2015

Tested by: Supermicro

Software Availability: Sep-2015

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	205	47.7	207	47.2	<b><u>206</u></b>	<b><u>47.5</u></b>	<b><u>187</u></b>	<b><u>52.3</u></b>	186	52.6	187	52.2
401.bzip2	<b><u>334</u></b>	<b><u>28.9</u></b>	334	28.9	335	28.8	<b><u>330</u></b>	<b><u>29.2</u></b>	330	29.2	330	29.3
403.gcc	161	49.9	<b><u>162</u></b>	<b><u>49.8</u></b>	162	49.8	<b><u>161</u></b>	<b><u>50.0</u></b>	161	49.9	161	50.1
429.mcf	111	82.0	109	83.5	<b><u>110</u></b>	<b><u>83.0</u></b>	<b><u>111</u></b>	<b><u>81.9</u></b>	111	82.4	113	80.8
445.gobmk	317	33.1	317	33.1	<b><u>317</u></b>	<b><u>33.1</u></b>	330	31.8	<b><u>330</u></b>	<b><u>31.8</u></b>	330	31.8
456.hmmer	96.3	96.8	96.7	96.5	<b><u>96.5</u></b>	<b><u>96.7</u></b>	96.3	96.8	96.7	96.5	<b><u>96.5</u></b>	<b><u>96.7</u></b>
458.sjeng	<b><u>322</u></b>	<b><u>37.6</u></b>	322	37.6	322	37.6	317	38.1	318	38.1	<b><u>318</u></b>	<b><u>38.1</u></b>
462.libquantum	<b><u>9.80</u></b>	<b><u>2110</u></b>	9.79	2120	9.82	2110	<b><u>9.80</u></b>	<b><u>2110</u></b>	9.79	2120	9.82	2110
464.h264ref	320	69.2	<b><u>319</u></b>	<b><u>69.3</u></b>	318	69.6	320	69.2	<b><u>319</u></b>	<b><u>69.3</u></b>	318	69.6
471.omnetpp	174	35.9	177	35.3	<b><u>177</u></b>	<b><u>35.4</u></b>	147	42.4	<b><u>147</u></b>	<b><u>42.5</u></b>	147	42.6
473.astar	179	39.3	181	38.9	<b><u>179</u></b>	<b><u>39.2</u></b>	178	39.4	179	39.2	<b><u>179</u></b>	<b><u>39.2</u></b>
483.xalancbmk	75.7	91.2	<b><u>75.8</u></b>	<b><u>91.0</u></b>	76.5	90.2	72.1	95.7	72.1	95.7	<b><u>72.1</u></b>	<b><u>95.7</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date::: 2014-06-25 #$
running on localhost.localdomain Fri Dec 25 06:03:31 2015
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1225 v5 @ 3.30GHz
        1 "physical id"s (chips)
        4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 4
        siblings    : 4
        physical 0: cores 0 1 2 3
cache size : 8192 KB
```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019S-M  
(X11SSH-F, Intel Xeon E3-1225 v5)

**SPECint2006 = 71.3**

**SPECint\_base2006 = 69.5**

**CPU2006 license:** 001176

**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** Dec-2015

**Hardware Availability:** Oct-2015

**Software Availability:** Sep-2015

## Platform Notes (Continued)

```
From /proc/meminfo
MemTotal:       16047796 kB
HugePages_Total:        0
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.2 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.2"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 24 17:28

SPEC is set to: /home/cpu2006
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs   280G   26G  254G  10% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0a 11/13/2015
Memory:
2x Micron 16ATF1G64AZ-2G1A2 8 GB 2 rank 2133 MHz
2x Not Specified Not Specified

(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019S-M  
(X11SSH-F, Intel Xeon E3-1225 v5)

**SPECint2006 = 71.3**

**SPECint\_base2006 = 69.5**

**CPU2006 license:** 001176

**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** Dec-2015

**Hardware Availability:** Oct-2015

**Software Availability:** Sep-2015

## General Notes (Continued)

memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019S-M  
(X11SSH-F, Intel Xeon E3-1225 v5)

**SPECint2006 = 71.3**

**SPECint\_base2006 = 69.5**

**CPU2006 license:** 001176

**Test date:** Dec-2015

**Test sponsor:** Supermicro

**Hardware Availability:** Oct-2015

**Tested by:** Supermicro

**Software Availability:** Sep-2015

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019S-M  
(X11SSH-F, Intel Xeon E3-1225 v5)

**SPECint2006 = 71.3**

**SPECint\_base2006 = 69.5**

**CPU2006 license:** 001176

**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** Dec-2015

**Hardware Availability:** Oct-2015

**Software Availability:** Sep-2015

## Peak Optimization Flags (Continued)

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5019S-M  
(X11SSH-F , Intel Xeon E3-1225 v5)

SPECint2006 = 71.3

SPECint\_base2006 = 69.5

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Oct-2015

Software Availability: Sep-2015

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jan 12 15:46:17 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 12 January 2016.