SPEC® CFP2006 Result

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ACTION S.A.

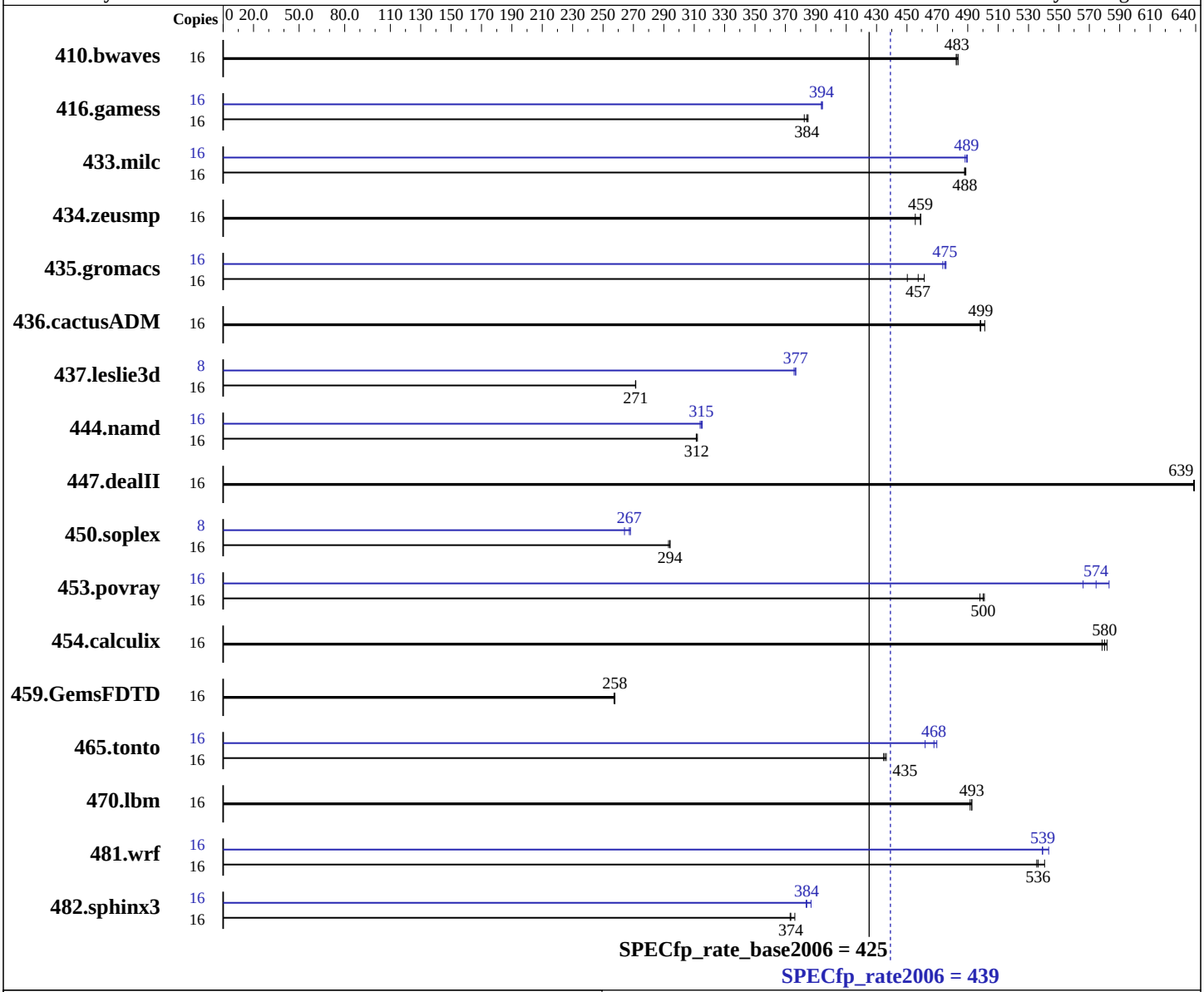
ACTINA SOLAR 220 S6 (Intel Xeon E5-2637 v3, 3.50 GHz)

SPECfp®_rate2006 = 439

SPECfp_rate_base2006 = 425

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Feb-2016
Hardware Availability: Sep-2014
Software Availability: Aug-2015



Hardware

CPU Name:	Intel Xeon E5-2637 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 7.1 (Maipo) 3.10.0-229.11.1.el7.x86_64
Compiler:	C/C++: Version 16.0.0.047 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.047 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 240 GB SATA II SSD
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	450	484	451	482	**450**	**483**	16	450	484	451	482	**450**	**483**
416.gamess	16	**815**	**384**	819	382	814	385	16	794	394	**795**	**394**	796	394
433.milc	16	301	488	**301**	**488**	301	489	16	301	488	300	490	**300**	**489**
434.zeusmp	16	317	459	320	455	**317**	**459**	16	317	459	320	455	**317**	**459**
435.gromacs	16	248	461	254	450	**250**	**457**	16	241	474	**241**	**475**	240	476
436.cactusADM	16	384	498	**384**	**499**	381	501	16	384	498	**384**	**499**	381	501
437.leslie3d	16	554	271	**554**	**271**	554	272	8	**200**	**377**	200	377	200	376
444.namd	16	412	311	**412**	**312**	411	312	16	409	314	**408**	**315**	407	315
447.dealII	16	287	639	**286**	**639**	286	639	16	287	639	**286**	**639**	286	639
450.soplex	16	455	293	454	294	**454**	**294**	8	249	268	253	264	**250**	**267**
453.povray	16	170	501	**170**	**500**	171	498	16	150	566	146	583	**148**	**574**
454.calculix	16	**228**	**580**	227	582	228	578	16	**228**	**580**	227	582	228	578
459.GemsFDTD	16	**659**	**258**	658	258	660	257	16	**659**	**258**	658	258	660	257
465.tonto	16	362	435	361	436	**362**	**435**	16	**337**	**468**	335	470	341	462
470.lbm	16	447	492	446	493	**446**	**493**	16	447	492	446	493	**446**	**493**
481.wrf	16	**333**	**536**	334	535	331	541	16	332	539	329	543	**331**	**539**
482.sphinx3	16	**835**	**374**	829	376	836	373	16	813	384	806	387	**812**	**384**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Bios Settings
Hyper-Threading (All) = Enable
```

Continued on next page

Platform Notes (Continued)

```
Power Technology = Energy Efficient
Enforce POR = Disabled
Memory Frequency = 2133

BMC Setting
Fan Mode = Full Speed

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on SUT Fri Feb 12 18:24:25 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2637 v3 @ 3.50GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 4 5
      physical 1: cores 0 1 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       263869000 kB
   HugePages_Total:       1
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   os-release.rpmnew:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.1 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.1"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
```

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Platform Notes (Continued)

```
      redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
      system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

   uname -a:
      Linux SUT 3.10.0-229.11.1.el7.x86_64 #5 SMP Mon Sep 14 17:11:19 CEST 2015
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Feb 12 10:02

   SPEC is set to: /cpu2006.1.2
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda1      ext4  212G   37G  165G  19% /

   Additional information from dmidecode:
     BIOS American Megatrends Inc. 1.1 08/13/2015
     Memory:
      16x   16 GB
      16x Micron 36ASF2G72PZ-2G1A2 16 GB 2133 MHz 2 rank

   (End of data from sysinfo program)
   dmidecode does not properly detect memory modules
   16 modules of 16 GB were used to run the test (256 GB total)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 Binaries compiled on a system with 2x Xeon E5-2650 v3 chips + 256 GB memory
 using RedHat EL 7.1
```

Base Compiler Invocation

C benchmarks:
```
     icc   -m64
```

C++ benchmarks:
```
     icpc  -m64
```

Fortran benchmarks:
```
     ifort -m64
```

Continued on next page

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/lib/ia32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

C++ benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevD-aug-2015-For-Supermicro-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevD-aug-2015-For-Supermicro-Platform.xml

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