SPEC® CFP2006 Result

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Huawei

Huawei RH2288 V3 (Intel Xeon E5-2683 v4)

SPECfp®_rate2006 = NC

SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Aug-2015

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU ru up policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">gener

Copies	
410.bwaves	
416.gamess	
433.milc	
434.zeusmp	
435.gromacs	
436.cactusADM	
437.leslie3d	
444.namd	
447.dealII	
450.soplex	
453.povray	
454.calculix	
459.GemsFDTD	
[illegible]	
[illegible]lbm	
481.wrf	
482.sphinx3	

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Hardware

CPU Name:	Intel Xeon E5-2683 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	32 cores, 2 chips, 16 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	40 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-)
Disk Subsystem:	1 x 500GB SATA, 10000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
416.gamess	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
433.milc	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
434.zeusmp	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
435.gromacs	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
436.cactusADM	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
437.leslie3d	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
444.namd	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
447.dealII	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
450.soplex	64	NC	NC	NC	NC	NC	NC	32	NC	NC	NC	NC	NC	NC
453.povray	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
454.calculix	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
459.GemsFDTD	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
465.tonto	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
470.lbm	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
481.wrf	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC
482.sphinx3	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

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Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode
Set Patrol Scrub to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667a2859632ceab81e28219e1
running on localhost.localdomain Fri Mar 25 04:39:00 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2683 v4 @ 2.10GHz
      2 "physical id"s (chips)
      64 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 1
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       263565104 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

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Platform Notes (Continued)

```
uname -a:
   Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
   EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 23 05:40

SPEC is set to: /spec16
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      xfs   449G   64G  386G  15% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Insyde Corp. 3.09 02/15/2016
  Memory:
   8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
   8x Samsung M393A2G40EB2-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)
```

General Notes

```
Environment variable set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc   -m64
```

Continued on next page

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP
416.gamess: -DSPEC_CPU_LP6
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP -nofor_main
437.leslie3d: -DSPEC_CPU_LP
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPE CPU_L 64
450.soplex: -DSPE CPU_LP
453.povray: -DSPEC_ _LP64
454.calculix: - CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC CPU_LP64
465.tonto: -DSPEC_ PU_LP64
470.lbm: -DSPEC_ PU_LP64
481.wrf: - PEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Continued on next page

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU ru up policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">gener

Base Optimization Flags (Continued)

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPECCPU_LP64
[illegible]: -DSPECCPU_LP64
433.milc: -DSPECCPU_LP64
434.zeusmp: -DSPECCPU_LP64
435.gromacs: -DSPECCPU_LP64 -nofor_main
436.cactusADM: -DSPECCPU_LP64 -nofor_main
437.leslie3d: -DSPECCPU_LP64
444.namd: -DSPECCPU_LP64
447.dealII: -DSPECCPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPECCPU_LP64
454.calculix: -DSPECCPU_LP64 -nofor_main
459.GemsFDTD: -DSPECCPU_LP64

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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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