Huawei RH2288H V3 (Intel Xeon E5-2609 v4)

**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Test date:** Mar-2016  
**Hardware Availability:** Mar-2016  
**Software Availability:** Mar-2016

<table>
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<tr>
<th>Copies</th>
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<tr>
<td>410.bwaves</td>
</tr>
<tr>
<td>416.gamess</td>
</tr>
<tr>
<td>433.milc</td>
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<tr>
<td>434.zeusmp</td>
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<tr>
<td>435.gromacs</td>
</tr>
<tr>
<td>436.cactusADM</td>
</tr>
<tr>
<td>437.leslie3d</td>
</tr>
<tr>
<td>444.namd</td>
</tr>
<tr>
<td>447.dealII</td>
</tr>
<tr>
<td>450.soplex</td>
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<td>453.povray</td>
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<td>454.calculix</td>
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<td>459.GemsFDTD</td>
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<tr>
<td>481.wrf</td>
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<td>482.sphinx3</td>
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SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).
Huawei RH2288H V3 (Intel Xeon E5-2609 v4)

<table>
<thead>
<tr>
<th>SPEC CFP2006 Result</th>
<th>Huawei RH2288H V3 (Intel Xeon E5-2609 v4)</th>
</tr>
</thead>
</table>

**CPU2006 license:** 3175  
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---

**SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.**

---

### Hardware

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<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
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</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon E5-2609 v4</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>16 cores, 2 chips, 8 cores/chip</td>
</tr>
<tr>
<td>CPU MHz</td>
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</tr>
<tr>
<td>FPU</td>
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<td>CPU(s) enabled</td>
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<tr>
<td>CPU(s) orderable</td>
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<td>Primary Cache</td>
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<td>Secondary Cache</td>
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<td>L3 Cache</td>
<td>Primary Cache 20 MB I+D on chip per chip</td>
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<td>Other Cache</td>
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<td>Memory</td>
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<tr>
<td>Disk Subsystem</td>
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<td>Other Hardware</td>
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</table>

### Software

<table>
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<tr>
<th>Parameter</th>
<th>Value</th>
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</thead>
<tbody>
<tr>
<td>Operating System</td>
<td>Red Hat Enterprise Linux Server release 7.0 (Maipo)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<tr>
<td>Auto Parallel</td>
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<td>File System</td>
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<td>System State</td>
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<tr>
<td>Base Pointers</td>
<td>32/64-bit</td>
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<td>Peak Pointers</td>
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<td>Other Software</td>
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SPEC CFP2006 Result

Huawei

Huawei RH2288H V3 (Intel Xeon E5-2609 v4)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

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Results Table

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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Non-Compliant
Huawei RH2288H V3 (Intel Xeon E5-2609 v4)

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Sysinfo program /speccpu/spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb866 e2a2855e02eab81e28219e1
running on localhost.localdomain Tue Mar  8 00:14:41 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2609 v4 @ 1.70GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 1 2 3 4 5 6 7
physical 1: cores 0 2 3 4 5 6 7
cache size : 20480 KB

From /proc/meminfo
MemTotal:  63571176 kB
B HogPages:Total:  0
HugePages:  2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.0:GA:server
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Non-Compliant
Huawei RH2288H V3 (Intel Xeon E5-2609 v4)

CPU2006 license: 3175
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SPECFp_rate2006 = NC
SPECFp_rate_base2006 = NC

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

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Platform Notes (Continued)

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 7 05:24

SPEC is set to: /speccpu/spec16
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda2      ext4  1.6T   25G  1.5T   2% /

Additional information from dmidecode:
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.09 02/22/2016
Memory:
8x NO DIMM NO DIMM 3 rank
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz, configured at 1867 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz, configured at 1867 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/speccpu/spec16/libs/32:/speccpu/spec16/libs/64:/speccpu/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runc spec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>

Non-Compliant
Huawei

Huawei RH2288H V3 (Intel Xeon E5-2609 v4)

<table>
<thead>
<tr>
<th>SPECfp_rate2006</th>
<th>NC</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
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Base Compiler Invocation

<table>
<thead>
<tr>
<th>C benchmarks:</th>
</tr>
</thead>
<tbody>
<tr>
<td>icc -m64</td>
</tr>
<tr>
<td>C++ benchmarks:</td>
</tr>
<tr>
<td>icpc -m64</td>
</tr>
<tr>
<td>Fortran benchmarks:</td>
</tr>
<tr>
<td>ifort -m64</td>
</tr>
<tr>
<td>Benchmarks using both Fortran and C:</td>
</tr>
<tr>
<td>icc -m64 ifort -m64</td>
</tr>
</tbody>
</table>

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

<table>
<thead>
<tr>
<th>C benchmarks:</th>
</tr>
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<tbody>
<tr>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3</td>
</tr>
</tbody>
</table>

Continued on next page
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Non-Compliant
Huawei

Huawei RH2288H V3 (Intel Xeon E5-2609 v4)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Peak Optimization Flags (Continued)

C++ benchmarks:
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -no-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipc(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-thread=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inlinel-level=0 -ansi-alias

434.leslie3d: basepeak = yes
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
-inlinel-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.
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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 19 April 2016.

Non-Compliant