Supermicro
SuperServer 5019S-MT
(X11SSH-TF, Intel Xeon E3-1240 v5)

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro
Test date: Jan-2016
Hardware Availability: Oct-2015

SPECfp®2006 = 100
SPECfp_base2006 = 98.1

Software
Operating System: Red Hat Enterprise Linux Server release 7.2, Kernel 3.10.0-327.el7.x86_64
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)

Hardware
CPU Name: Intel Xeon E3-1240 v5
CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz: 3500
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

SPECfp_base2006 = 98.1
SPECfp2006 = 100

Continued on next page
Supermicro
SuperServer 5019S-MT
(X11SSH-TF, Intel Xeon E3-1240 v5)

SPECfp2006 = 100
SPECfp_base2006 = 98.1

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-U)
Disk Subsystem: 1 x 400 GB SATA III SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Test date: Jan-2016
Hardware Availability: Oct-2015
Software Availability: Sep-2015

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>92.5</td>
<td>147</td>
<td>92.3</td>
<td>147</td>
<td>92.6</td>
<td>147</td>
<td>92.5</td>
<td>147</td>
<td>92.3</td>
<td>147</td>
<td>92.6</td>
<td>147</td>
</tr>
<tr>
<td>416.gamess</td>
<td>386</td>
<td>50.7</td>
<td>386</td>
<td>50.7</td>
<td>386</td>
<td>50.7</td>
<td>352</td>
<td>55.7</td>
<td>352</td>
<td>55.7</td>
<td>352</td>
<td>55.6</td>
</tr>
<tr>
<td>433.milc</td>
<td>81.2</td>
<td>113</td>
<td>81.3</td>
<td>113</td>
<td>81.3</td>
<td>113</td>
<td>81.2</td>
<td>113</td>
<td>81.3</td>
<td>113</td>
<td>81.3</td>
<td>113</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>42.4</td>
<td>215</td>
<td>42.4</td>
<td>215</td>
<td>42.4</td>
<td>215</td>
<td>42.4</td>
<td>215</td>
<td>42.4</td>
<td>215</td>
<td>42.4</td>
<td>215</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>104</td>
<td>68.9</td>
<td>104</td>
<td>68.9</td>
<td>104</td>
<td>68.9</td>
<td>104</td>
<td>68.9</td>
<td>104</td>
<td>68.9</td>
<td>104</td>
<td>68.9</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>32.7</td>
<td>365</td>
<td>32.6</td>
<td>367</td>
<td>32.4</td>
<td>369</td>
<td>32.7</td>
<td>365</td>
<td>32.6</td>
<td>367</td>
<td>32.4</td>
<td>369</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>86.3</td>
<td>109</td>
<td>86.5</td>
<td>109</td>
<td>86.3</td>
<td>109</td>
<td>86.3</td>
<td>109</td>
<td>86.3</td>
<td>109</td>
<td>86.3</td>
<td>109</td>
</tr>
<tr>
<td>444.namd</td>
<td>213</td>
<td>37.6</td>
<td>213</td>
<td>37.6</td>
<td>213</td>
<td>37.6</td>
<td>210</td>
<td>38.1</td>
<td>209</td>
<td>38.3</td>
<td>210</td>
<td>38.2</td>
</tr>
<tr>
<td>447.dealII</td>
<td>139</td>
<td>82.0</td>
<td>139</td>
<td>82.1</td>
<td>139</td>
<td>82.1</td>
<td>139</td>
<td>82.1</td>
<td>139</td>
<td>82.1</td>
<td>139</td>
<td>82.1</td>
</tr>
<tr>
<td>450.soplex</td>
<td>148</td>
<td>56.3</td>
<td>147</td>
<td>56.6</td>
<td>147</td>
<td>56.6</td>
<td>148</td>
<td>56.3</td>
<td>147</td>
<td>56.6</td>
<td>147</td>
<td>56.6</td>
</tr>
<tr>
<td>453.povray</td>
<td>73.0</td>
<td>72.8</td>
<td>73.4</td>
<td>72.5</td>
<td>73.4</td>
<td>72.5</td>
<td>64.7</td>
<td>82.2</td>
<td>65.7</td>
<td>81.0</td>
<td>64.9</td>
<td>82.0</td>
</tr>
<tr>
<td>454.calculix</td>
<td>105</td>
<td>78.9</td>
<td>105</td>
<td>78.7</td>
<td>105</td>
<td>78.8</td>
<td>103</td>
<td>79.8</td>
<td>104</td>
<td>79.4</td>
<td>104</td>
<td>79.4</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>126</td>
<td>84.1</td>
<td>126</td>
<td>84.0</td>
<td>126</td>
<td>84.1</td>
<td>124</td>
<td>85.4</td>
<td>124</td>
<td>85.6</td>
<td>124</td>
<td>85.5</td>
</tr>
<tr>
<td>465.tonto</td>
<td>145</td>
<td>67.9</td>
<td>145</td>
<td>67.9</td>
<td>145</td>
<td>67.7</td>
<td>132</td>
<td>74.4</td>
<td>132</td>
<td>74.6</td>
<td>132</td>
<td>74.4</td>
</tr>
<tr>
<td>470.lbm</td>
<td>74.7</td>
<td>184</td>
<td>74.6</td>
<td>184</td>
<td>74.6</td>
<td>184</td>
<td>74.7</td>
<td>184</td>
<td>74.6</td>
<td>184</td>
<td>74.6</td>
<td>184</td>
</tr>
<tr>
<td>481.wrf</td>
<td>84.3</td>
<td>133</td>
<td>84.3</td>
<td>132</td>
<td>84.0</td>
<td>133</td>
<td>84.3</td>
<td>133</td>
<td>84.3</td>
<td>132</td>
<td>84.0</td>
<td>133</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>187</td>
<td>104</td>
<td>189</td>
<td>103</td>
<td>187</td>
<td>104</td>
<td>187</td>
<td>104</td>
<td>189</td>
<td>103</td>
<td>187</td>
<td>104</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Hyper-threading = Disabled
 Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Mon Jan  4 19:41:32 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

Continued on next page
Supermicro
SuperServer 5019S-MT
(X11SSH-TF , Intel Xeon E3-1240 v5)

SPECfp2006 = 100
SPECfp_base2006 = 98.1

Platform Notes (Continued)

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1240 v5 @ 3.50GHz
   1 "physical id"s (chips)
   4 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
       cpu cores : 4
       siblings : 4
       physical 0: cores 0 1 2 3
       cache size : 8192 KB

From /proc/meminfo
   MemTotal:       16088400 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.2 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.2"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
      redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
      system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

   uname -a:
      Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 4 15:29

SPEC is set to: /home/cpu2006
   Filesystem        Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel-root xfs   280G   26G  254G  10% /

Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS American Megatrends Inc. T20151229190512 12/29/2015
   Memory:
      2x Micron 16ATF1G64AZ-2G1A2 8 GB 2 rank 2133 MHz
      2x Not Specified Not Specified
Supermicro
SuperServer 5019S-MT
(X11SSH-TF , Intel Xeon E3-1240 v5)

SPECfp2006 = 100
SPECfp_base2006 = 98.1

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Platform Notes (Continued)
(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
343.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Continued on next page

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Supermicro
SuperServer 5019S-MT
(X11SSH-TF, Intel Xeon E3-1240 v5)

SPECfp2006 = 100
SPECfp_base2006 = 98.1

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jan-2016
Hardware Availability: Oct-2015
Software Availability: Sep-2015

Base Portability Flags (Continued)

482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort  -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes

Continued on next page
Peak Optimization Flags (Continued)

470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes

Continued on next page
Supermicro
SuperServer 5019S-MT
(X11SSH-TF, Intel Xeon E3-1240 v5)

SPECfp2006 = 100
SPECfp_base2006 = 98.1

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jan-2016
Hardware Availability: Oct-2015
Software Availability: Sep-2015

Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 19 April 2016.