



SPEC[®] CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9
(3.20 GHz, Intel Xeon E5-2667 v4)

SPECfp[®]2006 = 127

SPECfp_base2006 = 124

CPU2006 license: 3

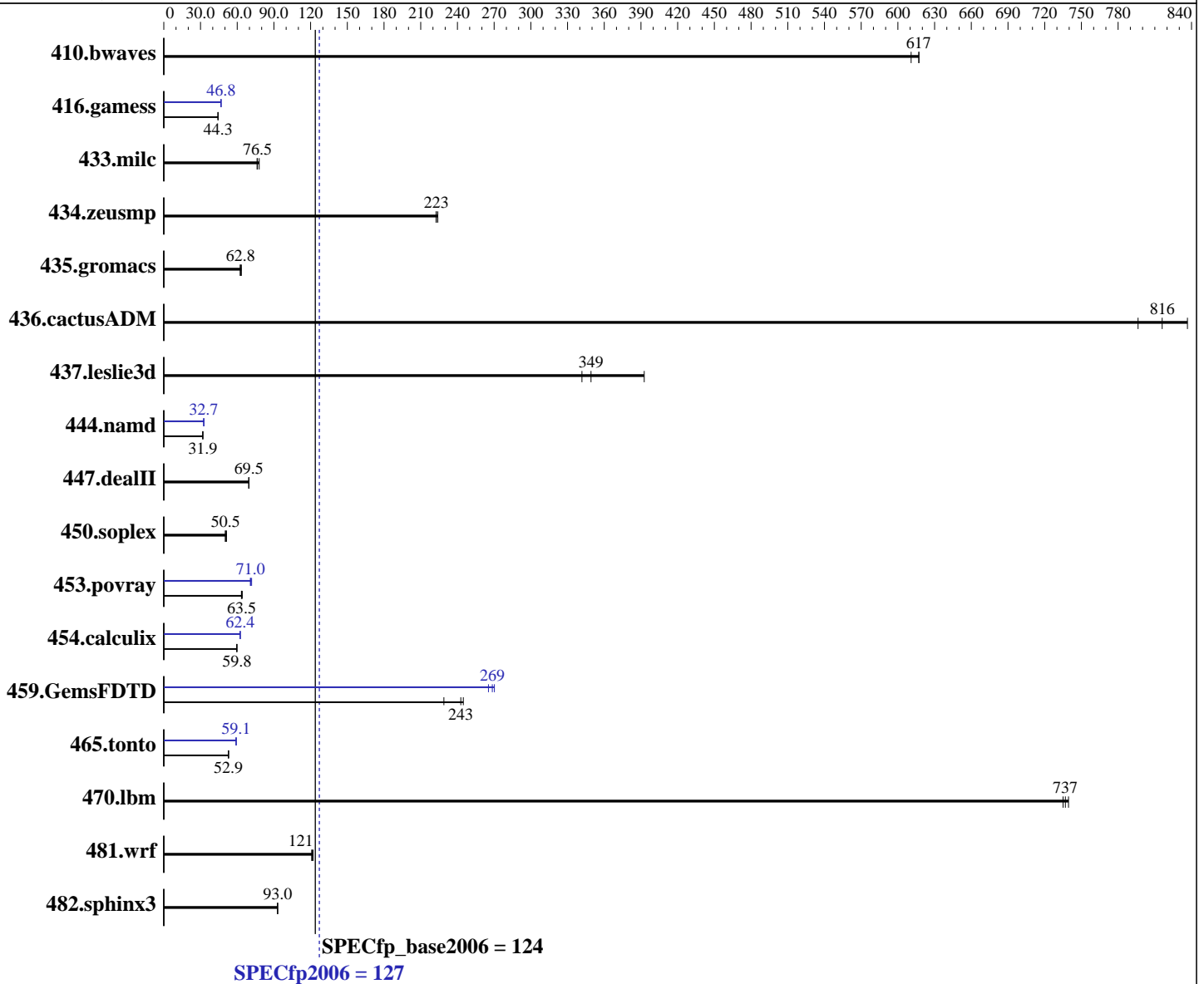
Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2667 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP1
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SAS SSD, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.2	611	22.0	617	<u>22.0</u>	<u>617</u>	22.2	611	22.0	617	<u>22.0</u>	<u>617</u>
416.gamess	442	44.3	441	44.4	<u>442</u>	<u>44.3</u>	419	46.7	418	46.8	<u>418</u>	<u>46.8</u>
433.milc	118	78.0	120	76.2	<u>120</u>	<u>76.5</u>	118	78.0	120	76.2	<u>120</u>	<u>76.5</u>
434.zeusmp	40.9	223	40.6	224	<u>40.8</u>	<u>223</u>	40.9	223	40.6	224	<u>40.8</u>	<u>223</u>
435.gromacs	<u>114</u>	<u>62.8</u>	114	62.4	113	63.4	<u>114</u>	<u>62.8</u>	114	62.4	113	63.4
436.cactusADM	<u>14.6</u>	<u>816</u>	14.3	837	15.0	796	<u>14.6</u>	<u>816</u>	14.3	837	15.0	796
437.leslie3d	27.5	342	<u>26.9</u>	<u>349</u>	23.9	393	27.5	342	<u>26.9</u>	<u>349</u>	23.9	393
444.namd	<u>251</u>	<u>31.9</u>	252	31.9	251	31.9	246	32.6	245	32.7	<u>245</u>	<u>32.7</u>
447.dealII	<u>165</u>	<u>69.5</u>	164	69.6	165	69.4	<u>165</u>	<u>69.5</u>	164	69.6	165	69.4
450.soplex	163	51.3	166	50.2	<u>165</u>	<u>50.5</u>	163	51.3	166	50.2	<u>165</u>	<u>50.5</u>
453.povray	83.9	63.4	82.9	64.1	<u>83.7</u>	<u>63.5</u>	74.1	71.8	<u>74.9</u>	<u>71.0</u>	75.2	70.7
454.calculix	138	59.8	<u>138</u>	<u>59.8</u>	138	59.6	132	62.5	132	62.4	<u>132</u>	<u>62.4</u>
459.GemsFDTD	43.3	245	46.3	229	<u>43.7</u>	<u>243</u>	40.0	265	<u>39.5</u>	<u>269</u>	39.3	270
465.tonto	<u>186</u>	<u>52.9</u>	186	53.0	187	52.8	<u>166</u>	<u>59.1</u>	167	59.0	166	59.2
470.lbm	<u>18.6</u>	<u>737</u>	18.6	739	18.7	735	<u>18.6</u>	<u>737</u>	18.6	739	18.7	735
481.wrf	<u>92.0</u>	<u>121</u>	92.4	121	91.5	122	<u>92.0</u>	<u>121</u>	92.4	121	91.5	122
482.sphinx3	210	93.0	210	93.0	<u>210</u>	<u>93.0</u>	210	93.0	210	93.0	<u>210</u>	<u>93.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
Intel Hyperthreading Option set to Enabled
HP Power Profile set to Custom
HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C1E State
Minimum Processor Idle Power Package C-State set to No Package State
QPI Snoop Configuration set to Home Snoop
Collaborative Power Control set to Disabled

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Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Energy Performance Bias set to Maximum Performance

Sysinfo program

```
/home/specuser/cpu2006/HP_build_ic16_suite_corrected_int_bins/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-szds Wed Mar 2 08:35:32 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2667 v4 @ 3.20GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 16
  physical 0: cores 0 2 3 4 8 10 11 12
  physical 1: cores 0 2 3 4 8 10 11 12
 cache size : 25600 KB
```

From /proc/meminfo

```
MemTotal:      529093800 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

/usr/bin/lsb_release -d

```
SUSE Linux Enterprise Server 12 SP1
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

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Platform Notes (Continued)

```
Linux linux-szds 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 2 08:34
```

SPEC is set to:

```
/home/specuser/cpu2006/HP_build_icl6_suite_corrected_int_bins/cpu2006
```

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   703G  185G  519G  27% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P92 02/22/2016

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

OMP_NUM_THREADS = "16"

LD_LIBRARY_PATH = "/home/specuser/cpu2006/HP_build_icl6_suite_corrected_int_bins/cpu2006/lib64:/home/specuser/cpu2006/HP_build_icl6_suite_corrected_int_bins/cpu2006/lib64:/home/specuser/cpu2006/HP_build_icl6_suite_corrected_int_bins/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB

memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

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Peak Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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