# SPEC® CFP2006 Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

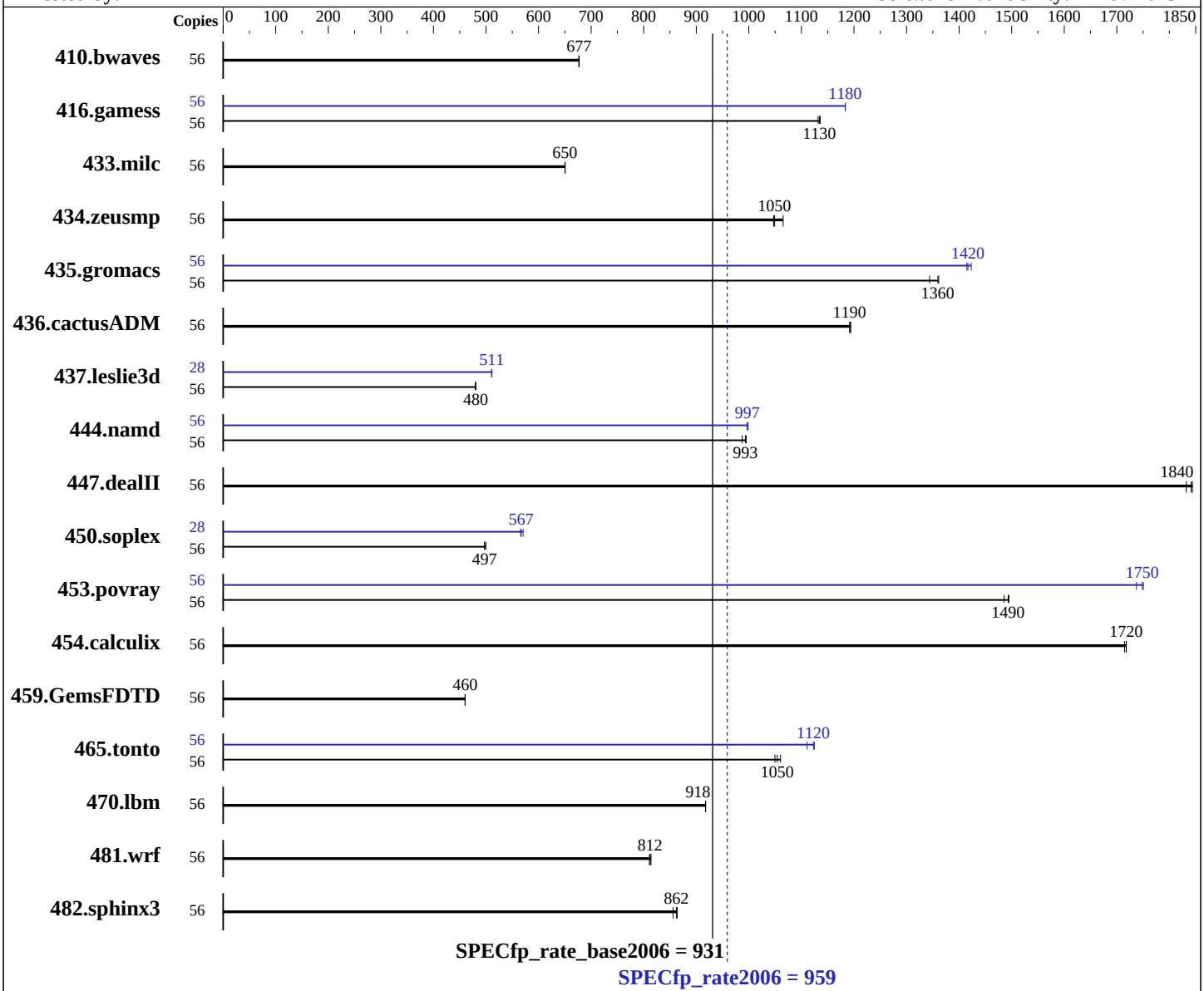
ProLiant DL380 Gen9
(2.60 GHz, Intel Xeon E5-2690 v4)

SPECfp®_rate2006 = 959

SPECfp_rate_base2006 = 931

**CPU2006 license:** 3
**Test sponsor:** HPE
**Tested by:** HPE

**Test date:** Mar-2016
**Hardware Availability:** Mar-2016
**Software Availability:** Nov-2015




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2690 v4 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.50 GHz |
| CPU MHz: | 2600 |
| FPU: | Integrated |
| CPU(s) enabled: | 28 cores, 2 chips, 14 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 7.2, (Maipo)<br>Kernel 3.10.0-327.el7.x86_64 |
| Compiler: | C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;<br>Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | xfs |

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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 2 x 500 GB SAS HDD 10 K, RAID 1
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 56 | 1125 | 677 | 1124 | 677 | **1124** | **677** | 56 | 1125 | 677 | 1124 | 677 | **1124** | **677** |
| 416.gamess | 56 | 965 | 1140 | 969 | 1130 | **967** | **1130** | 56 | **927** | **1180** | 927 | 1180 | 926 | 1180 |
| 433.milc | 56 | 791 | 650 | 790 | 651 | **790** | **650** | 56 | 791 | 650 | 790 | 651 | **790** | **650** |
| 434.zeusmp | 56 | 478 | 1070 | **486** | **1050** | 487 | 1050 | 56 | 478 | 1070 | **486** | **1050** | 487 | 1050 |
| 435.gromacs | 56 | 294 | 1360 | 298 | 1340 | **294** | **1360** | 56 | 281 | 1420 | 283 | 1410 | **282** | **1420** |
| 436.cactusADM | 56 | **562** | **1190** | 562 | 1190 | 560 | 1190 | 56 | **562** | **1190** | 562 | 1190 | 560 | 1190 |
| 437.leslie3d | 56 | 1098 | 480 | **1096** | **480** | 1096 | 480 | 28 | 516 | 511 | 515 | 511 | **515** | **511** |
| 444.namd | 56 | 451 | 995 | **452** | **993** | 455 | 987 | 56 | 450 | 999 | **450** | **997** | 451 | 996 |
| 447.dealII | 56 | **348** | **1840** | 347 | 1840 | 350 | 1830 | 56 | **348** | **1840** | 347 | 1840 | 350 | 1830 |
| 450.soplex | 56 | 934 | 500 | **939** | **497** | 940 | 497 | 28 | 409 | 571 | **412** | **567** | 413 | 566 |
| 453.povray | 56 | **199** | **1490** | 201 | 1490 | 199 | 1490 | 56 | 171 | 1740 | 170 | 1750 | **170** | **1750** |
| 454.calculix | 56 | **269** | **1720** | 269 | 1720 | 269 | 1710 | 56 | **269** | **1720** | 269 | 1720 | 269 | 1710 |
| 459.GemsFDTD | 56 | **1291** | **460** | 1291 | 460 | 1291 | 460 | 56 | **1291** | **460** | 1291 | 460 | 1291 | 460 |
| 465.tonto | 56 | 525 | 1050 | 520 | 1060 | **523** | **1050** | 56 | 490 | 1120 | **491** | **1120** | 496 | 1110 |
| 470.lbm | 56 | **838** | **918** | 838 | 918 | 839 | 917 | 56 | **838** | **918** | 838 | 918 | 839 | 917 |
| 481.wrf | 56 | **770** | **812** | 769 | 814 | 771 | 811 | 56 | **770** | **812** | 769 | 814 | 771 | 811 |
| 482.sphinx3 | 56 | **1266** | **862** | 1275 | 856 | 1264 | 864 | 56 | **1266** | **862** | 1275 | 856 | 1264 | 864 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 >      /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

## Platform Notes

```
BIOS Configuration:
Intel Hyperthreading Option set to Enabled
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C1E State
Minimum Processor Idle Power Package C-State set to No Package State
Collaborative Power Control set to Disabled
QPI Snoop Configuration set to Cluster on Die
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpuv1.3/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on DL380-Gen9-B Tue Mar  1 09:14:08 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2690 v4@ 2.60GHz
      2 "physical id"s (chips)
      56 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 14
      siblings  : 28
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
   cache size : 17920 KB

From /proc/meminfo
   MemTotal:       528063236 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.2 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.2"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
```

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## Platform Notes (Continued)

```
      Linux DL380-Gen9-B 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Mar 1 08:54

   SPEC is set to: /home/cpuv1.3/cpu2006
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda5      xfs   318G   21G  297G   7% /home
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS HP P89 02/22/2016
     Memory:
      8x UNKNOWN NOT AVAILABLE
      16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

   (End of data from sysinfo program)
   Regarding the sysinfo display about the memory installed, the correct amount of
   memory is 512 GB and the dmidecode description should have one line reading as:
   16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpuv1.3/cpu2006/libs/32:/home/cpuv1.3/cpu2006/libs/64:/home/cpuv1.3/cpu2006/sh"
 Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB
 memory using RedHat EL 7.2
```

# Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

Continued on next page

## Peak Compiler Invocation (Continued)

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -static(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -static(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -static(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -static(pass 2) -prof-use(pass 2)
-unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -static(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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## Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml`


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