Supermicro

SuperStorage Server 5048R-E1CR36L
(X10SRH-CLN4F, Intel Xeon E5-2699 v4)

**Specfp®2006 = 121**

**Specfp_base2006 = 115**

<table>
<thead>
<tr>
<th>Test sponsor:</th>
<th>Supermicro</th>
<th>Test date:</th>
<th>Apr-2016</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Mar-2016</td>
<td>Software Availability:</td>
<td>Sep-2015</td>
</tr>
</tbody>
</table>

CPU2006 license: 001176
Tested by: Supermicro

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<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Result</th>
</tr>
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<tbody>
<tr>
<td>410.bwaves</td>
<td>47.8</td>
</tr>
<tr>
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<td>37.7</td>
</tr>
<tr>
<td>433.milc</td>
<td>86.5</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>270</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>47.7</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>918</td>
</tr>
<tr>
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<td>502</td>
</tr>
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</tr>
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</tr>
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</tr>
<tr>
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<td>71.8</td>
</tr>
<tr>
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<td>63.8</td>
</tr>
<tr>
<td>459.GemsFD-pad</td>
<td>56.5</td>
</tr>
<tr>
<td>465.tonto</td>
<td>58.7</td>
</tr>
<tr>
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<td>42.9</td>
</tr>
<tr>
<td>481.wrf</td>
<td>484</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>71.2</td>
</tr>
</tbody>
</table>

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**Hardware**

| CPU Name: | Intel Xeon E5-2699 v4 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.60 GHz |
| CPU MHz: | 2200 |
| FPU: | Integrated |
| CPU(s) enabled: | 22 cores, 1 chip, 22 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

---

**Software**

| Operating System: | SUSE Linux Enterprise Server 12 SP1, Kernel 3.12.49-11-default |
| Compiler: | C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |

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Continued on next page
**Supermicro**

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**SPEC CFP2006 Result**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base Pointers</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak Pointers</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>36.8</td>
<td>369</td>
<td>36.6</td>
<td>36.6</td>
<td>372</td>
<td>36.6</td>
<td>36.6</td>
<td>372</td>
</tr>
<tr>
<td>416.gameess</td>
<td>520</td>
<td>37.7</td>
<td>520</td>
<td>37.7</td>
<td>372</td>
<td>410</td>
<td>410</td>
<td>47.8</td>
</tr>
<tr>
<td>433.milee</td>
<td>106</td>
<td>86.9</td>
<td>106</td>
<td>86.5</td>
<td>372</td>
<td>106</td>
<td>106</td>
<td>86.5</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>33.7</td>
<td>270</td>
<td>33.8</td>
<td>270</td>
<td>372</td>
<td>33.7</td>
<td>106</td>
<td>86.5</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>150</td>
<td>47.7</td>
<td>150</td>
<td>47.6</td>
<td>372</td>
<td>150</td>
<td>150</td>
<td>47.6</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>13.2</td>
<td>903</td>
<td>13.0</td>
<td>918</td>
<td>372</td>
<td>13.2</td>
<td>13.0</td>
<td>918</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>18.8</td>
<td>501</td>
<td>18.7</td>
<td>502</td>
<td>372</td>
<td>18.8</td>
<td>18.7</td>
<td>502</td>
</tr>
<tr>
<td>444.namd</td>
<td>253</td>
<td>31.7</td>
<td>253</td>
<td>31.7</td>
<td>372</td>
<td>245</td>
<td>246</td>
<td>32.7</td>
</tr>
<tr>
<td>447.dealII</td>
<td>164</td>
<td>69.8</td>
<td>164</td>
<td>69.9</td>
<td>372</td>
<td>164</td>
<td>164</td>
<td>69.9</td>
</tr>
<tr>
<td>450.soplex</td>
<td>158</td>
<td>52.9</td>
<td>158</td>
<td>52.9</td>
<td>372</td>
<td>158</td>
<td>158</td>
<td>52.9</td>
</tr>
<tr>
<td>453.povray</td>
<td>81.5</td>
<td>65.3</td>
<td>83.4</td>
<td>63.8</td>
<td>372</td>
<td>74.1</td>
<td>74.1</td>
<td>73.1</td>
</tr>
<tr>
<td>454.calculix</td>
<td>146</td>
<td>56.6</td>
<td>146</td>
<td>56.5</td>
<td>372</td>
<td>136</td>
<td>135</td>
<td>61.2</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>54.5</td>
<td>195</td>
<td>54.5</td>
<td>195</td>
<td>372</td>
<td>50.4</td>
<td>50.4</td>
<td>210</td>
</tr>
<tr>
<td>465.tonto</td>
<td>229</td>
<td>42.9</td>
<td>228</td>
<td>43.1</td>
<td>372</td>
<td>228</td>
<td>228</td>
<td>42.7</td>
</tr>
<tr>
<td>470.lbm</td>
<td>28.4</td>
<td>484</td>
<td>28.3</td>
<td>486</td>
<td>372</td>
<td>28.4</td>
<td>28.4</td>
<td>486</td>
</tr>
<tr>
<td>481.wrf</td>
<td>87.1</td>
<td>128</td>
<td>86.8</td>
<td>129</td>
<td>372</td>
<td>87.1</td>
<td>128</td>
<td>86.8</td>
</tr>
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<td>274</td>
<td>71.2</td>
<td>274</td>
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<td>372</td>
<td>274</td>
<td>274</td>
<td>71.2</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Platform Notes**

BIOS Settings:
- Early Snoop = Disable
- Enforce POR = Disabled
- Memory Frequency = 2400

Sysinfo program /home/cpu2006/config/sysinfo.rev6914

$Rev: 6914 $ $Date:: 2014-06-25 $ $e3fbb8667b5a285932ceab81e28219e1
running on linux-ueh7 Wed Apr 13 09:48:41 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: Continued on next page
Supermicro
SuperStorage Server 5048R-E1CR36L
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SPECfp2006 = 121
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Hardware Availability: Mar-2016
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Platform Notes (Continued)

http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
  1 "physical id"s (chips)
  44 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 22
siblings : 44
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
  cache size : 56320 KB

From /proc/meminfo
  MemTotal: 132181516 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 1
    # This file is deprecated and will be removed in a future service pack or
    release.
    # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
  (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 13 09:45

SPEC is set to: /home/cpu2006
  Filesystem Type  Size  Used Avail Use% Mounted on
  /dev/sda3      xfs  145G  5.0G  140G  4% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

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Platform Notes (Continued)

BIOS American Megatrends Inc. 2.0 12/17/2015
Memory:
8x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "22"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags
SPEC CFP2006 Result

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Tested by: Supermicro

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Hardware Availability: Mar-2016
Software Availability: Sep-2015

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
            -auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
            -ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml

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For other inquiries, please contact webmaster@spec.org.

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