



# SPEC® CINT2006 Result

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## Lenovo Group Limited

**SPECint®2006 = 36.0**

Lenovo System x3550 M5  
(1.70 GHz, Intel Xeon E5-2603 v4)

**SPECint\_base2006 = 34.8**

CPU2006 license: 9017

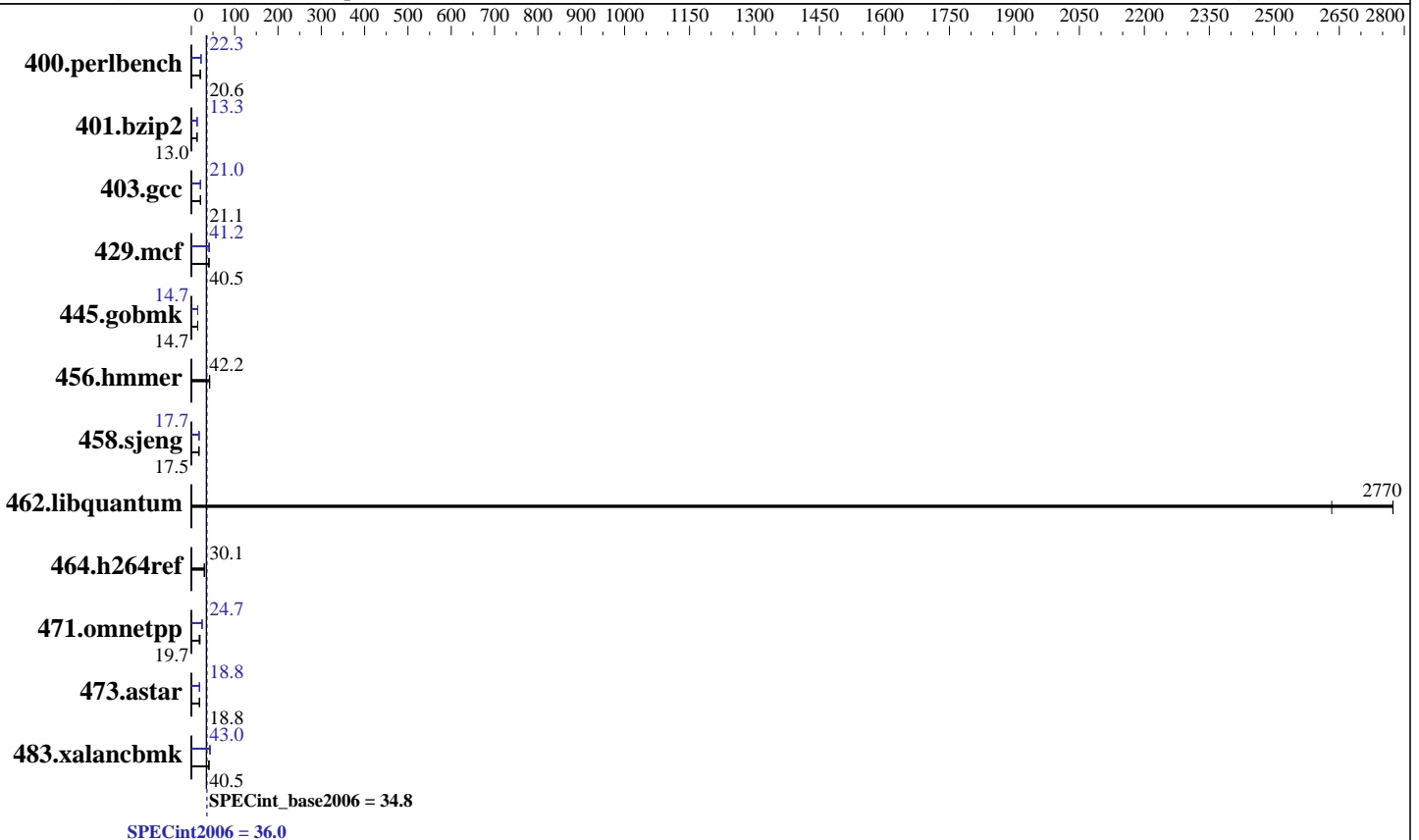
Test date: Apr-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2603 v4  
 CPU Characteristics: 1700  
 CPU MHz: 1700  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 1866 MHz)  
 Disk Subsystem: 1 x 800 GB SATA SSD  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64)  
 Kernel 3.12.28-4-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	473	20.6	<b><u>474</u></b>	<b><u>20.6</u></b>	474	20.6	<b><u>439</u></b>	<b><u>22.3</u></b>	439	22.3	439	22.3
401.bzip2	741	13.0	<b><u>740</u></b>	<b><u>13.0</u></b>	738	13.1	725	13.3	727	13.3	<b><u>726</u></b>	<b><u>13.3</u></b>
403.gcc	382	21.1	381	21.1	<b><u>382</u></b>	<b><u>21.1</u></b>	384	21.0	383	21.0	<b><u>383</u></b>	<b><u>21.0</u></b>
429.mcf	226	40.4	223	40.9	<b><u>225</u></b>	<b><u>40.5</u></b>	219	41.6	225	40.5	<b><u>221</u></b>	<b><u>41.2</u></b>
445.gobmk	715	14.7	712	14.7	<b><u>714</u></b>	<b><u>14.7</u></b>	714	14.7	714	14.7	<b><u>714</u></b>	<b><u>14.7</u></b>
456.hammer	222	42.0	221	42.2	<b><u>221</u></b>	<b><u>42.2</u></b>	222	42.0	221	42.2	<b><u>221</u></b>	<b><u>42.2</u></b>
458.sjeng	691	17.5	691	17.5	<b><u>691</u></b>	<b><u>17.5</u></b>	683	17.7	683	17.7	<b><u>683</u></b>	<b><u>17.7</u></b>
462.libquantum	<b><u>7.47</u></b>	<b><u>2770</u></b>	7.87	2630	7.47	2770	<b><u>7.47</u></b>	<b><u>2770</u></b>	7.87	2630	7.47	2770
464.h264ref	736	30.1	<b><u>735</u></b>	<b><u>30.1</u></b>	735	30.1	736	30.1	<b><u>735</u></b>	<b><u>30.1</u></b>	735	30.1
471.omnetpp	317	19.7	348	17.9	<b><u>317</u></b>	<b><u>19.7</u></b>	253	24.7	254	24.6	<b><u>254</u></b>	<b><u>24.7</u></b>
473.astar	373	18.8	375	18.7	<b><u>374</u></b>	<b><u>18.8</u></b>	373	18.8	374	18.8	<b><u>373</u></b>	<b><u>18.8</u></b>
483.xalancbmk	170	40.5	170	40.6	<b><u>170</u></b>	<b><u>40.5</u></b>	<b><u>161</u></b>	<b><u>43.0</u></b>	160	43.0	161	42.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:

Operating Mode set to Maximum Performance

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on linux-gj7w Wed Apr 6 17:35:50 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2603 v4@ 1.70GHz

2 "physical id"s (chips)

12 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 6

siblings : 6

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info@spec.org

<http://www.spec.org/>

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### Platform Notes (Continued)

```
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      263830844 kB
HugePages_Total:      0
Hugepagesize:   2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 0
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12"
  VERSION_ID="12"
  PRETTY_NAME="SUSE Linux Enterprise Server 12"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux linux-gj7w 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 6 17:31
```

```
SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   703G  11G  693G   2% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[TBE123H-2.10]- 03/25/2016

Memory:

8x NO DIMM Unknown

16x Samsung M393A2G40DB1-CRC 16 GB 2 rank 2400 MHz, configured at 1867 MHz

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmarthheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

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## Peak Optimization Flags (Continued)

401.bzip2 (continued):

-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc

-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel

-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2)

-opt-ra-region-strategy=block

-ansi-alias

-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Default-Platform-Flags.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/Default-Platform-Flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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