Huawei

Huawei XH628 V3 (Intel Xeon E5-2695 v4)

<table>
<thead>
<tr>
<th>SPECint®2006</th>
<th>NC</th>
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<tbody>
<tr>
<td>SPECint_base2006</td>
<td>NC</td>
</tr>
</tbody>
</table>

CPU2006 license: 3175  Test date:  Mar-2016
Test sponsor: Huawei  Hardware Availability: Mar-2016
Tested by: Huawei  Software Availability: Aug-2015

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<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>3.10.0-123.1.el7.x86_64</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux</td>
</tr>
<tr>
<td>FPU:</td>
<td>Auto Parallel: Yes</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>Base Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>Peak Pointers: 32/64-bit</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>Other Software: Microquill SmartHeap V10.2</td>
</tr>
<tr>
<td>Other Cache:</td>
<td></td>
</tr>
<tr>
<td>Memory:</td>
<td></td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td></td>
</tr>
<tr>
<td>Other Hardware:</td>
<td></td>
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</tbody>
</table>
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Peak Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
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<tr>
<td>401.bzip2</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
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<td>403.gcc</td>
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<td>NC</td>
<td>NC</td>
<td>NC</td>
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<td>429.mcf</td>
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<td>445.gobmk</td>
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<td>NC</td>
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<tr>
<td>456.hmmer</td>
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<td>458.sjeng</td>
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<td>462.libquantum</td>
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<td>471.omnetpp</td>
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<td>473.astar</td>
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<td>483.xalancbmk</td>
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<td>NC</td>
<td>NC</td>
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<td>NC</td>
<td>NC</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localhost Mon Mar 7 20:38:35 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
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Platform Notes (Continued)

http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2695 v4 @ 2.10GHz
2 "physical id"s (chips)
36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 18
siblings : 18
physical 0: cores 0 1 2 3 4 8 9 10 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB

From /proc/meminfo
MemTotal: 263568856 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
cpe:/o:redhat:enterprise_linux:7.0:ga:server
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 7 05:14

SPEC is set to: /spec16

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 449G 139G 311G 31% /

Additional information from dmidecode:

Continued on next page
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Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.09 02/17/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "36"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleaf=all runspec <etc>
The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.
The results have been measured on a Huawei XH620 V3 model.

Base Compiler Invocation

C benchmarks:
icc -m64
C++ benchmarks:
icpc -m64
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Non-Compliant
Huawei

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SPEC CINT2006 Result
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Peak Compiler Invocation (Continued)

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipо(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-alias
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipо(pass 2) -03(pass 2) -no-prec-div -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias
483.xalancbmk: -xCORE-AVX2 -ipo -03 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32
3.gcc: -xCORE-AVX2 -ipo -03 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32
429.mcf: -xCORE-AVX2 -ipo -03 -no-prec-div -parallel
-opt-prefetch -auto-p32
445.gobmk: basepeak = yes

Non-Compliant

Continued on next page
SPEC CINT2006 Result

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Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 2)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes
464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2)
           -opt-ra-region-strategy=block -ansi-alias
           -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
           -auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
                -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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