SPEC® CFP2006 Result

Hewlett Packard Enterprise

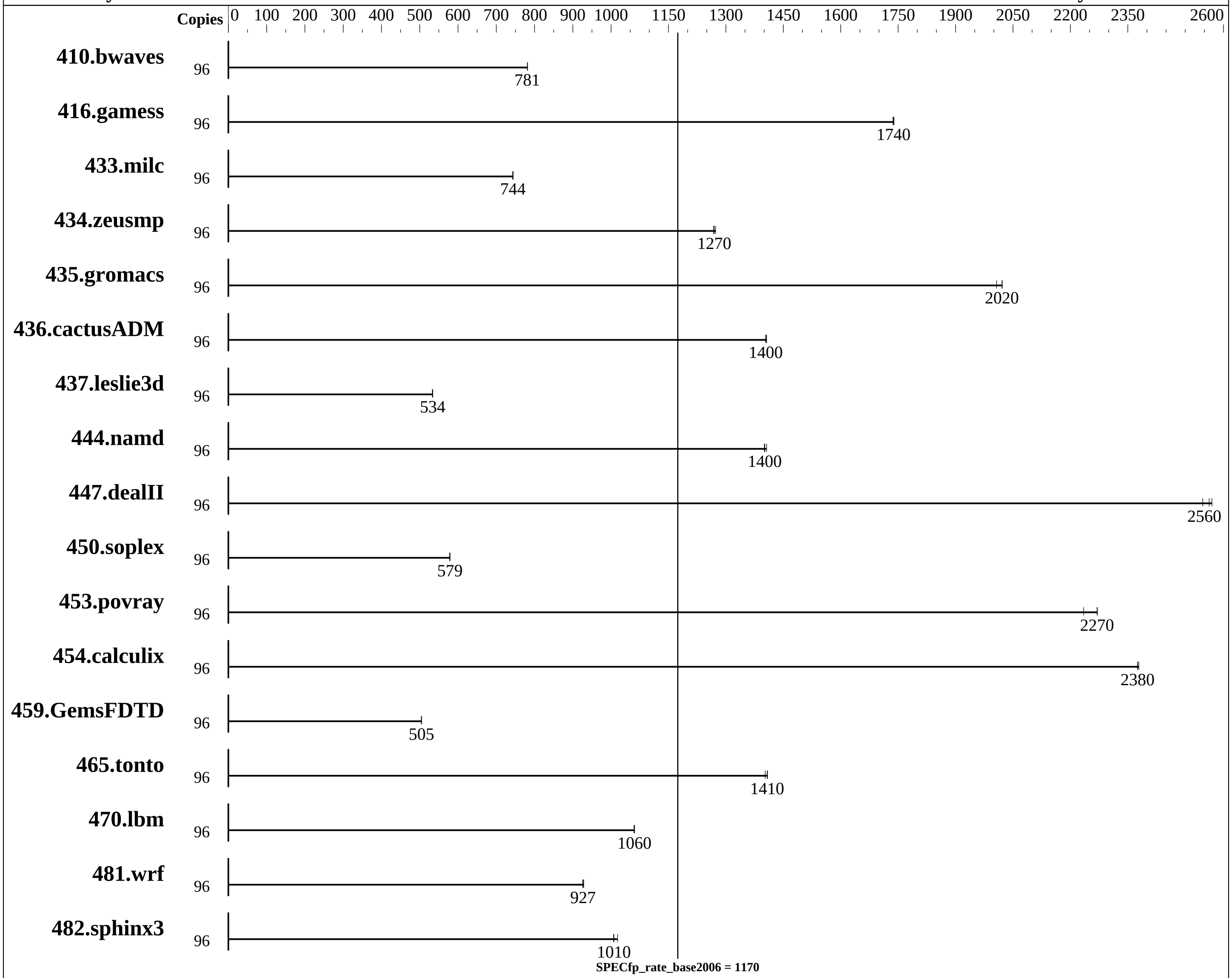
(Test Sponsor: HPE)

ProLiant DL580 Gen9
(2.20 GHz, Intel Xeon E7-8890 v4)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 1170

CPU2006 license: 3		**Test date:**	May-2016
Test sponsor:	HPE	**Hardware Availability:**	Jun-2016
Tested by:	HPE	**Software Availability:**	Dec-2015



Hardware

CPU Name:	Intel Xeon E7-8890 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	48 cores, 2 chips, 24 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 12 SP1 (x86_64) Kernel 3.12.49-11-default
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	btrfs
System State:	Run level 3 (multi-user)

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L3 Cache: 60 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 1600 MHz)
Disk Subsystem: 1 x 400 GB SSD, RAID 0
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	96	**1670**	**781**	1669	782	1670	781							
416.gamess	96	1083	1740	**1081**	**1740**	1081	1740							
433.milc	96	1186	743	1185	744	**1185**	**744**							
434.zeusmp	96	686	1270	689	1270	**688**	**1270**							
435.gromacs	96	**339**	**2020**	339	2020	342	2010							
436.cactusADM	96	816	1410	817	1400	**817**	**1400**							
437.leslie3d	96	1690	534	1693	533	**1690**	**534**							
444.namd	96	548	1410	**549**	**1400**	550	1400							
447.dealII	96	431	2550	427	2570	**429**	**2560**							
450.soplex	96	1385	578	1383	579	**1383**	**579**							
453.povray	96	229	2230	**225**	**2270**	225	2270							
454.calculix	96	333	2380	333	2380	**333**	**2380**							
459.GemsFDTD	96	**2018**	**505**	2018	505	2021	504							
465.tonto	96	**671**	**1410**	671	1410	674	1400							
470.lbm	96	1245	1060	**1243**	**1060**	1243	1060							
481.wrf	96	**1157**	**927**	1155	928	1158	926							
482.sphinx3	96	1859	1010	**1857**	**1010**	1840	1020							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

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Platform Notes

```
BIOS Configuration:
  HP Power Profile set to Custom
  HP Power Regulator to HP Static High Performance Mode
  Minimum Processor Idle Power Core C-State set to C6 State
  Minimum Processor Idle Power Package C-State set to No Package State
  QPI Snoop Configuration set to Cluster on Die
  Collaborative Power Control set to Disabled
  Thermal Configuration set to Maximum Cooling
  Processor Power and Utilization Monitoring set to Disabled

Sysinfo program /cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-hr3s Wed May 11 21:19:48 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-8890 v4 @ 2.20GHz
      2 "physical id"s (chips)
      96 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 24
      siblings  : 48
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
      27 28 29
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
      27 28 29
   cache size : 61440 KB

From /proc/meminfo
   MemTotal:       264550196 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
```

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Platform Notes (Continued)

```
        VERSION_ID="12.1"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp1"

  uname -a:
     Linux linux-hr3s 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
     (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 May 11 14:40

  SPEC is set to: /cpu2006
     Filesystem     Type   Size  Used Avail Use% Mounted on
     /dev/sda3      btrfs  371G   45G  325G  13% /
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS HP U17 04/26/2016
    Memory:
     80x UNKNOWN NOT AVAILABLE
     16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz

  (End of data from sysinfo program)
  Regarding the sysinfo display about the memory installed, the correct amount of
  memory is 256 GB and the dmidecode description should have one line reading as:
  16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

 Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB
 memory using RedHat EL 7.2
```

Base Compiler Invocation

C benchmarks:
    icc -m64

C++ benchmarks:
    icpc -m64

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Base Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves:	-DSPEC_CPU_LP64
416.gamess:	-DSPEC_CPU_LP64
433.milc:	-DSPEC_CPU_LP64
434.zeusmp:	-DSPEC_CPU_LP64
435.gromacs:	-DSPEC_CPU_LP64 -nofor_main
436.cactusADM:	-DSPEC_CPU_LP64 -nofor_main
437.leslie3d:	-DSPEC_CPU_LP64
444.namd:	-DSPEC_CPU_LP64
447.dealII:	-DSPEC_CPU_LP64
450.soplex:	-DSPEC_CPU_LP64
453.povray:	-DSPEC_CPU_LP64
454.calculix:	-DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD:	-DSPEC_CPU_LP64
465.tonto:	-DSPEC_CPU_LP64
470.lbm:	-DSPEC_CPU_LP64
481.wrf:	-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3:	-DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch
-qopt-prefetch-issue-excl-hint -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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