SPEC® CFP2006 Result

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Dell Inc.

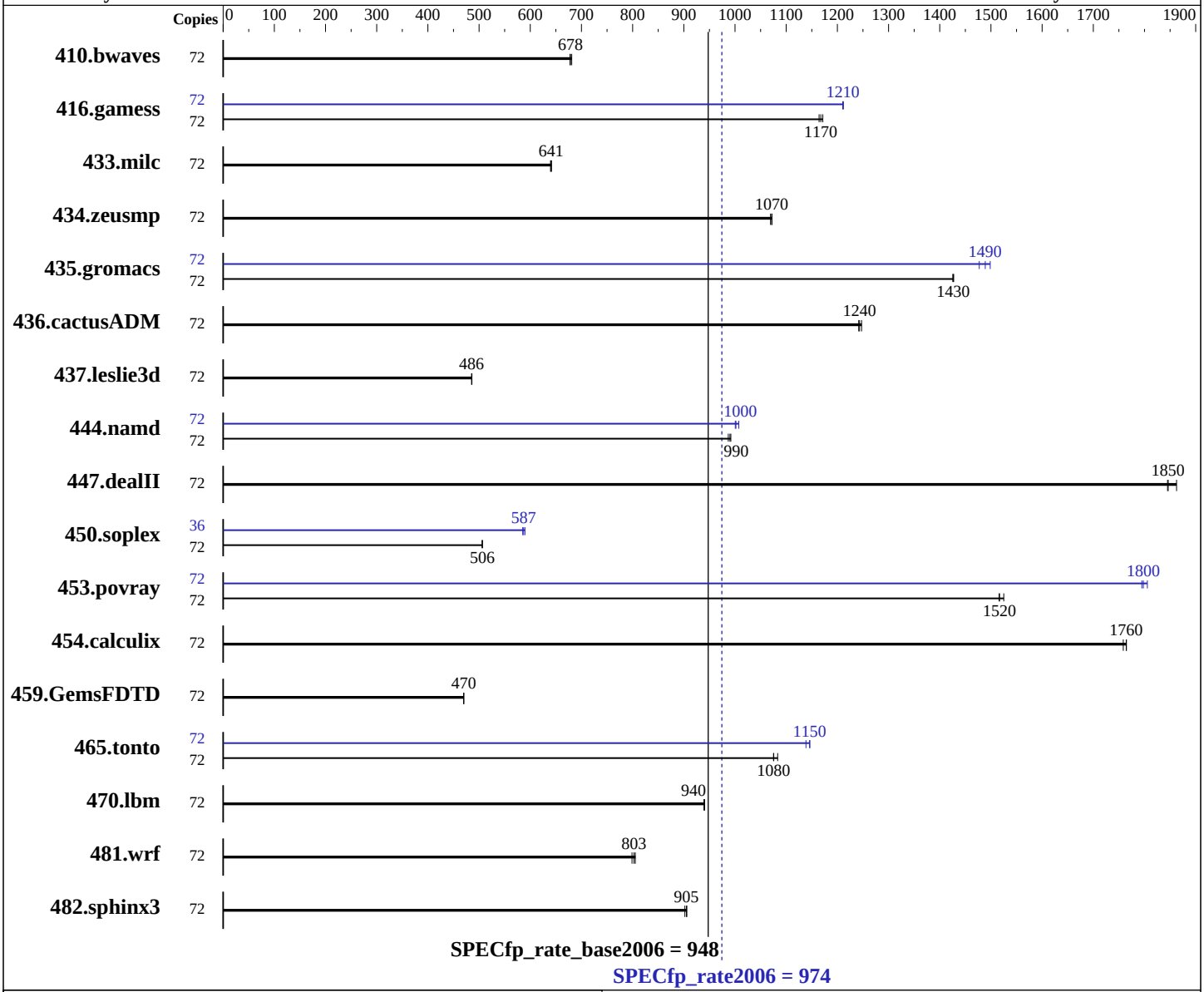
PowerEdge R430 (Intel Xeon E5-2695 v4, 2.10 GHz)

SPECfp®_rate2006 = 974

SPECfp_rate_base2006 = 948

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: May-2016
Hardware Availability: Jun-2016
Software Availability: Dec-2015



Hardware

CPU Name:	Intel Xeon E5-2695 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	36 cores, 2 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
Compiler:	C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)

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L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 250 GB 7200 RPM SATA HDD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	72	1437	681	1444	678	**1442**	**678**	72	1437	681	1444	678	**1442**	**678**
416.gamess	72	1203	1170	1211	1160	**1207**	**1170**	72	1165	1210	**1164**	**1210**	1163	1210
433.milc	72	1033	640	1030	641	**1032**	**641**	72	1033	640	1030	641	**1032**	**641**
434.zeusmp	72	613	1070	**611**	**1070**	611	1070	72	613	1070	**611**	**1070**	611	1070
435.gromacs	72	**360**	**1430**	361	1430	360	1430	72	343	1500	348	1480	**345**	**1490**
436.cactusADM	72	**692**	**1240**	690	1250	693	1240	72	**692**	**1240**	690	1250	693	1240
437.leslie3d	72	1393	486	1394	486	**1394**	**486**	72	1393	486	1394	486	**1394**	**486**
444.namd	72	582	992	**583**	**990**	585	987	72	577	1000	573	1010	**576**	**1000**
447.dealII	72	446	1850	**446**	**1850**	442	1860	72	446	1850	**446**	**1850**	442	1860
450.soplex	72	1187	506	**1186**	**506**	1186	506	36	**511**	**587**	513	585	509	590
453.povray	72	**253**	**1520**	253	1520	251	1530	72	212	1810	213	1790	**213**	**1800**
454.calculix	72	338	1760	337	1760	**337**	**1760**	72	338	1760	337	1760	**337**	**1760**
459.GemsFDTD	72	1625	470	**1625**	**470**	1626	470	72	1625	470	**1625**	**470**	1626	470
465.tonto	72	659	1080	**659**	**1080**	654	1080	72	622	1140	618	1150	**618**	**1150**
470.lbm	72	1053	939	**1052**	**940**	1052	940	72	1053	939	**1052**	**940**	1052	940
481.wrf	72	999	805	1007	799	**1002**	**803**	72	999	805	1007	799	**1002**	**803**
482.sphinx3	72	**1550**	**905**	1556	902	1549	906	72	**1550**	**905**	1556	902	1549	906

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS settings:
Snoop Mode set to Cluster on Die
Virtualization Technology disabled
```

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Platform Notes (Continued)

```
System Profile set to custom
CPU Power Management set to Hardware P States
C States set to Autonomous
C1E disabled
Energy Efficient Turbo disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Balanced Performance
Memory Patrol Scrub disabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-n1xa Wed May 25 08:08:13 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2695 v4 @ 2.10GHz
      2 "physical id"s (chips)
      72 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 18
      siblings  : 36
      physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
   cache size : 23040 KB

From /proc/meminfo
   MemTotal:       264436480 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
```

Continued on next page

Platform Notes (Continued)

```
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux linux-n1xa 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 24 19:22

SPEC is set to: /root/cpu2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext4  221G  8.7G  212G   4% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Dell Inc. 2.0.1 04/11/2016
  Memory:
   8x 00CE00B300CE M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz
   4x Not Specified Not Specified

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

 Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB
 memory using RedHat EL 7.2 glibc 2.17
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Continued on next page

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml

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