# SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Fujitsu

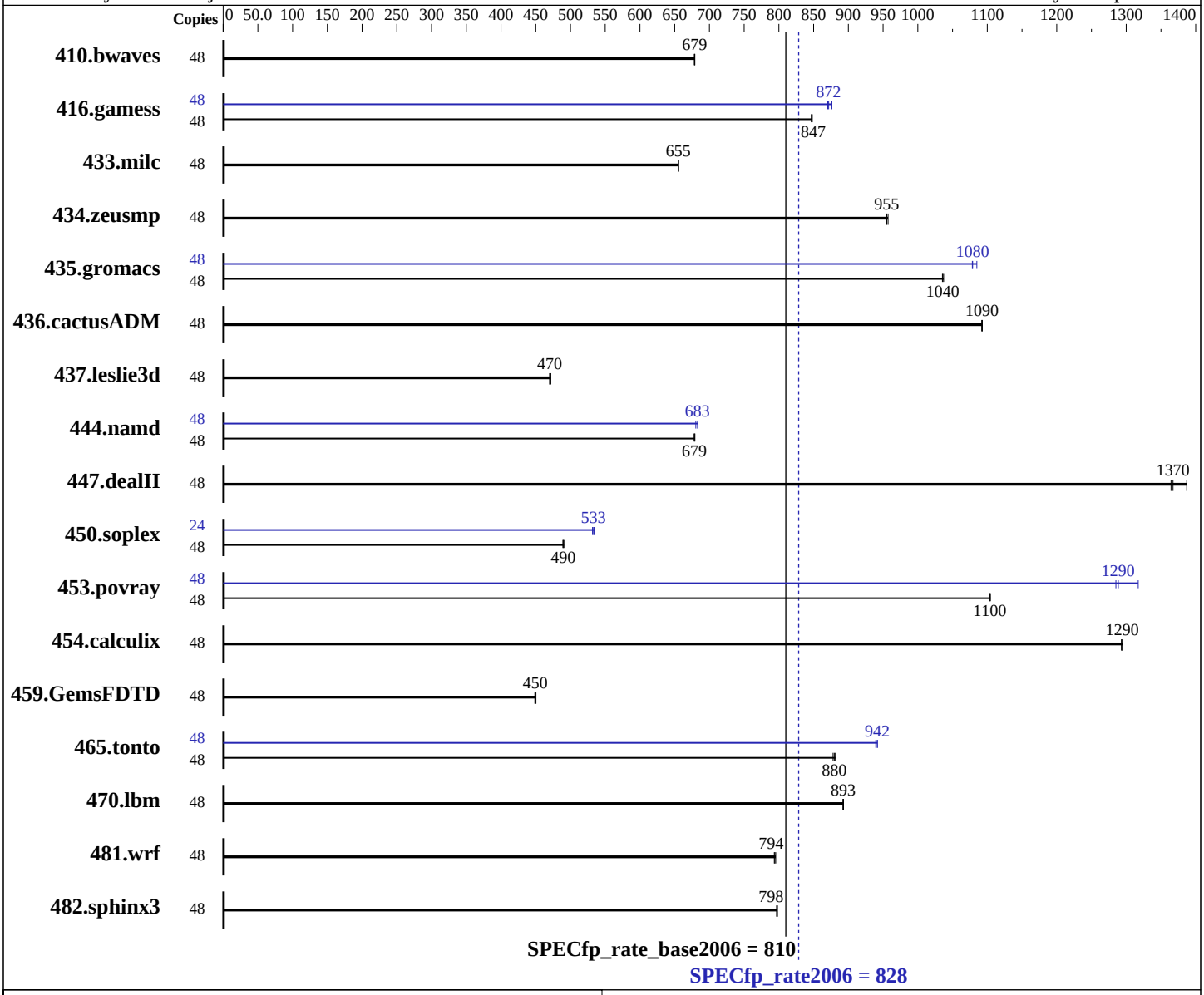
PRIMERGY BX2580 M2, Intel Xeon E5-2650 v4, 2.20 GHz

SPECfp®_rate2006 = 828

SPECfp_rate_base2006 = 810

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Jul-2016
**Hardware Availability:** Apr-2016
**Software Availability:** Sep-2015




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2650 v4 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.90 GHz |
| CPU MHz: | 2200 |
| FPU: | Integrated |
| CPU(s) enabled: | 24 cores, 2 chips, 12 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

Continued on next page

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 SP1 (x86_64) Kernel 3.12.49-11-default |
| Compiler: | C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |

Continued on next page

# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY BX2580 M2, Intel Xeon E5-2650 v4, 2.20 GHz

SPECfp_rate2006 = 828

SPECfp_rate_base2006 = 810

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Jul-2016
**Hardware Availability:** Apr-2016
**Software Availability:** Sep-2015

L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x SATA, 1000 GB, 7200 RPM
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 48 | 961 | 679 | **961** | **679** | 962 | 678 | 48 | 961 | 679 | **961** | **679** | 962 | 678 |
| 416.gamess | 48 | **1110** | **847** | 1110 | 847 | 1108 | 848 | 48 | **1078** | **872** | 1080 | 870 | 1073 | 876 |
| 433.milc | 48 | **672** | **655** | 673 | 655 | 672 | 656 | 48 | **672** | **655** | 673 | 655 | 672 | 656 |
| 434.zeusmp | 48 | **457** | **955** | 458 | 954 | 456 | 957 | 48 | **457** | **955** | 458 | 954 | 456 | 957 |
| 435.gromacs | 48 | **331** | **1040** | 330 | 1040 | 331 | 1040 | 48 | **318** | **1080** | 318 | 1080 | 316 | 1080 |
| 436.cactusADM | 48 | 525 | 1090 | 525 | 1090 | **525** | **1090** | 48 | 525 | 1090 | 525 | 1090 | **525** | **1090** |
| 437.leslie3d | 48 | **960** | **470** | 957 | 472 | 960 | 470 | 48 | **960** | **470** | 957 | 472 | 960 | 470 |
| 444.namd | 48 | 567 | 679 | **567** | **679** | 568 | 678 | 48 | 566 | 680 | 563 | 683 | **564** | **683** |
| 447.dealII | 48 | 396 | 1390 | **402** | **1370** | 402 | 1360 | 48 | 396 | 1390 | **402** | **1370** | 402 | 1360 |
| 450.soplex | 48 | 816 | 491 | 819 | 489 | **818** | **490** | 24 | 375 | 534 | 376 | 532 | **375** | **533** |
| 453.povray | 48 | **231** | **1100** | 231 | 1100 | 231 | 1100 | 48 | 199 | 1290 | 194 | 1320 | **198** | **1290** |
| 454.calculix | 48 | 306 | 1290 | 306 | 1290 | **306** | **1290** | 48 | 306 | 1290 | 306 | 1290 | **306** | **1290** |
| 459.GemsFDTD | 48 | 1131 | 450 | 1135 | 449 | **1133** | **450** | 48 | 1131 | 450 | 1135 | 449 | **1133** | **450** |
| 465.tonto | 48 | **537** | **880** | 536 | 881 | 538 | 878 | 48 | 503 | 939 | **502** | **942** | 502 | 942 |
| 470.lbm | 48 | **739** | **893** | 739 | 893 | 739 | 892 | 48 | **739** | **893** | 739 | 893 | 739 | 892 |
| 481.wrf | 48 | **675** | **794** | 676 | 793 | 674 | 795 | 48 | **675** | **794** | 676 | 793 | 674 | 795 |
| 482.sphinx3 | 48 | **1173** | **798** | 1174 | 797 | 1173 | 798 | 48 | **1173** | **798** | 1174 | 797 | 1173 | 798 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced
```

Continued on next page

## Platform Notes (Continued)

```
QPI snoop mode: Cluster on Die
    COD Enable = Enabled, Early Snoop = Disabled, Home Snoop Dir OSB = Disabled
CPU C1E Support = Disabled
Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-vaud Sat Jul  9 01:09:20 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
      2 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       264315052 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux linux-vaud 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

Continued on next page

## Platform Notes (Continued)

```
 run-level 3 Jul 8 13:42 last=5

 SPEC is set to: /home/SPECcpu2006
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda3      xfs   331G   46G  285G  14% /home
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.3.0 for D3321-B1x
   02/19/2016
   Memory:
    16x Micron Technology 36ASF2G72PZ-2G3A3 16 GB 2 rank 2400 MHz

 (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 For information about Fujitsu please visit: http://www.fujitsu.com
```

# Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Continued on next page

## Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

Continued on next page

# Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevB.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Aug 24 13:12:54 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 August 2016.