



SPEC® CFP2006 Result

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ACTION S.A.

ACTINA SOLAR 220 X6 (Intel Xeon E5-2603 v4, 1.70 GHz)

SPECfp®_rate2006 = 358

SPECfp_rate_base2006 = 350

CPU2006 license: 9008

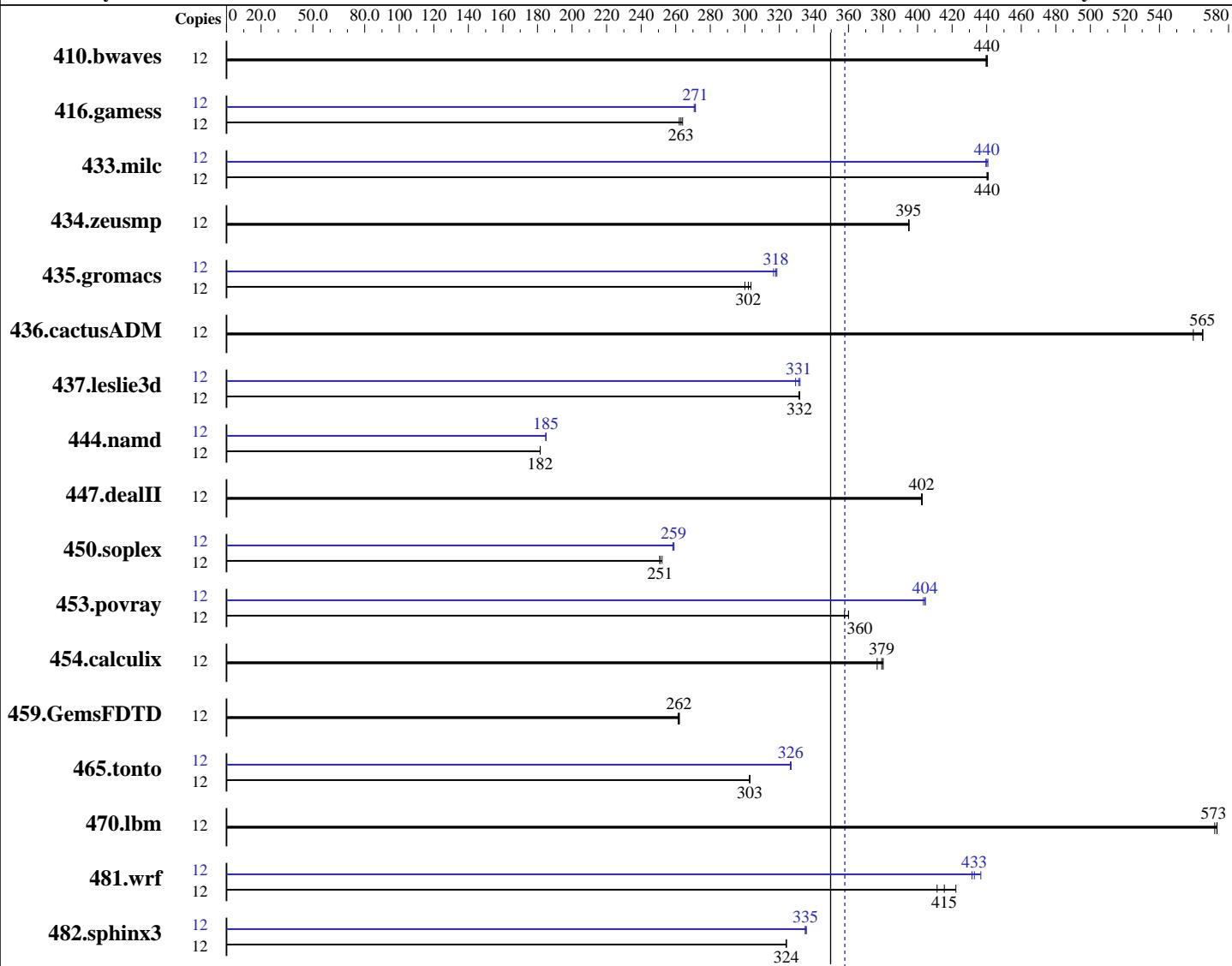
Test date: Sep-2016

Test sponsor: ACTION S.A.

Hardware Availability: Mar-2016

Tested by: ACTION S.A.

Software Availability: Mar-2016



SPECfp_rate_base2006 = 350

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Hardware

CPU Name: Intel Xeon E5-2603 v4
 CPU Characteristics:
 CPU MHz:
 FPU:
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 Compiler: 3.10.0-327.18.2.el7.x86_64
 C/C++: Version 16.0.3.210 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.3.210 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache:	15 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 240 GB SATA II SSD	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	371	440	<u>371</u>	<u>440</u>	370	440	12	371	440	<u>371</u>	<u>440</u>	370	440
416.gamess	12	890	264	<u>893</u>	<u>263</u>	897	262	12	868	271	<u>866</u>	<u>271</u>	866	271
433.milc	12	<u>250</u>	<u>440</u>	250	441	250	440	12	<u>250</u>	<u>440</u>	251	440	250	441
434.zeusmp	12	276	395	<u>277</u>	<u>395</u>	277	395	12	276	395	<u>277</u>	<u>395</u>	277	395
435.gromacs	12	<u>284</u>	<u>302</u>	282	304	286	300	12	269	319	271	317	<u>269</u>	<u>318</u>
436.cactusADM	12	256	560	<u>254</u>	<u>565</u>	254	565	12	256	560	<u>254</u>	<u>565</u>	254	565
437.leslie3d	12	340	332	341	331	<u>340</u>	<u>332</u>	12	<u>341</u>	<u>331</u>	342	329	340	332
444.namd	12	530	182	530	182	<u>530</u>	<u>182</u>	12	521	185	<u>521</u>	<u>185</u>	520	185
447.dealII	12	341	403	341	402	<u>341</u>	<u>402</u>	12	341	403	341	402	<u>341</u>	<u>402</u>
450.soplex	12	399	251	397	252	<u>399</u>	<u>251</u>	12	<u>387</u>	<u>259</u>	387	258	386	259
453.povray	12	177	360	<u>177</u>	<u>360</u>	178	358	12	158	404	<u>158</u>	<u>404</u>	158	403
454.calculix	12	263	377	260	380	<u>261</u>	<u>379</u>	12	263	377	260	380	<u>261</u>	<u>379</u>
459.GemsFDTD	12	<u>486</u>	<u>262</u>	487	261	486	262	12	<u>486</u>	<u>262</u>	487	261	486	262
465.tonto	12	390	303	390	303	<u>390</u>	<u>303</u>	12	361	327	362	326	<u>362</u>	<u>326</u>
470.lbm	12	288	572	<u>288</u>	<u>573</u>	288	573	12	288	572	<u>288</u>	<u>573</u>	288	573
481.wrf	12	326	411	317	422	<u>323</u>	<u>415</u>	12	307	437	311	431	<u>310</u>	<u>433</u>
482.sphinx3	12	722	324	<u>722</u>	<u>324</u>	722	324	12	698	335	697	336	<u>698</u>	<u>335</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Bios Settings:

Power & Performance = Performance

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Platform Notes (Continued)

```
Enforce POR = Disabled
Memory Operating Speed Selection = Auto
Set FAN Profile = Performance
Fan PWM Offset = 0
```

```
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on SUT Fri Sep 23 05:09:51 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2603 v4 @ 1.70GHz
        2 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 6
        siblings   : 6
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      263860780 kB
HugePages_Total:       1
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.2 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.2"
    PRETTY_NAME="Red Hat Enterprise Linux"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
os-release.rpmsave:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
Continued on next page
```



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Platform Notes (Continued)

```
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
Linux SUT 3.10.0-327.18.2.el7.x86_64 #2 SMP Wed Jun 1 17:37:13 CEST 2016
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 21 10:10

SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sdal      ext4  212G  118G   84G  59%  /

Additional information from dmidecode:
BIOS Intel Corporation SE5C610.86B.11.01.0136.062220161656 06/22/2016
Memory:
 16x 16 GB
 16x Hynix HMA42GR7AFR4N-TF 16 GB 1866 MHz 2 rank
 8x NO DIMM NO DIMM

(End of data from sysinfo program)
dmidecode does not properly detect memory modules
16 modules of 16 GB were used to run the test (256 GB total)
```

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Binaries compiled on a system with 2x Xeon E5-2650 v4 chips + 256 GB memory
using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2)
            -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
            -auto-ilp32

```

470.lbm: basepeak = yes

```

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
            -unroll2

```

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.xml>

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