



# SPEC® CFP2006 Result

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**Huawei**

**SPECfp\_rate2006 = 1130**

**Huawei CH220 V3 (Intel Xeon E5-2699 v4)**

**SPECfp\_rate\_base2006 = 1090**

**CPU2006 license:** 3175

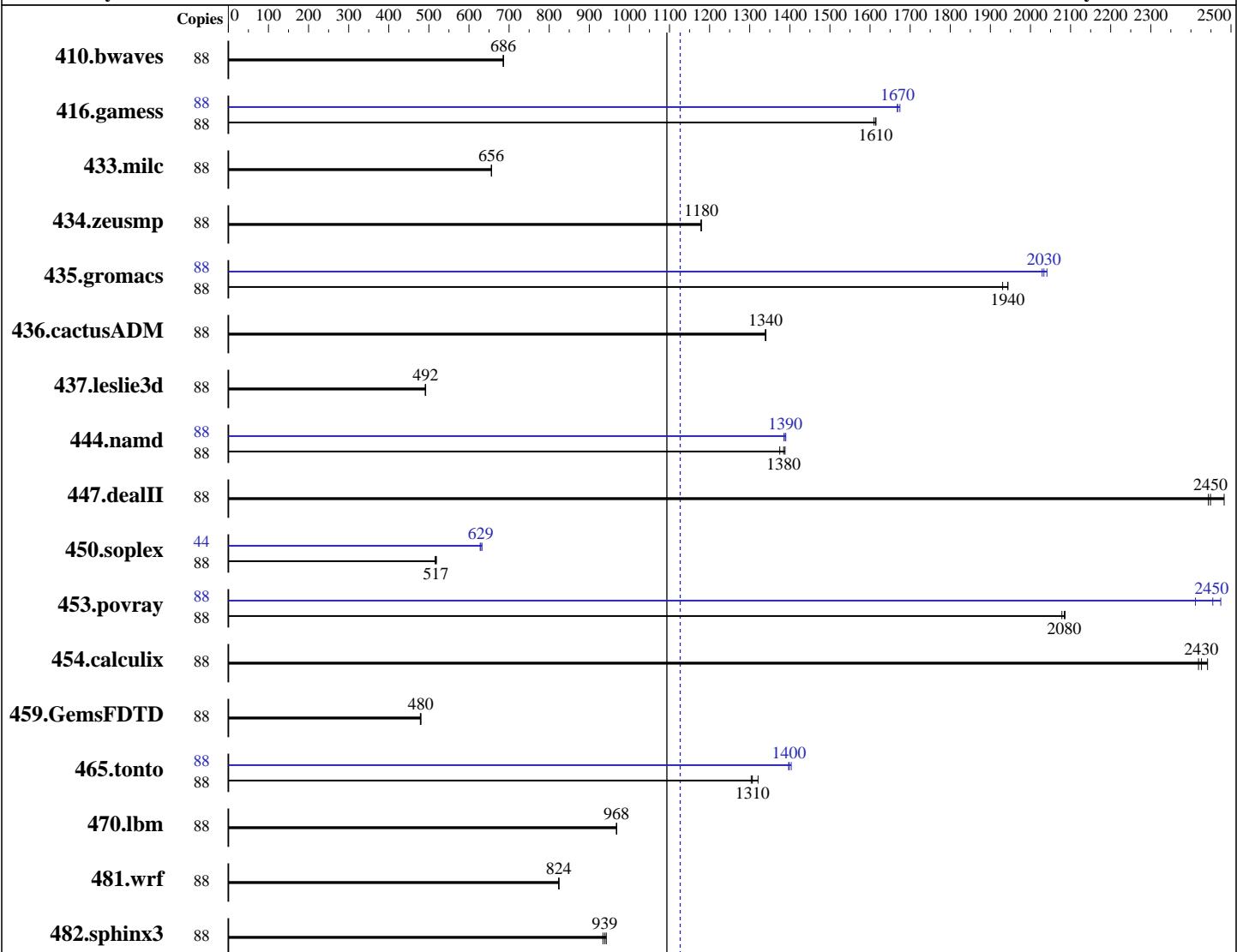
**Test date:** Nov-2016

**Test sponsor:** Huawei

**Hardware Availability:** Apr-2016

**Tested by:** Huawei

**Software Availability:** Dec-2015



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## Hardware

CPU Name: Intel Xeon E5-2699 v4  
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
CPU MHz: 2200  
FPU: Integrated  
CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
Auto Parallel: No  
File System: xfs  
System State: Run level 3 (multi-user)

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L3 Cache: 55 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	88	<b><u>1743</u></b>	<b><u>686</u></b>	1746	685	1743	686	88	<b><u>1743</u></b>	<b><u>686</u></b>	1746	685	1743	686
416.gamess	88	1070	1610	1067	1610	<b><u>1068</u></b>	<b><u>1610</u></b>	88	1029	1670	1033	1670	<b><u>1032</u></b>	<b><u>1670</u></b>
433.milc	88	1231	656	1231	656	<b><u>1231</u></b>	<b><u>656</u></b>	88	1231	656	1231	656	<b><u>1231</u></b>	<b><u>656</u></b>
434.zeusmp	88	<b><u>679</u></b>	<b><u>1180</u></b>	679	1180	679	1180	88	<b><u>679</u></b>	<b><u>1180</u></b>	679	1180	679	1180
435.gromacs	88	<b><u>323</u></b>	<b><u>1940</u></b>	323	1940	325	1930	88	310	2030	<b><u>309</u></b>	<b><u>2030</u></b>	308	2040
436.cactusADM	88	785	1340	785	1340	<b><u>785</u></b>	<b><u>1340</u></b>	88	785	1340	785	1340	<b><u>785</u></b>	<b><u>1340</u></b>
437.leslie3d	88	1681	492	<b><u>1682</u></b>	<b><u>492</u></b>	1685	491	88	1681	492	<b><u>1682</u></b>	<b><u>492</u></b>	1685	491
444.namd	88	508	1390	<b><u>510</u></b>	<b><u>1380</u></b>	513	1370	88	509	1390	508	1390	<b><u>508</u></b>	<b><u>1390</u></b>
447.dealII	88	<b><u>411</u></b>	<b><u>2450</u></b>	412	2440	405	2480	88	<b><u>411</u></b>	<b><u>2450</u></b>	412	2440	405	2480
450.soplex	88	<b><u>1418</u></b>	<b><u>517</u></b>	1415	519	1423	516	44	<b><u>580</u></b>	<b><u>632</u></b>	584	628	<b><u>583</u></b>	<b><u>629</u></b>
453.povray	88	<b><u>225</u></b>	<b><u>2080</u></b>	224	2090	225	2080	88	194	2410	<b><u>191</u></b>	<b><u>2450</u></b>	189	2470
454.calculix	88	<b><u>299</u></b>	<b><u>2430</u></b>	300	2420	297	2440	88	<b><u>299</u></b>	<b><u>2430</u></b>	300	2420	297	2440
459.GemsFDTD	88	<b><u>1947</u></b>	<b><u>480</u></b>	1950	479	1945	480	88	<b><u>1947</u></b>	<b><u>480</u></b>	1950	479	1945	480
465.tonto	88	656	1320	664	1300	<b><u>663</u></b>	<b><u>1310</u></b>	88	<b><u>619</u></b>	<b><u>1400</u></b>	617	1400	620	1400
470.lbm	88	<b><u>1249</u></b>	<b><u>968</u></b>	1249	968	1249	968	88	<b><u>1249</u></b>	<b><u>968</u></b>	1249	968	1249	968
481.wrf	88	<b><u>1193</u></b>	<b><u>824</u></b>	1193	824	1192	824	88	<b><u>1193</u></b>	<b><u>824</u></b>	1193	824	1192	824
482.sphinx3	88	1836	934	<b><u>1827</u></b>	<b><u>939</u></b>	1820	942	88	<b><u>1836</u></b>	<b><u>934</u></b>	<b><u>1827</u></b>	<b><u>939</u></b>	1820	942

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Performance  
 Set Snoop Mode to COD mode

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## Platform Notes (Continued)

Set Patrol Scrub to Disable

Sysinfo program /spec16/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1

running on linux-6392 Fri Nov 11 01:15:31 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz

2 "physical id"s (chips)

88 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 22

siblings : 44

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27

28

physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27

28

cache size : 28160 KB

From /proc/meminfo

MemTotal: 528839004 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

SUSE Linux Enterprise Server 12 SP1

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 1

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP1"

VERSION\_ID="12.1"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP1"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:

Linux linux-6392 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015

(8d714a0) x86\_64 x86\_64 x86\_64 GNU/Linux

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## Platform Notes (Continued)

run-level 3 Nov 9 08:24 last=5

SPEC is set to: /spec16

Filesystem Type Size Used Avail Use% Mounted on  
/dev/md126p2 xfs 455G 22G 434G 5% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

16x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

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## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

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## Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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