SPEC® CFP2006 Result

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NEC Corporation

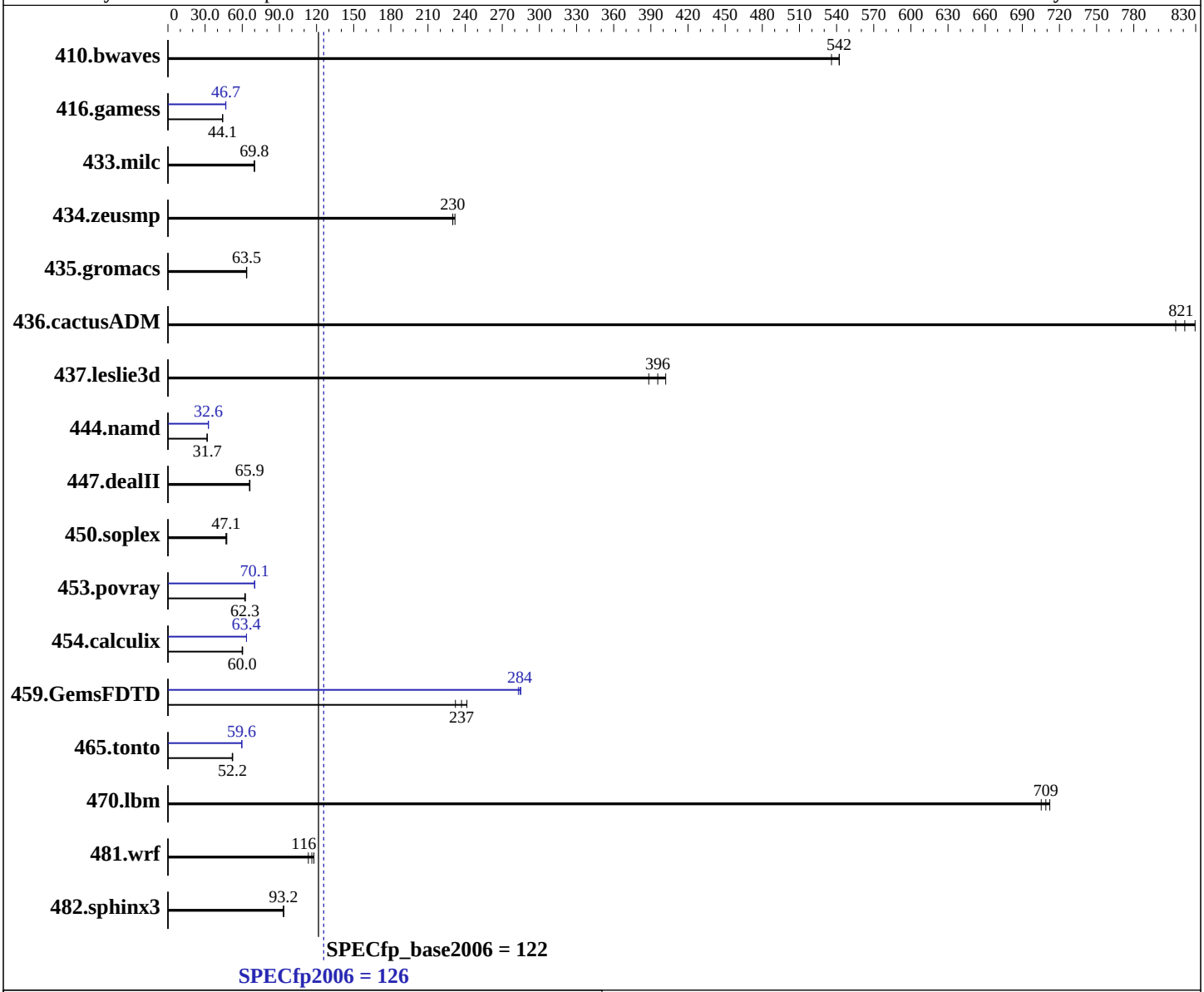
Express5800/B120g-h (Intel Xeon E5-2667 v4)

SPECfp®2006 = 126

SPECfp_base2006 = 122

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Mar-2016
Hardware Availability: Dec-2016
Software Availability: Jan-2016



Hardware

CPU Name:	Intel Xeon E5-2667 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo) Kernel 3.10.0-327.4.5.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: NEC Storage M310 via Fibre Channel (See additional details below)
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	25.1	542	**25.1**	**542**	25.4	536	25.1	542	**25.1**	**542**	25.4	536
416.gamess	**444**	**44.1**	445	44.0	443	44.2	**419**	**46.7**	420	46.6	419	46.7
433.milc	131	69.9	**132**	**69.8**	132	69.8	131	69.9	**132**	**69.8**	132	69.8
434.zeusmp	**39.6**	**230**	39.6	230	39.3	232	**39.6**	**230**	39.6	230	39.3	232
435.gromacs	113	63.4	**112**	**63.5**	112	63.6	113	63.4	**112**	**63.5**	112	63.6
436.cactusADM	**14.6**	**821**	14.4	830	14.7	814	**14.6**	**821**	14.4	830	14.7	814
437.leslie3d	24.2	388	**23.8**	**396**	23.4	402	24.2	388	**23.8**	**396**	23.4	402
444.namd	253	31.6	253	31.7	**253**	**31.7**	245	32.7	246	32.6	**246**	**32.6**
447.dealII	173	66.2	**174**	**65.9**	174	65.9	173	66.2	**174**	**65.9**	174	65.9
450.soplex	175	47.5	**177**	**47.1**	178	46.9	175	47.5	**177**	**47.1**	178	46.9
453.povray	85.0	62.6	85.8	62.0	**85.4**	**62.3**	75.8	70.2	76.2	69.8	**75.8**	**70.1**
454.calculix	138	60.0	137	60.3	**137**	**60.0**	130	63.5	**130**	**63.4**	130	63.4
459.GemsFDTD	45.7	232	**44.8**	**237**	43.9	241	**37.3**	**284**	37.2	285	37.5	283
465.tonto	188	52.3	**189**	**52.2**	190	51.9	165	59.6	**165**	**59.6**	165	59.7
470.lbm	**19.4**	**709**	19.3	712	19.5	705	**19.4**	**709**	19.3	712	19.5	705
481.wrf	**96.1**	**116**	98.5	113	94.8	118	**96.1**	**116**	98.5	113	94.8	118
482.sphinx3	209	93.2	208	93.6	**209**	**93.2**	209	93.2	208	93.6	**209**	**93.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
 Energy Performance: Performance
 Patrol Scrub: Disabled
 Snoop Mode: Home Snoop
 Hyper-Threading: Disabled

Storage Configuration for Disk Subsystem:
 NEC Storage M310 has 4 x 600 GB 10000 RPM SAS disks under RAID-10 configuration
 mounted over 8Gbps Fibre Channel interface with these options
```

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Platform Notes (Continued)

```
"defaults" in the /etc/fstab.
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-B120g-RevB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-B120g-RevB.xml`

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