



# SPEC® CINT2006 Result

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## Cisco Systems

Cisco UCS B420 M4 (Intel Xeon E5-4669 v4 2.20 GHz)

**SPECint®2006 = 65.5**

**SPECint\_base2006 = 63.5**

**CPU2006 license:** 9019

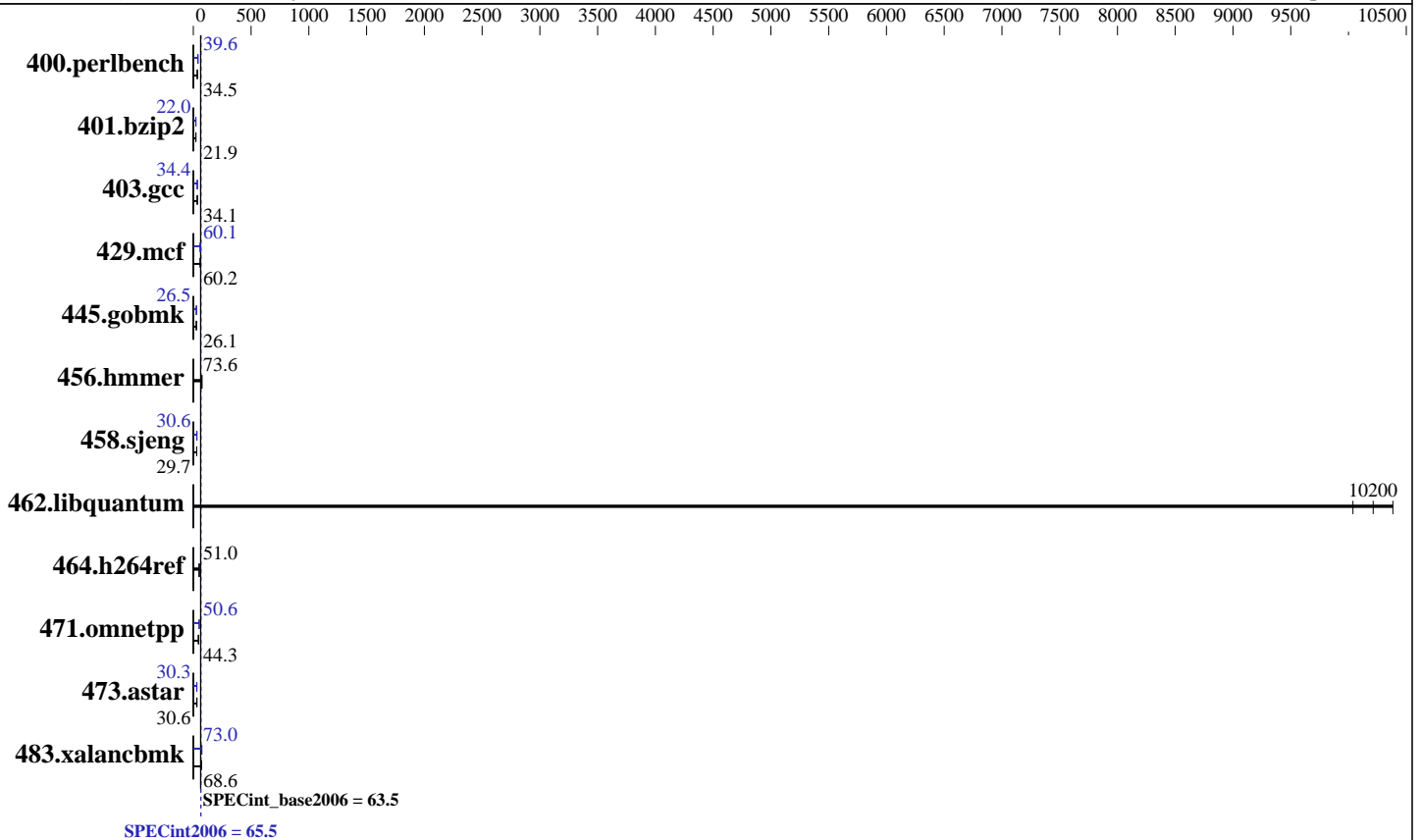
**Test sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test date:** Feb-2017

**Hardware Availability:** Jun-2016

**Software Availability:** Sep-2016



### Hardware

**CPU Name:** Intel Xeon E5-4669 v4  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.00 GHz  
**CPU MHz:** 2200  
**FPU:** Integrated  
**CPU(s) enabled:** 88 cores, 4 chips, 22 cores/chip  
**CPU(s) orderable:** 2,4 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 55 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 1 TB (32 x 32 GB 2Rx4 PC4-2400T-R)  
**Disk Subsystem:** 1 x 300 GB SAS, 15K RPM  
**Other Hardware:** None

### Software

**Operating System:** SUSE Linux Enterprise Server 12 SP1 (x86\_64) 3.12.49-11-default  
**Compiler:** C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux  
**Auto Parallel:** Yes  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	284	34.5	<b>283</b>	<b>34.5</b>	283	34.6	247	39.6	<b>246</b>	<b>39.6</b>	245	39.8
401.bzip2	439	22.0	441	21.9	<b>440</b>	<b>21.9</b>	<b>438</b>	<b>22.0</b>	438	22.0	438	22.0
403.gcc	236	34.2	<b>236</b>	<b>34.1</b>	236	34.1	<b>234</b>	<b>34.4</b>	234	34.4	235	34.3
429.mcf	152	60.1	<b>152</b>	<b>60.2</b>	150	60.9	151	60.4	152	60.1	<b>152</b>	<b>60.1</b>
445.gobmk	<b>402</b>	<b>26.1</b>	402	26.1	401	26.2	396	26.5	<b>396</b>	<b>26.5</b>	395	26.5
456.hammer	127	73.6	<b>127</b>	<b>73.6</b>	126	73.8	127	73.6	<b>127</b>	<b>73.6</b>	126	73.8
458.sjeng	406	29.8	<b>408</b>	<b>29.7</b>	408	29.6	395	30.6	395	30.7	<b>395</b>	<b>30.6</b>
462.libquantum	2.00	10400	<b>2.03</b>	<b>10200</b>	2.06	10000	2.00	10400	<b>2.03</b>	<b>10200</b>	2.06	10000
464.h264ref	434	51.0	434	51.0	<b>434</b>	<b>51.0</b>	434	51.0	434	51.0	<b>434</b>	<b>51.0</b>
471.omnetpp	139	44.8	<b>141</b>	<b>44.3</b>	143	43.8	123	50.7	<b>123</b>	<b>50.6</b>	124	50.6
473.astar	229	30.7	230	30.5	<b>230</b>	<b>30.6</b>	233	30.2	231	30.4	<b>232</b>	<b>30.3</b>
483.xalancbmk	101	68.1	100	68.9	<b>101</b>	<b>68.6</b>	94.5	73.0	<b>94.5</b>	<b>73.0</b>	94.3	73.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

### BIOS Settings:

```

Intel Hyper-Threading Technology option set to Disabled
CPU performance set to Enterprise
Power Technology set to Energy Efficient
Energy Performance set to Balanced Performance
Memory RAS configuration set to Maximum Performance
Memory Power Saving Mode set to Disabled
QPI Snoop Mode set to Home Directory Snoop with OSB
Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-1fno Tue Feb 28 20:41:53 2017

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4669 v4 @ 2.20GHz
4 "physical id"s (chips)

```

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### Platform Notes (Continued)

88 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 22
siblings  : 22
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
```

cache size : 56320 KB

From /proc/meminfo

```
MemTotal:      1058692552 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux linux-1fno 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Feb 28 20:37

SPEC is set to: /opt/cpu2006-1.2

```
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sdal        xfs       280G      13G  267G   5% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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## Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. B420M4.3.1.2.0.052320161053 05/23/2016

Memory:

32x 0xCE00 M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz

16x NO DIMM NO DIMM

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh10.2"

OMP\_NUM\_THREADS = "88"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32  
445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32  
C++ benchmarks (except as noted below):  
icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32  
473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

- 400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-prefetch
- 401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -auto-ilp32 -qopt-prefetch
- 403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-qopt-malloc-options=3 -auto-ilp32
- 429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-qopt-prefetch -auto-p32
- 445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2)
- 456.hmmer: basepeak = yes
- 458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4
- 462.libquantum: basepeak = yes
- 464.h264ref: basepeak = yes

C++ benchmarks:

- 471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-ra-region-strategy=block  
-Wl,-z,muldefs -L/sh10.2 -lsmarheap
- 473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmarheap64
- 483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-Wl,-z,muldefs -L/sh10.2 -lsmarheap

## Peak Other Flags

C benchmarks:

Continued on next page



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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revD.20170404.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revD.20170404.xml>

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