SPEC® CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

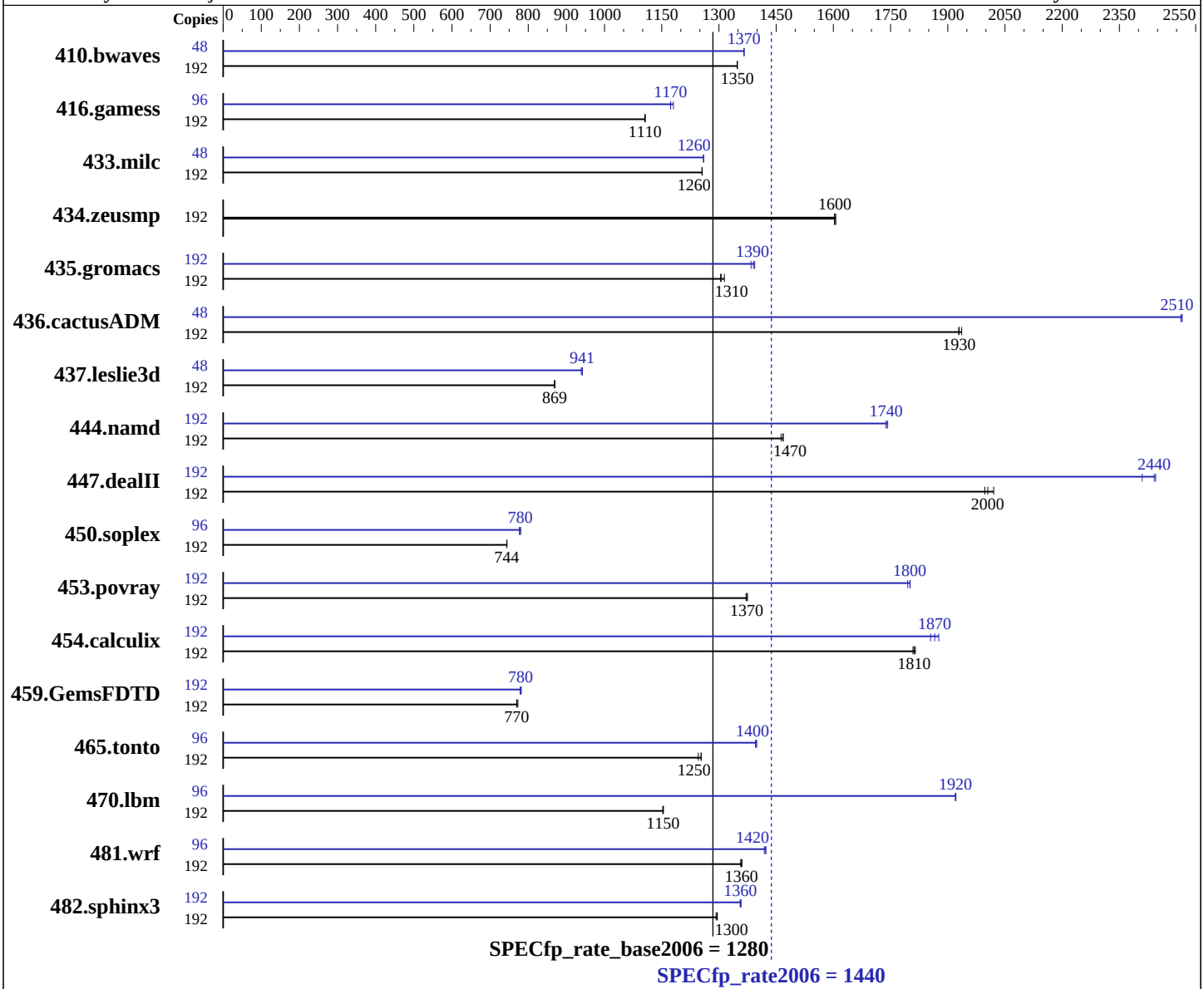
Fujitsu
Fujitsu SPARC M12-2

SPECfp®_rate2006 = 1440

SPECfp_rate_base2006 = 1280

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017



Hardware

CPU Name:	SPARC64 XII
CPU Characteristics:	
CPU MHz:	3900
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip, 8 threads/core
CPU(s) orderable:	1 or 2 CPU chips; the number of orderable total cores is 2, 3, 4, .. 24
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

Continued on next page

Software

Operating System:	Oracle Solaris 11.3 (with June 2017 SRU)
Compiler:	C/C++/Fortran: Version 12.6 of Oracle Developer Studio
Auto Parallel:	No
File System:	tmpfs
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None

SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Fujitsu
Fujitsu SPARC M12-2

SPECfp_rate2006 = 1440

SPECfp_rate_base2006 = 1280

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017

L3 Cache: 32 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (32 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 600 GB 10K RPM SAS (for system disk)
Other Hardware: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	192	**1936**	**1350**	1936	1350	1936	1350	48	478	1370	478	1370	**478**	**1370**
416.gamess	192	**3397**	**1110**	3401	1110	3397	1110	96	1592	1180	**1602**	**1170**	1602	1170
433.milc	192	1404	1260	1403	1260	**1403**	**1260**	48	350	1260	**350**	**1260**	350	1260
434.zeusmp	192	1090	1600	1088	1610	**1089**	**1600**	192	1090	1600	1088	1610	**1089**	**1600**
435.gromacs	192	1043	1310	**1050**	**1310**	1051	1300	192	**986**	**1390**	984	1390	990	1380
436.cactusADM	192	1185	1940	**1189**	**1930**	1190	1930	48	228	2510	**228**	**2510**	228	2510
437.leslie3d	192	2075	870	2077	869	**2076**	**869**	48	**479**	**941**	481	939	479	942
444.namd	192	**1049**	**1470**	1053	1460	1048	1470	192	886	1740	884	1740	**886**	**1740**
447.dealII	192	1100	2000	1087	2020	**1096**	**2000**	192	**900**	**2440**	898	2450	911	2410
450.soplex	192	**2153**	**744**	2153	744	2152	744	96	**1027**	**780**	1027	780	1031	776
453.povray	192	743	1370	745	1370	**744**	**1370**	192	569	1790	567	1800	**567**	**1800**
454.calculix	192	873	1820	**874**	**1810**	876	1810	192	854	1850	844	1880	**849**	**1870**
459.GemsFDTD	192	**2645**	**770**	2636	773	2647	770	192	**2611**	**780**	2617	779	2606	782
465.tonto	192	**1508**	**1250**	1507	1250	1517	1250	96	**676**	**1400**	677	1400	675	1400
470.lbm	192	2285	1150	**2286**	**1150**	2289	1150	96	687	1920	**687**	**1920**	687	1920
481.wrf	192	1580	1360	1577	1360	**1578**	**1360**	96	754	1420	756	1420	**754**	**1420**
482.sphinx3	192	**2889**	**1300**	2895	1290	2888	1300	192	2756	1360	2760	1360	**2759**	**1360**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
Processes were assigned to specific processors using 'pbind' commands.
The config file option 'submit' was used, along with a list of
processors in the 'BIND' variable, to generate the pbind commands.
(For details, please see the config file.)
```

Operating System Notes

```
Shell Environments:
   ulimit -s 131072 was used to limit the space consumed by the stack
   (and therefore make more space available to the heap).

   The "Logical Domains Manager" service was turned off using the command "svcadm disable ldmd".

System Tunables:
```

Continued on next page

Operating System Notes (Continued)

```
(/etc/system parameters)
   autoup = 86400
      Causes pages older than the listed number of seconds to be written by fsflush.
   doiflush = 0
     Controls whether file system metadata syncs will be executed during fsflush invocations.
   dopageflush = 0
      Controls whether memory is examined for modified pages during fsflush invocations.
   zfs:zfs_arc_max=1073741824
      Determines the maximum size of the ZFS Adaptive Replacement Cache (ARC).
```

Platform Notes

```
Sysinfo program /export/cpu2006/config/sysinfo
Revision 6993 of 2015-11-06 (975e92c7086bc383773e22882bdda8dd)
running on H2S-210-D0 Sat Mar  4 19:45:17 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /usr/sbin/psrinfo
   SPARC64-XII (chipid 0, clock 3900 MHz)
   SPARC64-XII (chipid 1, clock 3900 MHz)
   2 chips
   192 threads
   3900 MHz

From kstat:       24 cores

From prtconf: 1046016 Megabytes

/etc/release:
     Oracle Solaris 11.3 SPARC
uname -a:
   SunOS H2S-210-D0 5.11 11.3 sun4v sparc sun4v

SPEC is set to: /export/cpu2006

disk: df -h  /export/cpu2006
Filesystem             Size   Used  Available Capacity  Mounted on
rpool/export           547G   2.7G       401G     1%    /export

(End of data from sysinfo program)
```

General Notes

```
File System:
 tmpfs: output_root was used to put run directories in /tmp/cpu2006
 zfs: operating system
```

Continued on next page

General Notes (Continued)

```
SPEC CPU2006 benchmark:
 Updated with runspec --update
```

Base Compiler Invocation

C benchmarks:
`cc`

C++ benchmarks:
`CC`

Fortran benchmarks:
`f90`

Benchmarks using both Fortran and C:
`cc f90`

Base Portability Flags

447.dealII: `-DBOOST_NO_COMPILER_CONFIG`

Base Optimization Flags

C benchmarks:
`-std=c99 -m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M -xsegment_align=4M -xthroughput -xalias_level=std -xprefetch_level=2`

C++ benchmarks:
`-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M -xsegment_align=4M -xthroughput -xalias_level=compatible -library=stlport4`

Fortran benchmarks:
`-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M -xsegment_align=4M -xthroughput -xvector=no%lib`

Benchmarks using both Fortran and C:
`-std=c99 -m32 -fast(cc) -fast(f95) -xtarget=sparc64xii -xipo=2 -xpagesize=4M -xsegment_align=4M -xthroughput -xalias_level=std -xprefetch_level=2 -xvector=no%lib`

Base Other Flags

C benchmarks:
`-xjobs=8`

C++ benchmarks:
`-xjobs=8`

Fortran benchmarks:
`-xjobs=8`

Benchmarks using both Fortran and C:
`-xjobs=8`

Peak Compiler Invocation

C benchmarks:
`cc`

C++ benchmarks:
`CC`

Fortran benchmarks:
`f90`

Benchmarks using both Fortran and C:
`cc f90`

Peak Portability Flags

447.dealII: `-DBOOST_NO_COMPILER_CONFIG`

Peak Optimization Flags

C benchmarks:

433.milc: `-std=c99 -m32 -fast -xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M -xthroughput -xipo=2 -xalias_level=std -fsimple=1 -W2,-Ainline:rs=400 -Qoption cg -Qms_pipe+alldoall -W2,-Asac -xthroughput=no`

470.lbm: `-std=c99 -m32 -fast -xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M -xthroughput -xipo=2 -xalias_level=std -xprefetch_level=2 -xpagesize=256M -xsegment_align=256M -xthroughput=no -lbsdmalloc`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: -std=c99 -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xO4 -xipo=2 -xprefetch=latx:0.6
-xinline_param=level:1 -xprefetch=no%auto -lbsdmalloc

C++ benchmarks:

444.namd: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xalias_level=compatible -xprefetch=no%auto
-Wc,-Qms_pipe+alldoall

447.dealII: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xtarget=sparc64xplus -xipo=1
-xalias_level=compatible -xrestrict -xprefetch=no%auto
-Qoption cg -Qiselect-funcalign=64 -xthroughput=yes
-library=stdcxx4 -template=extdef

450.soplex: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xipo=2 -Wc,-Qlp=0

453.povray: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xO4 -xtarget=sparc64xplus -xipo=2
-xalias_level=compatible -xlinkopt=2 -xprefetch=no%auto
-xunroll=7 -Qoption iropt -Ainline:rs=1024
-Qoption iropt -Ainline:cs=1024
-Qoption iropt -Ainline:inc=900 -lfast

Fortran benchmarks:

410.bwaves: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xipo=2 -xunroll=4 -xvector=%none
-xprefetch=no%auto

416.gamess: -m32 -fast -xtarget=sparc64xii -xpagesize=4M
-xsegment_align=4M -xthroughput -xvector=no%simd
-xprefetch=latx:0.1

434.zeusmp: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

437.leslie3d: -m32 -fast -xtarget=sparc64xii -xpagesize=4M
-xsegment_align=4M -xthroughput -xunroll=2 -xvector=%none
-xprefetch=latx:0.8 -Qoption cg -Qms_pipe+alldoall
-xinline_param=level:1 -xthroughput=no

459.GemsFDTD: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xunroll=9 -xprefetch=latx:0.2
-xprefetch_level=3 -Qoption cg -Qlp-av=128
-Qoption iropt -Rujam

465.tonto: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xipo=1 -xO4 -xunroll=3 -xprefetch=no%auto
-xthroughput=no -lbsdmalloc

Benchmarks using both Fortran and C:

435.gromacs: -std=c99 -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast(cc) -fast(f95)
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xalias_level=strong -Wc,-Qicache-chbab=1
-Wc,-Qiselect-rsqrta=2 -Wc,-Qiselect-rsqrta1x=2
-qoption cg -Qicache-chbab=1 -qoption cg -Qiselect-rsqrta=2
-qoption cg -Qiselect-rsqrta1x=2

436.cactusADM: -std=c99 -m32 -fast(cc) -fast(f95) -xtarget=sparc64xii
-xpagesize=4M -xsegment_align=4M -xthroughput
-xtarget=sparc64xplus -xunroll=10 -xprefetch=latx:2.0
-xpagesize=256M -xsegment_align=256M -xthroughput=no
-lbsdmalloc

454.calculix: -std=c99 -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -m32 -fast(cc) -fast(f95)
-xtarget=sparc64xii -xpagesize=4M -xsegment_align=4M
-xthroughput -xtarget=sparc64xplus -xipo=1
-Wc,-Qiselect-funcalign=64 -xinline_param=level:3
-Qoption cg -Qiselect-funcalign=64

481.wrf: -std=c99 -m32 -fast(cc) -fast(f95) -xtarget=sparc64xii
-xpagesize=4M -xsegment_align=4M -xthroughput -xunroll=9
-xprefetch=latx:0.4 -Qoption iropt -Rujam -xO4
-xthroughput=no

Peak Other Flags

C benchmarks:
`-xjobs=8`

C++ benchmarks:
`-xjobs=8`

Fortran benchmarks:
`-xjobs=8`

Benchmarks using both Fortran and C:
`-xjobs=8`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Oracle-Developer-Studio12.6.html`
`http://www.spec.org/cpu2006/flags/Fujitsu-M12-2.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Oracle-Developer-Studio12.6.xml`
`http://www.spec.org/cpu2006/flags/Fujitsu-M12-2.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Apr 20 09:42:26 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 April 2017.