



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = **128**

Huawei 2288H V5 (Intel Xeon Gold 5118)

SPECfp_base2006 = **122**

CPU2006 license: 3175

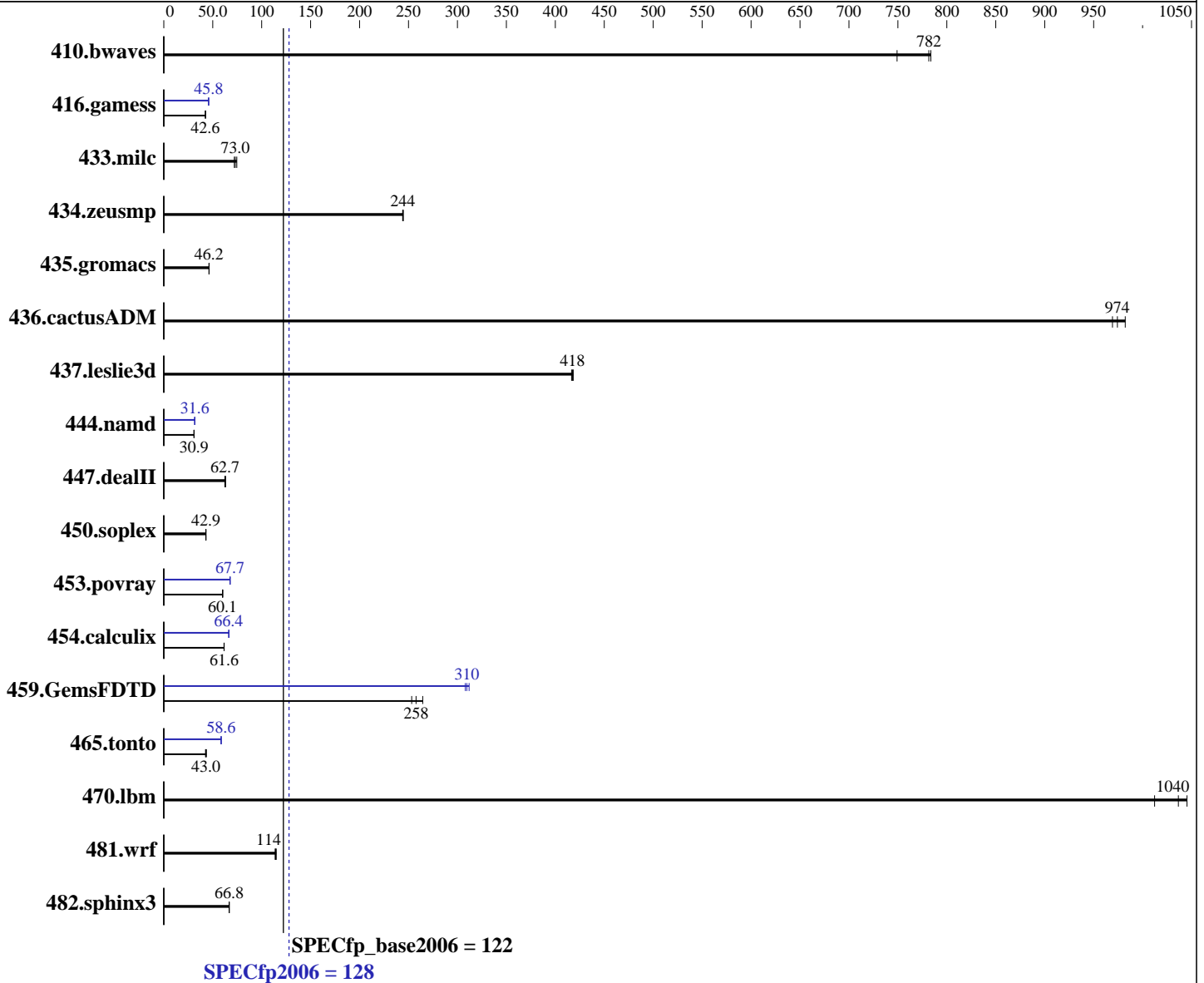
Test sponsor: Huawei

Tested by: Huawei

Test date: Jul-2017

Hardware Availability: Aug-2017

Software Availability: Nov-2016



Hardware

CPU Name: Intel Xeon Gold 5118
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)
 3.10.0-514.el7.x86_64
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux
 Auto Parallel: Yes
 File System: xfs

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L3 Cache: 16.5 MB I+D on chip per chip
 Other Cache: None
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400 MHz)
 Disk Subsystem: 1 x 1200 GB SAS, 10000 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 17.3 | 784 | 17.4 | 782 | 18.1 | 749 | 17.3 | 784 | 17.4 | 782 | 18.1 | 749 |
| 416.gamess | 461 | 42.5 | 460 | 42.6 | 460 | 42.6 | 428 | 45.8 | 427 | 45.8 | 428 | 45.8 |
| 433.milc | 123 | 74.6 | 127 | 72.0 | 126 | 73.0 | 123 | 74.6 | 127 | 72.0 | 126 | 73.0 |
| 434.zeusmp | 37.3 | 244 | 37.2 | 245 | 37.3 | 244 | 37.3 | 244 | 37.2 | 245 | 37.3 | 244 |
| 435.gromacs | 154 | 46.2 | 154 | 46.3 | 155 | 46.2 | 154 | 46.2 | 154 | 46.3 | 155 | 46.2 |
| 436.cactusADM | 12.3 | 969 | 12.3 | 974 | 12.2 | 982 | 12.3 | 969 | 12.3 | 974 | 12.2 | 982 |
| 437.leslie3d | 22.5 | 418 | 22.6 | 417 | 22.5 | 418 | 22.5 | 418 | 22.6 | 417 | 22.5 | 418 |
| 444.namd | 260 | 30.9 | 260 | 30.9 | 260 | 30.9 | 254 | 31.6 | 254 | 31.6 | 254 | 31.6 |
| 447.dealII | 181 | 63.1 | 183 | 62.4 | 182 | 62.7 | 181 | 63.1 | 183 | 62.4 | 182 | 62.7 |
| 450.soplex | 193 | 43.2 | 194 | 42.9 | 194 | 42.9 | 193 | 43.2 | 194 | 42.9 | 194 | 42.9 |
| 453.povray | 88.5 | 60.1 | 88.8 | 59.9 | 88.3 | 60.3 | 78.7 | 67.6 | 78.4 | 67.8 | 78.6 | 67.7 |
| 454.calculix | 134 | 61.6 | 134 | 61.7 | 134 | 61.5 | 124 | 66.7 | 124 | 66.4 | 124 | 66.3 |
| 459.GemsFDTD | 41.2 | 258 | 41.9 | 253 | 40.1 | 265 | 34.4 | 308 | 34.0 | 312 | 34.3 | 310 |
| 465.tonto | 229 | 43.0 | 226 | 43.5 | 231 | 42.6 | 168 | 58.5 | 168 | 58.6 | 168 | 58.7 |
| 470.lbm | 13.1 | 1050 | 13.6 | 1010 | 13.3 | 1040 | 13.1 | 1050 | 13.6 | 1010 | 13.3 | 1040 |
| 481.wrf | 97.8 | 114 | 97.2 | 115 | 98.2 | 114 | 97.8 | 114 | 97.2 | 115 | 98.2 | 114 |
| 482.sphinx3 | 292 | 66.8 | 292 | 66.8 | 292 | 66.8 | 292 | 66.8 | 292 | 66.8 | 292 | 66.8 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Hyper-Threading to Disable
 Sysinfo program /spec17/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on localhost.localdomain Tue Jul 4 08:38:39 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz

2 "physical id"s (chips)

24 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 12

siblings : 12

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13

physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

cache size : 16896 KB

From /proc/meminfo

MemTotal: 394145204 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.3 (Maipo)"

ID="rhel"

ID_LIKE="fedora"

VERSION_ID="7.3"

PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"

ANSI_COLOR="0;31"

CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13

EDT 2016 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 3 13:11

SPEC is set to: /spec17

Filesystem Type Size Used Avail Use% Mounted on

/dev/sda2 xfs 898G 18G 881G 2% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.15 05/09/2017

Memory:

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Platform Notes (Continued)

24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/spec17/libs/32:/spec17/libs/64:/spec17/sh10.2"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.deallI: -DSPEC_CPU_LP64

450.soplex: -DSPEC_CPU_LP64

453.povray: -DSPEC_CPU_LP64

454.calculix: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

```

459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3
-auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.xml>

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For other inquiries, please contact webmaster@spec.org.

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