# SPEC® CFP2006 Result

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## Lenovo Global Technology

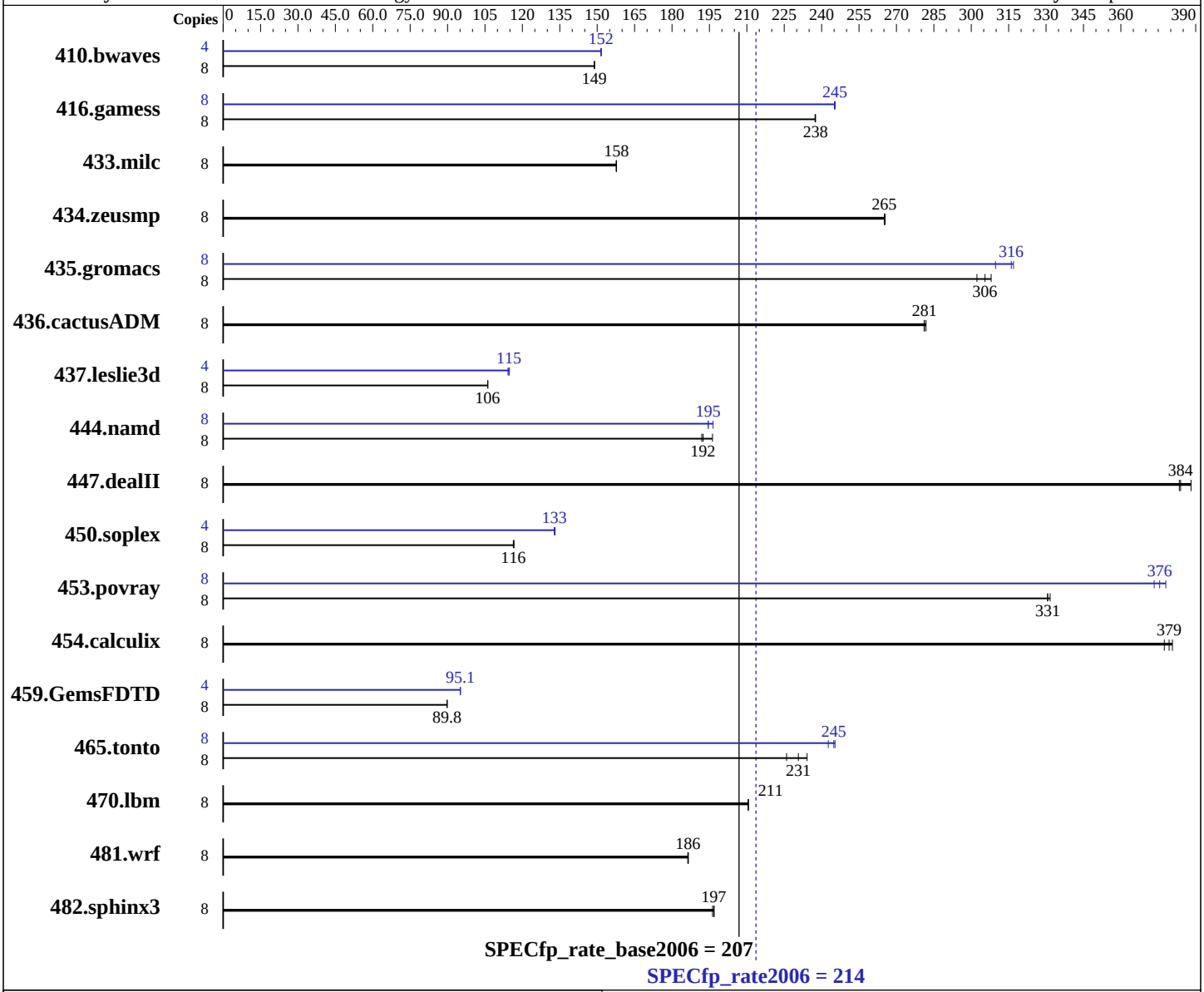
Lenovo ThinkServer TS150
(3.80 GHz, Intel Xeon E3-1270 v6)

SPECfp®_rate2006 = 214

SPECfp_rate_base2006 = 207

**CPU2006 license:** 9017
**Test sponsor:** Lenovo Global Technology
**Tested by:** Lenovo Global Technology

**Test date:** Jul-2017
**Hardware Availability:** Apr-2017
**Software Availability:** Apr-2017




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E3-1270 v6 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 4.20 GHz |
| CPU MHz: | 3800 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 SP1 (x86_64)<br>Kernel 3.12.49-11-default |
| Compiler: | C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;<br>Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux |
| Auto Parallel: | Yes |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2400T-U)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 8 | 730 | 149 | 730 | 149 | **730** | **149** | 4 | 359 | 151 | **359** | **152** | 358 | 152 |
| 416.gamess | 8 | 659 | 238 | **659** | **238** | 660 | 237 | 8 | 639 | 245 | 638 | 245 | **638** | **245** |
| 433.milc | 8 | **466** | **158** | 466 | 158 | 466 | 158 | 8 | **466** | **158** | 466 | 158 | 466 | 158 |
| 434.zeusmp | 8 | 274 | 265 | 275 | 265 | **275** | **265** | 8 | 274 | 265 | 275 | 265 | **275** | **265** |
| 435.gromacs | 8 | 185 | 308 | 189 | 302 | **187** | **306** | 8 | 184 | 310 | 180 | 317 | **181** | **316** |
| 436.cactusADM | 8 | 339 | 282 | **340** | **281** | 340 | 281 | 8 | 339 | 282 | **340** | **281** | 340 | 281 |
| 437.leslie3d | 8 | 709 | 106 | 709 | 106 | **709** | **106** | 4 | **328** | **115** | 329 | 114 | 328 | 115 |
| 444.namd | 8 | 334 | 192 | **333** | **192** | 327 | 196 | 8 | 330 | 194 | **330** | **195** | 327 | 196 |
| 447.dealII | 8 | **238** | **384** | 239 | 383 | 236 | 388 | 8 | **238** | **384** | 239 | 383 | 236 | 388 |
| 450.soplex | 8 | 572 | 117 | 573 | 116 | **573** | **116** | 4 | 251 | 133 | 251 | 133 | **251** | **133** |
| 453.povray | 8 | **129** | **331** | 128 | 332 | 129 | 331 | 8 | 113 | 378 | 114 | 373 | **113** | **376** |
| 454.calculix | 8 | 175 | 377 | 173 | 381 | **174** | **379** | 8 | 175 | 377 | 173 | 381 | **174** | **379** |
| 459.GemsFDTD | 8 | 945 | 89.9 | 946 | 89.8 | **945** | **89.8** | 4 | 446 | 95.1 | **446** | **95.1** | 446 | 95.1 |
| 465.tonto | 8 | 336 | 234 | 348 | 226 | **341** | **231** | 8 | 324 | 243 | **321** | **245** | 321 | 245 |
| 470.lbm | 8 | 522 | 211 | **522** | **211** | 522 | 211 | 8 | 522 | 211 | **522** | **211** | 522 | 211 |
| 481.wrf | 8 | 479 | 187 | **479** | **186** | 480 | 186 | 8 | 479 | 187 | **479** | **186** | 480 | 186 |
| 482.sphinx3 | 8 | 792 | 197 | **792** | **197** | 795 | 196 | 8 | 792 | 197 | **792** | **197** | 795 | 196 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Intel Virtualization Technology set to Enabled
VT-d set to Enabled
```

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## Platform Notes (Continued)

```
 ICE Performance Mode set to Full Speed
  Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993
  Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
  running on TS150-MLK Wed Jul 19 22:48:38 2017

  This section contains SUT (System Under Test) info as seen by
  some common utilities.  To remove or add to this section, see:
    http://www.spec.org/cpu2006/Docs/config.html#sysinfo

  From /proc/cpuinfo
     model name : Intel(R) Xeon(R) CPU E3-1270 v6 @ 3.80GHz
        1 "physical id"s (chips)
        8 "processors"
     cores, siblings (Caution: counting these is hw and system dependent.  The
     following excerpts from /proc/cpuinfo might not be reliable.  Use with
     caution.)
        cpu cores : 4
        siblings  : 8
        physical 0: cores 0 1 2 3
     cache size : 8192 KB

  From /proc/meminfo
     MemTotal:       66040296 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 1
        # This file is deprecated and will be removed in a future service pack or
        release.
        # Please check /etc/os-release for details about this release.
     os-release:
        NAME="SLES"
        VERSION="12-SP1"
        VERSION_ID="12.1"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp1"

  uname -a:
     Linux TS150-MLK 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
     (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jul 19 15:37

  SPEC is set to: /home/cpu2006-1.2-ic17.0u3
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda4      xfs   689G   16G  673G   3% /home
```

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## Platform Notes (Continued)

```
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS LENOVO S06KT03A 02/07/2017
   Memory:
    4x Micron 16ATF2G64AZ-2G3B1 16 GB 2 rank 2400 MHz

 (End of data from sysinfo program)
```

## General Notes

```
 Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"

  Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
  memory using Redhat Enterprise Linux 7.2
  Transparent Huge Pages enabled by default
  Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
```

## Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

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## Base Portability Flags (Continued)

437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-malloc-options=3
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

Continued on next page

# Peak Optimization Flags (Continued)

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-KBL-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-KBL-revC.xml


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