# SPEC® CFP2006 Result

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## Lenovo Global Technology

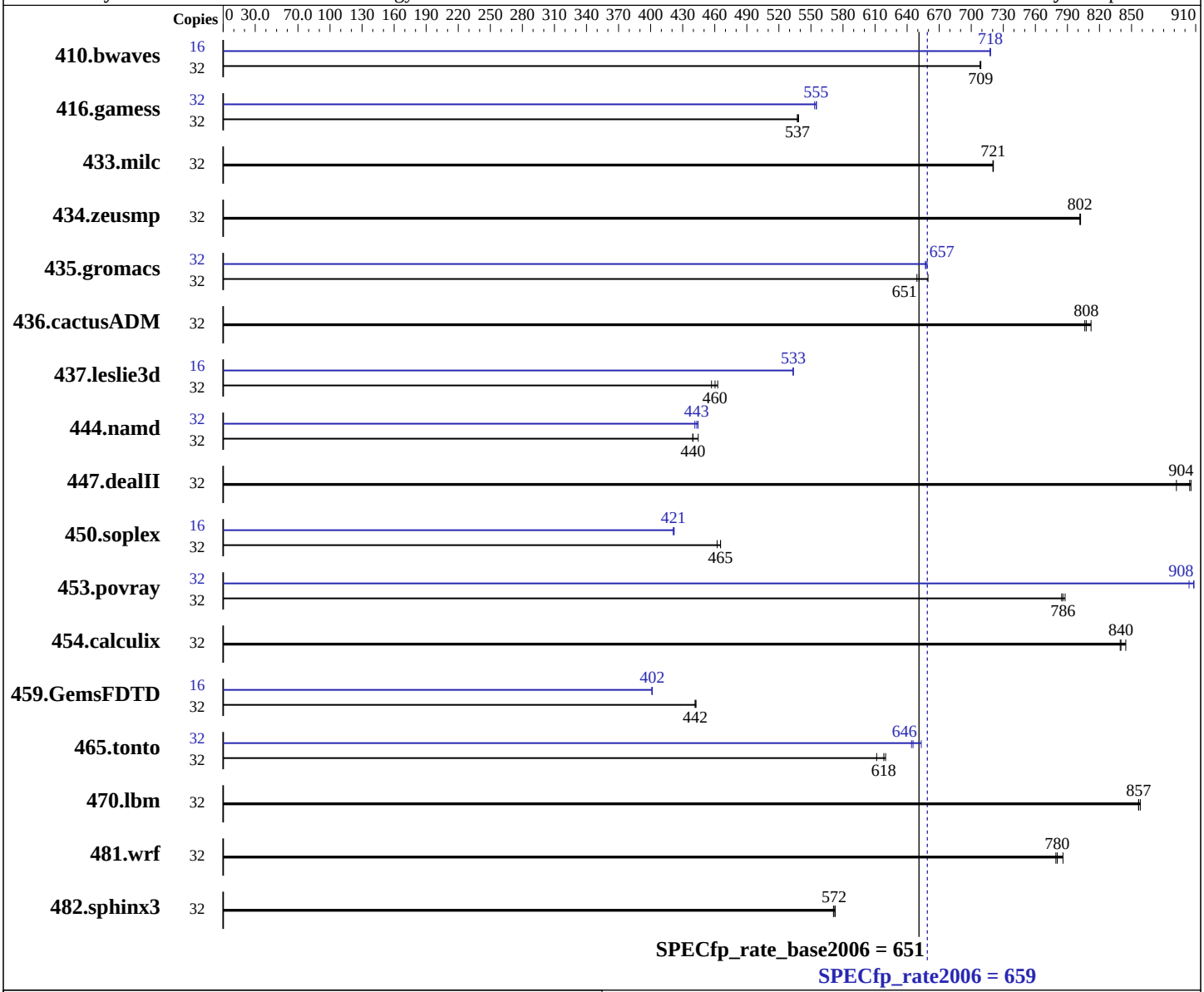
ThinkSystem SR530
(2.00 GHz, Intel Xeon Silver 4109T)

SPECfp®_rate2006 = 659

SPECfp_rate_base2006 = 651

**CPU2006 license:** 9017
**Test sponsor:** Lenovo Global Technology
**Tested by:** Lenovo Global Technology

**Test date:** Jul-2017
**Hardware Availability:** Aug-2017
**Software Availability:** Apr-2017




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon Silver 4109T |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.00 GHz |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 2 chips, 8 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 1 MB I+D on chip per core |

Continued on next page

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 SP2 (x86_64)<br>Kernel 4.4.21-69-default |
| Compiler: | C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;<br>Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux |
| Auto Parallel: | Yes |
| File System: | btrfs |
| System State: | Run level 3 (multi-user) |

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L3 Cache: 11 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400 MHz)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 32 | 614 | 708 | 613 | 709 | **613** | **709** | 16 | 303 | 718 | 303 | 718 | **303** | **718** |
| 416.gamess | 32 | **1166** | **537** | 1166 | 537 | 1164 | 538 | 32 | 1132 | 553 | **1129** | **555** | 1129 | 555 |
| 433.milc | 32 | 408 | 720 | **408** | **721** | 408 | 721 | 32 | 408 | 720 | **408** | **721** | 408 | 721 |
| 434.zeusmp | 32 | **363** | **802** | 363 | 802 | 363 | 802 | 32 | **363** | **802** | 363 | 802 | 363 | 802 |
| 435.gromacs | 32 | **351** | **651** | 352 | 649 | 346 | 660 | 32 | **348** | **657** | 347 | 659 | 348 | 657 |
| 436.cactusADM | 32 | 471 | 812 | **473** | **808** | 474 | 806 | 32 | 471 | 812 | **473** | **808** | 474 | 806 |
| 437.leslie3d | 32 | **654** | **460** | 650 | 463 | 658 | 457 | 16 | **282** | **533** | 282 | 533 | 282 | 534 |
| 444.namd | 32 | 577 | 445 | 584 | 439 | **584** | **440** | 32 | 581 | 441 | **579** | **443** | 577 | 444 |
| 447.dealII | 32 | 404 | 906 | 410 | 892 | **405** | **904** | 32 | 404 | 906 | 410 | 892 | **405** | **904** |
| 450.soplex | 32 | **573** | **465** | 573 | 465 | 577 | 462 | 16 | **317** | **421** | 316 | 422 | 317 | 421 |
| 453.povray | 32 | **217** | **786** | 216 | 788 | 217 | 785 | 32 | **187** | **908** | 188 | 904 | 187 | 908 |
| 454.calculix | 32 | 313 | 845 | **314** | **840** | 315 | 839 | 32 | 313 | 845 | **314** | **840** | 315 | 839 |
| 459.GemsFDTD | 32 | 769 | 442 | **769** | **442** | 767 | 442 | 16 | **423** | **402** | 423 | 402 | 423 | 401 |
| 465.tonto | 32 | 515 | 611 | 508 | 620 | **509** | **618** | 32 | 482 | 653 | **488** | **646** | 489 | 644 |
| 470.lbm | 32 | 512 | 858 | **513** | **857** | 513 | 857 | 32 | 512 | 858 | **513** | **857** | 513 | 857 |
| 481.wrf | 32 | 455 | 786 | 459 | 779 | **458** | **780** | 32 | 455 | 786 | 459 | 779 | **458** | **780** |
| 482.sphinx3 | 32 | **1091** | **572** | 1089 | 573 | 1092 | 571 | 32 | **1091** | **572** | 1089 | 573 | 1092 | 571 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Operating Mode set to Maximum Performance
DCU Streamer Prefetcher set to Disable
```

Continued on next page

## Platform Notes (Continued)

```
XPT Prefetcher set to Disable
LLC dead line alloc set to Disable
Stale AtoS set to Enable
 Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on linux-dqoj Tue Jul 18 23:53:34 2017

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
       2 "physical id"s (chips)
       32 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 8
       siblings  : 16
       physical 0: cores 0 1 2 3 4 5 6 7
       physical 1: cores 0 1 2 3 4 5 6 7
    cache size : 11264 KB

 From /proc/meminfo
    MemTotal:       395882636 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

 From /etc/*release* /etc/*version*
    SuSE-release:
       SUSE Linux Enterprise Server 12 (x86_64)
       VERSION = 12
       PATCHLEVEL = 2
       # This file is deprecated and will be removed in a future service pack or
       release.
       # Please check /etc/os-release for details about this release.
    os-release:
       NAME="SLES"
       VERSION="12-SP2"
       VERSION_ID="12.2"
       PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
       ID="sles"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:12:sp2"

 uname -a:
    Linux linux-dqoj 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
    (9464f67) x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 Jul 18 23:38
```

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## Platform Notes (Continued)

```
SPEC is set to: /home/cpu2006-1.2-ic17.0u3
   Filesystem     Type   Size  Used Avail Use% Mounted on
   /dev/sdb2      btrfs  744G  138G  605G  19% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Lenovo -[TEE105Z-1.00]- 04/27/2017
  Memory:
   12x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled by default
 Filesystem page cache cleared with:
 shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`

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## Base Portability Flags (Continued)

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Fortran benchmarks:
ifort -m64

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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-malloc-options=3
-qopt-mem-layout-trans=3

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# Peak Optimization Flags (Continued)

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4 -auto -inline-calloc -qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32 -qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.xml

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