



# SPEC® CINT2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL560 Gen10  
(3.20 GHz, Intel Xeon Gold 6146)

**SPECint®\_rate2006 = Not Run**

**SPECint\_rate\_base2006 = 2960**

**CPU2006 license:** 3

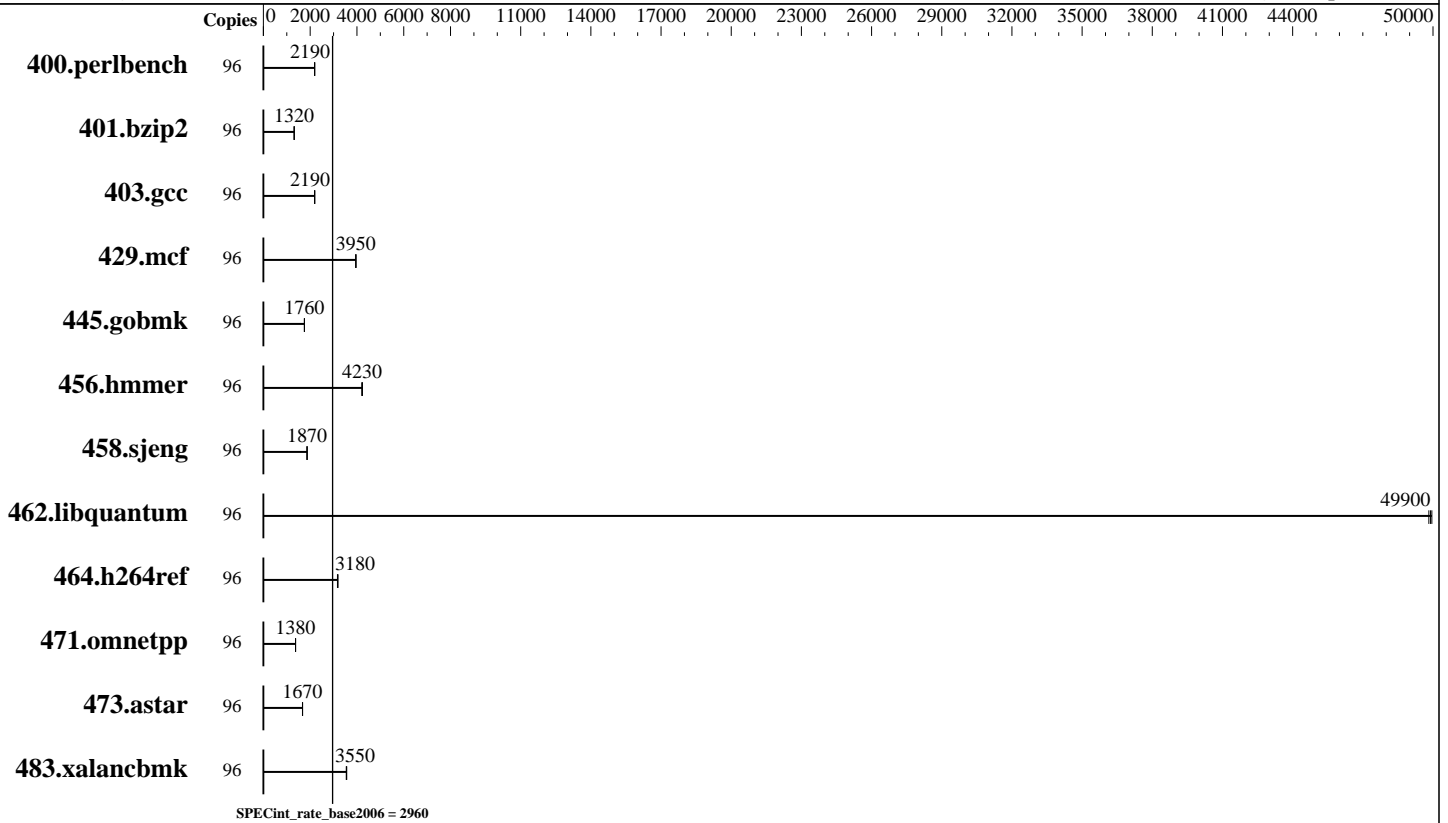
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Dec-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Apr-2017



## Hardware

CPU Name: Intel Xeon Gold 6146  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.20 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2, 4 chip(s)  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 24.75 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)  
 Disk Subsystem: 2 x 480 GB SATA SSD, RAID 0  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP2  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	96	428	2190	<b><u>428</u></b>	<b><u>2190</u></b>	428	2190									
401.bzip2	96	703	1320	703	1320	<b><u>703</u></b>	<b><u>1320</u></b>									
403.gcc	96	<b><u>353</u></b>	<b><u>2190</u></b>	353	2190	352	2200									
429.mcf	96	<b><u>221</u></b>	<b><u>3950</u></b>	222	3950	221	3960									
445.gobmk	96	<b><u>574</u></b>	<b><u>1760</u></b>	574	1760	574	1750									
456.hammer	96	213	4210	212	4230	<b><u>212</u></b>	<b><u>4230</u></b>									
458.sjeng	96	<b><u>622</u></b>	<b><u>1870</u></b>	622	1870	622	1870									
462.libquantum	96	<b><u>39.9</u></b>	<b><u>49900</u></b>	39.9	49800	39.8	50000									
464.h264ref	96	<b><u>668</u></b>	<b><u>3180</u></b>	667	3190	671	3170									
471.omnetpp	96	<b><u>436</u></b>	<b><u>1380</u></b>	436	1380	436	1380									
473.astar	96	<b><u>403</u></b>	<b><u>1670</u></b>	402	1680	403	1670									
483.xalancbmk	96	187	3540	186	3560	<b><u>186</u></b>	<b><u>3550</u></b>									

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"
tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"
```

## Platform Notes

```
BIOS Configuration:
Thermal Configuration set to Maximum Cooling
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
State A to S set to Enabled
Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E State
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```



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## Platform Notes (Continued)

Sysinfo program /home/cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-e6az Mon Dec 4 16:04:04 2017

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz
 4 "physical id"s (chips)
 96 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 12
siblings   : 24
physical 0: cores 0 1 2 3 4 9 10 16 18 19 25 26
physical 1: cores 0 1 2 3 4 8 9 11 17 18 19 20
physical 2: cores 0 1 2 3 8 9 10 11 18 19 24 27
physical 3: cores 0 1 2 3 4 8 9 11 17 18 19 20
cache size : 25344 KB
```

From /proc/meminfo

```
MemTotal:      792278428 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

/usr/bin/lsc\_release -d

```
SUSE Linux Enterprise Server 12 SP2
```

From /etc/\*release\* /etc/\*version\*

SuSE-release:

```
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
```

```
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
```

os-release:

```
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

uname -a:

```
Linux linux-e6az 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

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## Platform Notes (Continued)

run-level 3 Dec 4 16:03

SPEC is set to: /home/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	852G	86G	767G	11%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017

Memory:

48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
 403.gcc: -D\_FILE\_OFFSET\_BITS=64  
 429.mcf: -D\_FILE\_OFFSET\_BITS=64  
 445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
 456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
 458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
 462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
 471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

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## Base Portability Flags (Continued)

473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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