## SPEC® CPU2017 Floating Point Speed Result

**Hewlett Packard Enterprise**
*(Test Sponsor: HPE)*
**ProLiant DL380 Gen10**
*(2.50 GHz, Intel Xeon Platinum 8180M)*

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Oct-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Oct-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

### CPU2017 License:
3

### Tested by:
HPE

### Test Sponsor:
HPE

### Hardware

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base = 132</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>56</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>56</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>56</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>56</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>56</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>56</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>56</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>56</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>56</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>56</td>
</tr>
</tbody>
</table>

**SPECspeed2017_fp_peak = 133**

### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux Server release 7.3 (Maipo), Kernel 3.10.0-514.6.1.el7.x86_64</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

### CPU Name:
Intel Xeon Platinum 8180M

| Max MHz.: | 3800 |
| Nominal: | 2500 |
| Enabled: | 56 cores, 2 chips |
| Orderable: | 1, 2 chip(s) |
| Cache L1: | 32 KB I + 32 KB D on chip per core |
| L2: | 1 MB I+D on chip per core |
| L3: | 38.5 MB I+D on chip per chip |
| Other: | None |
| Memory: | 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R) |
| Storage: | 1 x 960 GB SATA SSD, RAID 0 |
| Other: | None |

### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux Server release 7.3 (Maipo), Kernel 3.10.0-514.6.1.el7.x86_64</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>
# SPEC CPU2017 Floating Point Speed Result

**Test Sponsor:** HPE  
**ProLiant DL380 Gen10**  
**(2.50 GHz, Intel Xeon Platinum 8180M)**

**CPU2017 License:** 3  
**Test Date:** Oct-2017  
**Test Sponsor:** HPE  
**Hardware Availability:** Oct-2017  
**Tested by:** HPE  
**Software Availability:** Sep-2017

## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>56</td>
<td>123</td>
<td>479</td>
<td>123</td>
<td>479</td>
<td>123</td>
<td>479</td>
<td>56</td>
<td>123</td>
<td>479</td>
<td>123</td>
<td>479</td>
<td>123</td>
<td>479</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>56</td>
<td>87.2</td>
<td>191</td>
<td>86.7</td>
<td>192</td>
<td>86.5</td>
<td>193</td>
<td>56</td>
<td>85.6</td>
<td>195</td>
<td>85.6</td>
<td>195</td>
<td>85.6</td>
<td>195</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>56</td>
<td>118</td>
<td>44.5</td>
<td>117</td>
<td>44.6</td>
<td>117</td>
<td>44.8</td>
<td>56</td>
<td>118</td>
<td>44.6</td>
<td>117</td>
<td>44.6</td>
<td>117</td>
<td>44.6</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>56</td>
<td>144</td>
<td>92.1</td>
<td>144</td>
<td>92.2</td>
<td>143</td>
<td>92.4</td>
<td>56</td>
<td>134</td>
<td>98.5</td>
<td>136</td>
<td>97.4</td>
<td>135</td>
<td>97.7</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>56</td>
<td>76.6</td>
<td>116</td>
<td>76.9</td>
<td>115</td>
<td>76.9</td>
<td>115</td>
<td>56</td>
<td>77.2</td>
<td>115</td>
<td>76.5</td>
<td>116</td>
<td>77.0</td>
<td>115</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>56</td>
<td>186</td>
<td>63.7</td>
<td>186</td>
<td>64.0</td>
<td>190</td>
<td>62.6</td>
<td>56</td>
<td>182</td>
<td>65.4</td>
<td>188</td>
<td>63.0</td>
<td>182</td>
<td>65.3</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>56</td>
<td>94.3</td>
<td>153</td>
<td>94.9</td>
<td>152</td>
<td>90.3</td>
<td>160</td>
<td>56</td>
<td>95.3</td>
<td>151</td>
<td>93.4</td>
<td>154</td>
<td>89.2</td>
<td>162</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>56</td>
<td>60.6</td>
<td>288</td>
<td>60.6</td>
<td>288</td>
<td>60.6</td>
<td>288</td>
<td>56</td>
<td>60.6</td>
<td>288</td>
<td>60.8</td>
<td>288</td>
<td>60.6</td>
<td>288</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>56</td>
<td>107</td>
<td>85.5</td>
<td>107</td>
<td>85.5</td>
<td>107</td>
<td>85.5</td>
<td>56</td>
<td>107</td>
<td>85.5</td>
<td>107</td>
<td>85.5</td>
<td>107</td>
<td>85.5</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>56</td>
<td>106</td>
<td>149</td>
<td>106</td>
<td>149</td>
<td>107</td>
<td>147</td>
<td>56</td>
<td>102</td>
<td>154</td>
<td>103</td>
<td>154</td>
<td>102</td>
<td>154</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
</table>
| SPECspeed2017_fp_base = 132  
SPECspeed2017_fp_peak = 133

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"

## General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=core,compact"
LD_LIBRARY_PATH = "/spec2017/lib/ia32:/spec2017/lib/intel64:/spec2017/je5.0.1-32"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/spec2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4

## Platform Notes

BIOS Configuration:
Intel Hyperthreading set to Disabled
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetcher set to Enabled
LLC Dead Line Allocation set to Disabled
Workload Profile set to General Peak Frequency Compute

(Continued on next page)
Hewlett Packard Enterprise
ProLiant DL380 Gen10
(2.50 GHz, Intel Xeon Platinum 8180M)

SPECspeed2017_fp_base = 132
SPECspeed2017_fp_peak = 133

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

Energy/Performance Bias set to Maximum Performance
Uncore Frequency Scaling set to Auto
Workload Profile set to General Peak Frequency Compute
NUMA Group Size Optimization set to Flat

Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b0091c0f
running on DL380-sys2-RHEL73 Tue Oct 3 01:36:34 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8180M CPU @ 2.50GHz
  2  "physical id"s (chips)
  56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 56
On-line CPU(s) list: 0-55
Thread(s) per core: 1
Core(s) per socket: 28
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Platinum 8180M CPU @ 2.50GHz
Stepping: 4
CPU MHz: 2500.000
BogoMIPS: 5005.49
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K

(Continued on next page)
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.50 GHz, Intel Xeon Platinum 8180M)

SPECspeed2017_fp_base = 132
SPECspeed2017_fp_peak = 133

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

L3 cache: 39424K
NUMA node0 CPU(s): 0-27
NUMA node1 CPU(s): 28-55

/proc/cpuinfo cache data
cache size: 39424 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
node 0 size: 97963 MB
node 0 free: 93559 MB
node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55
node 1 size: 98303 MB
node 1 free: 95361 MB
node distances:
note 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 197569032 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
uname -a:
Linux DL380-sys2-RHEL73 3.10.0-514.6.1.el7.x86_64 #1 SMP Sat Dec 10 11:15:38 EST 2016
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 2 15:06

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.50 GHz, Intel Xeon Platinum 8180M)

SPECspeed2017_fp_base = 132
SPECspeed2017_fp_peak = 133

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

SPEC is set to: /spec2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 889G 29G 861G 4% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U30 09/29/2017
Memory:
24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC  607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC   607.cactuBSSN_s(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
SPEC CPU2017 Floating Point Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.50 GHz, Intel Xeon Platinum 8180M)

<table>
<thead>
<tr>
<th>CPU2017 License: 3</th>
<th>Test Date: Oct-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: HPE</td>
<td>Hardware Availability: Oct-2017</td>
</tr>
<tr>
<td>Tested by: HPE</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

**SPECspeed2017_fp_base** = 132

**SPECspeed2017_fp_peak** = 133

---

**Compiler Version Notes (Continued)**

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 621.wrf_s(peak) 628.pop2_s(peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:
icc

Fortran benchmarks:
ifort

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.50 GHz, Intel Xeon Platinum 8180M)

| SPECspeed2017_fp_base = 132 |
| SPECspeed2017_fp_peak = 133 |

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte
SPEC CPU2017 Floating Point Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.50 GHz, Intel Xeon Platinum 8180M)

SPECspeed2017_fp_base = 132
SPECspeed2017_fp_peak = 133

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

(Continued on next page)
Peak Optimization Flags (Continued)

638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:

603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-qopenmp -nostandard-realloc-lhs -align array32byte

649.fotonik3d_s: basepeak = yes

654.roms_s: Same as 603.bwaves_s

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

Peak Other Flags

C benchmarks:

-m64 -std=c11

Fortran benchmarks:

-m64

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.50 GHz, Intel Xeon Platinum 8180M)

| SPECspeed2017_fp_base = 132 |
| SPECspeed2017_fp_peak = 133 |

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Peak Other Flags (Continued)

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-10-03 03:36:34-0400.
Report generated on 2018-10-31 14:35:03 by CPU2017 PDF formatter v6067.
Originally published on 2017-10-31.