



SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3

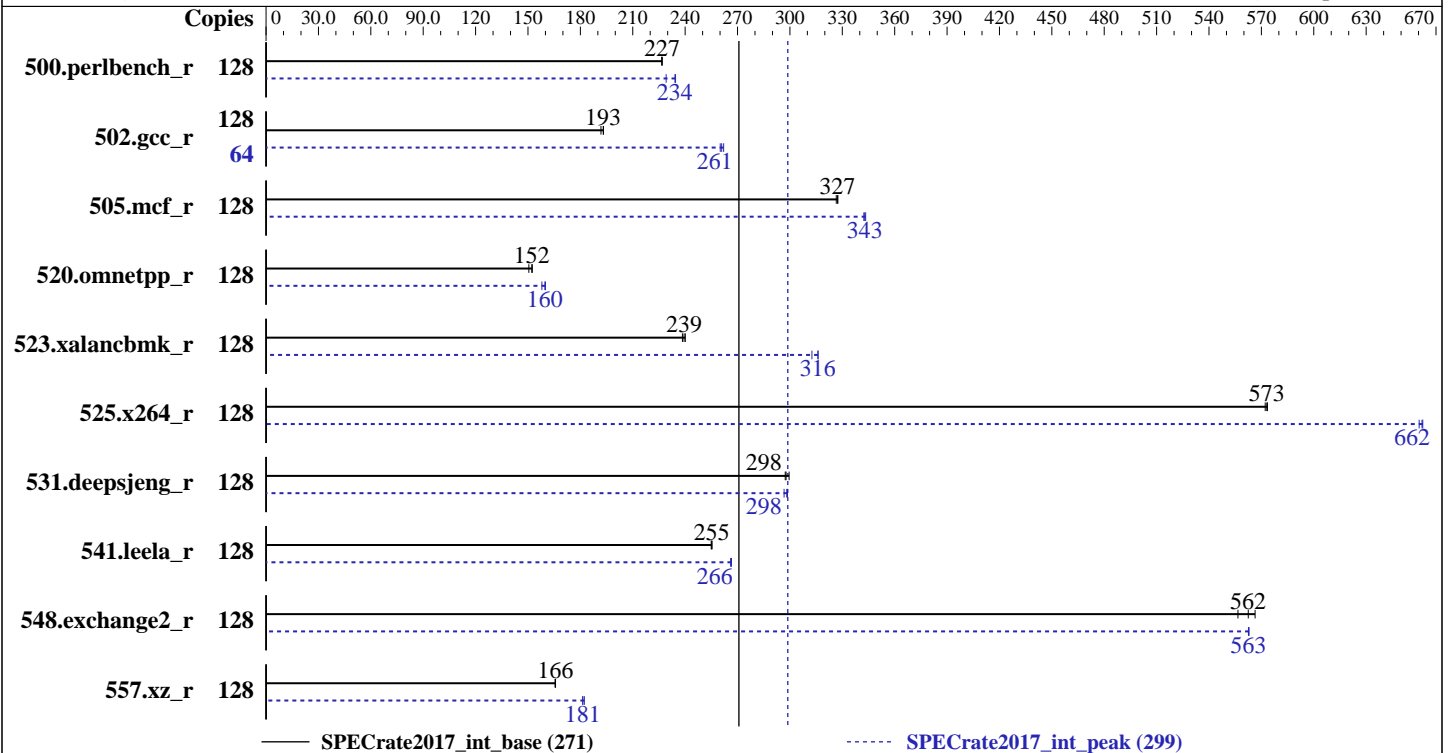
Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017



Hardware

CPU Name: AMD EPYC 7601
 Max MHz.: 3200
 Nominal: 2200
 Enabled: 64 cores, 2 chips, 2 threads/core
 Orderable: 1, 2 chip(s)
 Cache L1: 64 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 64 MB I+D on chip per chip, 8 MB shared / 4 cores
 Other: None
 Memory: 1 TB (16 x 64 GB 4Rx4 PC4-2666V-L)
 Storage: 1 x 300 GB 15 K RPM SAS, RAID 0
 Other: None

Software

OS: SUSE Linux Enterprise Server 12 (x86_64) SP3
 Kernel 4.4.73-5-default
 Compiler: C/C++: Version 1.0.0 of AOCC
 Fortran: Version 4.8.5 of GCC
 Parallel: No
 Firmware: HPE BIOS Version A40 released Nov-2017 (tested with A40 10/13/2017)
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc general purpose malloc implementation v4.5.0



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2017
Hardware Availability: Nov-2017
Software Availability: Sep-2017

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	128	898	227	899	227	899	227	128	889	229	870	234	869	235
502.gcc_r	128	939	193	938	193	945	192	64	347	261	348	260	346	262
505.mcf_r	128	633	327	632	327	632	327	128	602	343	603	343	604	342
520.omnetpp_r	128	1102	152	1104	152	1116	150	128	1050	160	1063	158	1052	160
523.xalancbmk_r	128	565	239	567	238	563	240	128	432	313	428	316	428	316
525.x264_r	128	391	573	392	572	391	573	128	338	662	339	660	339	662
531.deepsjeng_r	128	490	300	493	298	493	297	128	494	297	491	299	492	298
541.leela_r	128	832	255	830	255	830	255	128	796	266	795	267	796	266
548.exchange2_r	128	592	566	596	562	603	557	128	596	563	596	563	596	563
557.xz_r	128	834	166	834	166	835	166	128	762	181	762	181	758	182

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu
Linux governor set to performance with cpupower "cpupower frequency-set -r -g performance"

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages were enabled for this run (OS default)

Huge pages were not configured for this run.



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/amd1704-rate-libs-revC/64;/home/cpu2017/amd1704-rate-libs-revC/32:"
MALLOCCONF = "lg_chunk:26"
```

The AMD64 AOCC Compiler Suite is available at

<http://developer.amd.com/tools-and-sdks/cpu-development/amd-optimizing-cc-compiler/>

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using RHEL 7.4

jemalloc, a general purpose malloc implementation, was obtained at

<https://github.com/jemalloc/jemalloc/releases/download/4.5.0/jemalloc-4.5.0.tar.bz2>

jemalloc was built with GCC v4.8.5 in RHEL v7.2 under default conditions.

jemalloc uses environment variable MALLOCCONF with values narenas and lg_chunk:

narenas: sets the maximum number of arenas to use for automatic multiplexing of threads and arenas.

lg_chunk: set the virtual memory chunk size (log base 2). For example,

lg_chunk:21 sets the default chunk size to 2^{21} = 2MiB.

The AOCC Gold Linker plugin was installed and used for the link stage.

The AOCC Fortran Plugin version 1.0 was used to leverage AOCC optimizers with gfortran. It is available here:

<http://developer.amd.com/amd-aocc/>

Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling

Performance Determinism set to Power Deterministic

Processor Power and Utilization Monitoring set to Disabled

Workload Profile set to General Throughput Compute

Minimum Processor Idle Power Core C-State set to C6 State

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on linux-55v8 Tue Oct 24 17:29:00 2017

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : AMD EPYC 7601 32-Core Processor

2 "physical id"s (chips)

128 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Platform Notes (Continued)

excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 32
siblings  : 64
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                 128
On-line CPU(s) list:   0-127
Thread(s) per core:    2
Core(s) per socket:    32
Socket(s):              2
NUMA node(s):          8
Vendor ID:              AuthenticAMD
CPU family:             23
Model:                  1
Model name:             AMD EPYC 7601 32-Core Processor
Stepping:               2
CPU MHz:                2200.000
CPU max MHz:            2200.0000
CPU min MHz:            1200.0000
BogoMIPS:               4391.81
Virtualization:        AMD-V
L1d cache:              32K
L1i cache:              64K
L2 cache:               512K
L3 cache:               8192K
NUMA node0 CPU(s):     0-7,64-71
NUMA node1 CPU(s):     8-15,72-79
NUMA node2 CPU(s):     16-23,80-87
NUMA node3 CPU(s):     24-31,88-95
NUMA node4 CPU(s):     32-39,96-103
NUMA node5 CPU(s):     40-47,104-111
NUMA node6 CPU(s):     48-55,112-119
NUMA node7 CPU(s):     56-63,120-127
```

```
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc extd_apicid amd_dcm aperfmperf eagerfpu pni
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx fl6c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx arat cpb
hw_pstate npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold vmmcall avic fsgsbase bmi1 avx2 smep bmi2 rdseed adx smap
clflushopt sha_ni xsaveopt xsavec xgetbv1 clzero irperf overflow_recov succor smca
```

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Platform Notes (Continued)

```
/proc/cpuinfo cache data
cache size : 512 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0 1 2 3 4 5 6 7 64 65 66 67 68 69 70 71
node 0 size: 128846 MB
node 0 free: 128619 MB
node 1 cpus: 8 9 10 11 12 13 14 15 72 73 74 75 76 77 78 79
node 1 size: 129021 MB
node 1 free: 128793 MB
node 2 cpus: 16 17 18 19 20 21 22 23 80 81 82 83 84 85 86 87
node 2 size: 129021 MB
node 2 free: 128804 MB
node 3 cpus: 24 25 26 27 28 29 30 31 88 89 90 91 92 93 94 95
node 3 size: 129021 MB
node 3 free: 128815 MB
node 4 cpus: 32 33 34 35 36 37 38 39 96 97 98 99 100 101 102 103
node 4 size: 129021 MB
node 4 free: 128838 MB
node 5 cpus: 40 41 42 43 44 45 46 47 104 105 106 107 108 109 110 111
node 5 size: 129021 MB
node 5 free: 128842 MB
node 6 cpus: 48 49 50 51 52 53 54 55 112 113 114 115 116 117 118 119
node 6 size: 129021 MB
node 6 free: 128846 MB
node 7 cpus: 56 57 58 59 60 61 62 63 120 121 122 123 124 125 126 127
node 7 size: 129020 MB
node 7 free: 128838 MB
```

```
node distances:
node  0  1  2  3  4  5  6  7
0:  10 16 16 16 32 32 32 32
1:  16 10 16 16 32 32 32 32
2:  16 16 10 16 32 32 32 32
3:  16 16 16 10 32 32 32 32
4:  32 32 32 32 10 16 16 16
5:  32 32 32 32 16 10 16 16
6:  32 32 32 32 16 16 10 16
7:  32 32 32 32 16 16 16 10
```

From /proc/meminfo

MemTotal: 1056761484 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Platform Notes (Continued)

```

/usr/bin/lsb_release -d
    SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 3
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
    Linux linux-55v8 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017 (b7ce4e4) x86_64
    x86_64 x86_64 GNU/Linux

run-level 3 Oct 24 17:25

SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   238G  5.6G  232G   3% /home

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS HPE A40 10/13/2017
Memory:
    16x UNKNOWN NOT AVAILABLE
    16x UNKNOWN NOT AVAILABLE 64 GB 4 rank 2666

(End of data from sysinfo program)

```

Compiler Version Notes

```

=====
CC 502.gcc_r(peak)
=====
AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM

```

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2017
Hardware Availability: Nov-2017
Software Availability: Sep-2017

Compiler Version Notes (Continued)

AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

=====
CXXC 523.xalancbmk_r(peak)

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

=====
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
525.x264_r(base) 557.xz_r(base, peak)

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

=====
CXXC 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base,
peak) 541.leela_r(base)

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

=====
CC 500.perlbench_r(peak) 525.x264_r(peak)

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Compiler Version Notes (Continued)

=====
CXXC 541.leela_r(peak)
=====

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
=====

=====
FC 548.exchange2_r(base, peak)
=====

GNU Fortran (GCC) 4.8.2
Copyright (C) 2013 Free Software Foundation, Inc.
GNU Fortran comes with NO WARRANTY, to the extent permitted by law.
You may redistribute copies of GNU Fortran
under the terms of the GNU General Public License.
For more information about these matters, see the file named COPYING
=====

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

clang gfortran

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Base Portability Flags (Continued)

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

```
-flto -Wl,-plugin-opt= -merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop -disable-vect-cmp -O3 -ffast-math  
-march=znver1 -fstruct-layout=2 -mllvm -unroll-threshold=100  
-fremap-arrays -mno-avx2 -inline-threshold=1000 -z muldefs -ljemalloc
```

C++ benchmarks:

```
-flto -Wl,-plugin-opt= -merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop -disable-vect-cmp -O3  
-march=znver1 -mllvm -unroll-threshold=100 -finline-aggressive  
-fremap-arrays -inline-threshold=1000 -z muldefs -ljemalloc
```

Fortran benchmarks:

```
-flto -Wl,-plugin-opt= -merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop -disable-vect-cmp -O3(gfortran)  
-O3(clang) -mavx -madox -funroll-loops -ffast-math -z muldefs -Ofast  
-fdefault-integer-8 -fplugin=dragonegg.so  
-fplugin-arg-dragonegg-llvm-option="-lsr-in-nested-loop -enable-iv-split  
-inline-threshold:1000 -disable-vect-cmp" -ljemalloc -lgfortran  
-lamdlibm
```

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

clang gfortran



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -D_FILE_OFFSET_BITS=64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -flto -Wl,-plugin-opt= -merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop
-fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver1
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively
-mno-avx2 -unroll-threshold=100 -fremap-arrays
-inline-threshold=1000 -ljemalloc
```

```
502.gcc_r: -m32 -flto -Wl,-plugin-opt= -merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -Ofast -march=znver1
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively
-mno-avx2 -unroll-threshold=100 -fremap-arrays
-inline-threshold=1000 -fgnu89-inline
-L/root/work/lib/jemalloc/lib32 -ljemalloc
```

```
505.mcf_r: -flto -Wl,-plugin-opt= -merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -Ofast -march=znver1
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively
-mno-avx2 -unroll-threshold=100 -fremap-arrays
-inline-threshold=1000 -ljemalloc
```

525.x264_r: Same as 500.perlbench_r

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

```
520.omnetpp_r: -flto -Wl,-plugin-opt= -merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -Ofast -march=znver1
```

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10

(2.20 GHz, AMD EPYC 7601)

SPECrate2017_int_base = 271

SPECrate2017_int_peak = 299

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Peak Optimization Flags (Continued)

520.omnetpp_r (continued):

```
-finline-aggressive -mllvm -unroll-threshold=100  
-fremap-arrays -inline-threshold=1000 -ljemalloc
```

```
523.xalancbmk_r: -m32 -flto -Wl,-plugin-opt= -merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop -Ofast -march=znver1  
-finline-aggressive -mllvm -unroll-threshold=100  
-fremap-arrays -inline-threshold=1000  
-L/root/work/lib/jemalloc/lib32 -ljemalloc
```

531.deepsjeng_r: Same as 520.omnetpp_r

```
541.leela_r: -flto -Wl,-plugin-opt= -merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop  
-fprofile-instr-generate(pass 1)  
-fprofile-instr-use(pass 2) -Ofast -march=znver1 -mllvm  
-unroll-count=8 -unroll-threshold=100 -ljemalloc
```

Fortran benchmarks:

```
-flto -Wl,-plugin-opt= -merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop -O3(gfortran) -O3(clang) -mavx2  
-madx -funroll-loops -ffast-math -Ofast -fdefault-integer-8  
-fplugin=dragonegg.so  
-fplugin-arg-dragonegg-llvm-option="-lsr-in-nested-loop -enable-iv-split  
-inline-threshold:1000 -disable-vect-cmp" -ljemalloc -lgfortran  
-lamdlibm
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/gcc.2017-11-20.html>

<http://www.spec.org/cpu2017/flags/aocc100-flags-revC-I.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/gcc.2017-11-20.xml>

<http://www.spec.org/cpu2017/flags/aocc100-flags-revC-I.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revB.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-10-24 18:28:59-0400.

Report generated on 2018-03-02 18:09:31 by CPU2017 PDF formatter v5865.

Originally published on 2017-11-20.