SPEC® CPU2017 Integer Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

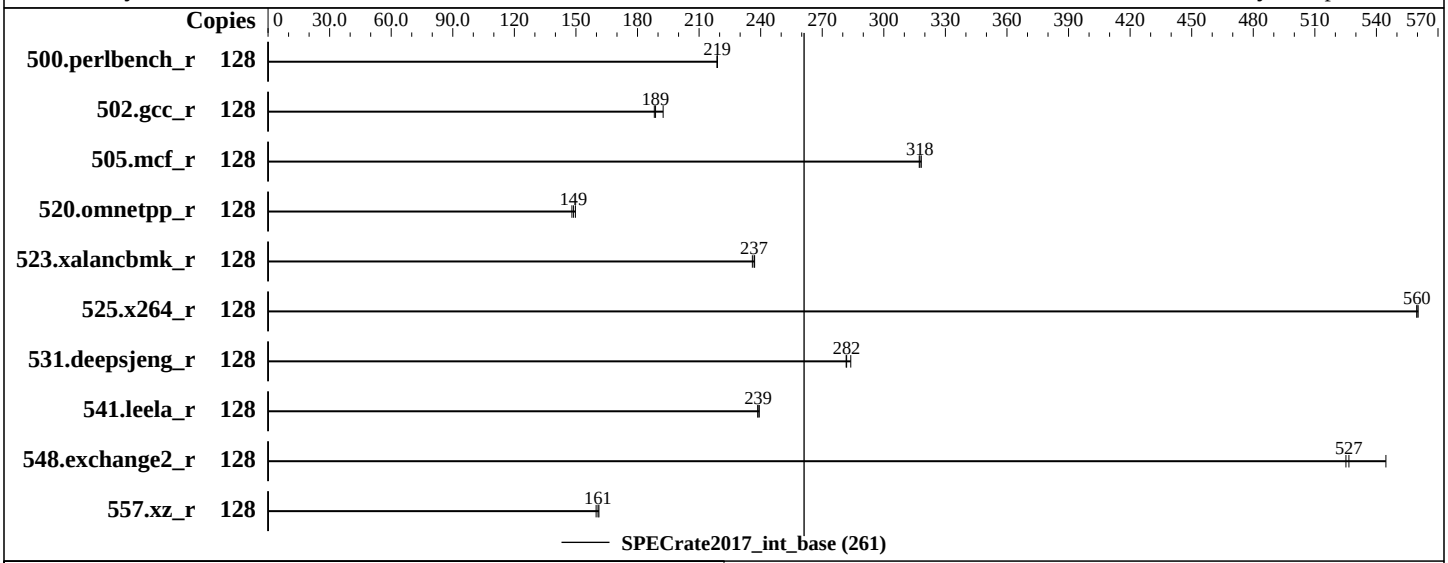
ProLiant DL385 Gen10
(2.00 GHz, AMD EPYC 7551)

SPECrate2017_int_base = 261

SPECrate2017_int_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Dec-2017
Hardware Availability: Nov-2017
Software Availability: Sep-2017



Hardware

CPU Name:	AMD EPYC 7551
Max MHz.:	3000
Nominal:	2000
Enabled:	64 cores, 2 chips, 2 threads/core
Orderable:	1, 2 chip(s)
Cache L1:	64 KB I + 32 KB D on chip per core
L2:	512 KB I+D on chip per core
L3:	64 MB I+D on chip per chip, 8 MB shared / 4 cores
Other:	None
Memory:	1 TB (16 x 64 GB 4Rx4 PC4-2666V-L)
Storage:	1 x 400 GB SAS SSD, RAID 0
Other:	None

Software

OS:	SUSE Linux Enterprise Server 12 (x86_64) SP3 Kernel 4.4.73-5-default
Compiler:	C/C++: Version 1.0.0 of AOCC Fortran: Version 4.8.2 of GCC
Parallel:	No
Firmware:	HPE BIOS Version A40 released Nov-2017 (tested with A40 (11/10/2017))
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other:	jemalloc general purpose malloc implementation v4.5.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	128	**931**	**219**	931	219	932	219							
502.gcc_r	128	**960**	**189**	942	192	963	188							
505.mcf_r	128	650	318	652	317	**651**	**318**							
520.omnetpp_r	128	1121	150	1134	148	**1128**	**149**							
523.xalancbmk_r	128	573	236	570	237	**571**	**237**							
525.x264_r	128	400	560	**400**	**560**	400	560							
531.deepsjeng_r	128	**520**	**282**	517	284	521	282							
541.leela_r	128	886	239	889	239	**888**	**239**							
548.exchange2_r	128	616	545	**637**	**527**	639	525							
557.xz_r	128	865	160	**860**	**161**	858	161							

SPECrate2017_int_base = 261

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu
Linux governor set to performance with cpupower "cpupower frequency-set -r -g performance"
dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages were enabled for this run (OS default)

Huge pages were not configured for this run.
```

General Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/amd1704-rate-libs-revC/64;/home/cpu2017/amd1704-rate-libs-revC/32:"
MALLOC_CONF = "lg_chunk:26"

The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/tools-and-sdks/cpu-development/amd-optimizing-cc-compiler/

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using RHEL 7.4

jemalloc, a general purpose malloc implementation, was obtained at
https://github.com/jemalloc/jemalloc/releases/download/4.5.0/jemalloc-4.5.0.tar.bz2
jemalloc was built with GCC v4.8.5 in RHEL v7.2 under default conditions.
jemalloc uses environment variable MALLOC_CONF with values narenas and lg_chunk:
  narenas: sets the maximum number of arenas to use for automatic multiplexing
           of threads and arenas.
  lg_chunk: set the virtual memory chunk size (log base 2). For example,
            lg_chunk:21 sets the default chunk size to 2^21 = 2MiB.

The AOCC Gold Linker plugin was installed and used for the link stage.

The AOCC Fortran Plugin version 1.0 was used to leverage AOCC optimizers
with gfortran. It is available here:
http://developer.amd.com/amd-aocc/
```

Platform Notes

```
 BIOS Configuration:
  Thermal Configuration set to Maximum Cooling
  Performance Determinism set to Power Deterministic
  Memory Patrol Scrubbing set to Disabled
  Workload Profile set to General Throughput Compute
  Minimum Processor Idle Power Core C-State set to C6 State
  Processor Power and Utilization Monitoring set to Disabled
 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on dl385g10-1 Sun Dec 10 04:32:28 2017

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : AMD EPYC 7551 32-Core Processor
       2  "physical id"s (chips)
       128 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
```

(Continued on next page)

Platform Notes (Continued)

```
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 32
       siblings  : 64
       physical 0: cores 0 1 2 3 4 5 6 7
       physical 1: cores 0 1 2 3 4 5 6 7

  From lscpu:
       Architecture:          x86_64
       CPU op-mode(s):        32-bit, 64-bit
       Byte Order:            Little Endian
       CPU(s):                128
       On-line CPU(s) list:   0-127
       Thread(s) per core:    2
       Core(s) per socket:    32
       Socket(s):             2
       NUMA node(s):          8
       Vendor ID:             AuthenticAMD
       CPU family:            23
       Model:                 1
       Model name:            AMD EPYC 7551 32-Core Processor
       Stepping:              2
       CPU MHz:               2000.000
       CPU max MHz:           2000.0000
       CPU min MHz:           1200.0000
       BogoMIPS:              3992.44
       Virtualization:        AMD-V
       L1d cache:             32K
       L1i cache:             64K
       L2 cache:              512K
       L3 cache:              8192K
       NUMA node0 CPU(s):     0-7,64-71
       NUMA node1 CPU(s):     8-15,72-79
       NUMA node2 CPU(s):     16-23,80-87
       NUMA node3 CPU(s):     24-31,88-95
       NUMA node4 CPU(s):     32-39,96-103
       NUMA node5 CPU(s):     40-47,104-111
       NUMA node6 CPU(s):     48-55,112-119
       NUMA node7 CPU(s):     56-63,120-127
       Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
       pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
       constant_tsc rep_good nopl nonstop_tsc extd_apicid amd_dcm aperfmperf eagerfpu pni
       pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
       rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
       osvw skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx arat cpb
       hw_pstate npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
       pausefilter pfthreshold vmmcall avic fsgsbase bmi1 avx2 smep bmi2 rdseed adx smap
       clflushopt sha_ni xsaveopt xsavec xgetbv1 clzero irperf overflow_recov succor smca
```

(Continued on next page)

Platform Notes (Continued)

```
 /proc/cpuinfo cache data
    cache size : 512 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 8 nodes (0-7)
   node 0 cpus: 0 1 2 3 4 5 6 7 64 65 66 67 68 69 70 71
   node 0 size: 128774 MB
   node 0 free: 128536 MB
   node 1 cpus: 8 9 10 11 12 13 14 15 72 73 74 75 76 77 78 79
   node 1 size: 129021 MB
   node 1 free: 128797 MB
   node 2 cpus: 16 17 18 19 20 21 22 23 80 81 82 83 84 85 86 87
   node 2 size: 129021 MB
   node 2 free: 128806 MB
   node 3 cpus: 24 25 26 27 28 29 30 31 88 89 90 91 92 93 94 95
   node 3 size: 129021 MB
   node 3 free: 128824 MB
   node 4 cpus: 32 33 34 35 36 37 38 39 96 97 98 99 100 101 102 103
   node 4 size: 129021 MB
   node 4 free: 128843 MB
   node 5 cpus: 40 41 42 43 44 45 46 47 104 105 106 107 108 109 110 111
   node 5 size: 129021 MB
   node 5 free: 128840 MB
   node 6 cpus: 48 49 50 51 52 53 54 55 112 113 114 115 116 117 118 119
   node 6 size: 129021 MB
   node 6 free: 128844 MB
   node 7 cpus: 56 57 58 59 60 61 62 63 120 121 122 123 124 125 126 127
   node 7 size: 128867 MB
   node 7 free: 128696 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  16  16  16  32  32  32  32
     1:  16  10  16  16  32  32  32  32
     2:  16  16  10  16  32  32  32  32
     3:  16  16  16  10  32  32  32  32
     4:  32  32  32  32  10  16  16  16
     5:  32  32  32  32  16  10  16  16
     6:  32  32  32  32  16  16  10  16
     7:  32  32  32  32  16  16  16  10

 From /proc/meminfo
    MemTotal:       1056531276 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB
```

(Continued on next page)

Platform Notes (Continued)

```
 /usr/bin/lsb_release -d
    SUSE Linux Enterprise Server 12 SP3

 From /etc/*release* /etc/*version*
    SuSE-release:
       SUSE Linux Enterprise Server 12 (x86_64)
       VERSION = 12
       PATCHLEVEL = 3
       # This file is deprecated and will be removed in a future service pack or release.
       # Please check /etc/os-release for details about this release.
    os-release:
       NAME="SLES"
       VERSION="12-SP3"
       VERSION_ID="12.3"
       PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
       ID="sles"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:12:sp3"

 uname -a:
    Linux dl385g10-1 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017 (b7ce4e4) x86_64
    x86_64 x86_64 GNU/Linux

 run-level 3 Dec 31 19:17

 SPEC is set to: /home/cpu2017
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda4      xfs   331G   74G  258G  23% /home

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS HPE A40 11/10/2017
   Memory:
    16x UNKNOWN NOT AVAILABLE
    16x UNKNOWN NOT AVAILABLE 64 GB 4 rank 2666

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
 CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
      557.xz_r(base)
------------------------------------------------------------------------------
```

(Continued on next page)

Compiler Version Notes (Continued)

```
AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
  AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
------------------------------------------------------------------------------

==============================================================================
 CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
      541.leela_r(base)
------------------------------------------------------------------------------
AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
  AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
------------------------------------------------------------------------------

==============================================================================
 FC  548.exchange2_r(base)
------------------------------------------------------------------------------
GNU Fortran (GCC) 4.8.2
Copyright (C) 2013 Free Software Foundation, Inc.
GNU Fortran comes with NO WARRANTY, to the extent permitted by law.
You may redistribute copies of GNU Fortran
under the terms of the GNU General Public License.
For more information about these matters, see the file named COPYING
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`clang gfortran`

Base Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`

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Base Portability Flags (Continued)

502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-flto -Wl,-plugin-opt= -merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -disable-vect-cmp -O3 -ffast-math
-march=znver1 -fstruct-layout=2 -mllvm -unroll-threshold=100
-fremap-arrays -mno-avx2 -inline-threshold=1000 -z muldefs -ljemalloc

C++ benchmarks:
-flto -Wl,-plugin-opt= -merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -disable-vect-cmp -O3
-march=znver1 -mllvm -unroll-threshold=100 -finline-aggressive
-fremap-arrays -inline-threshold=1000 -z muldefs -ljemalloc

Fortran benchmarks:
-flto -Wl,-plugin-opt= -merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -disable-vect-cmp -O3(gfortran)
-O3(clang) -mavx -madx -funroll-loops -ffast-math -z muldefs -Ofast
-fdefault-integer-8 -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option="-lsr-in-nested-loop -enable-iv-split
-inline-threshold:1000 -disable-vect-cmp" -ljemalloc -lgfortran
-lamdlibm

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/gcc.2017-11-20.html
http://www.spec.org/cpu2017/flags/aocc100-flags-revC-I.html
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/gcc.2017-11-20.xml
http://www.spec.org/cpu2017/flags/aocc100-flags-revC-I.xml
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revD.xml

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Tested with SPEC CPU2017 v1.0.2 on 2017-12-10 04:32:28-0500.
Report generated on 2019-02-21 12:44:26 by CPU2017 PS formatter v6067.
Originally published on 2018-01-14.