SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

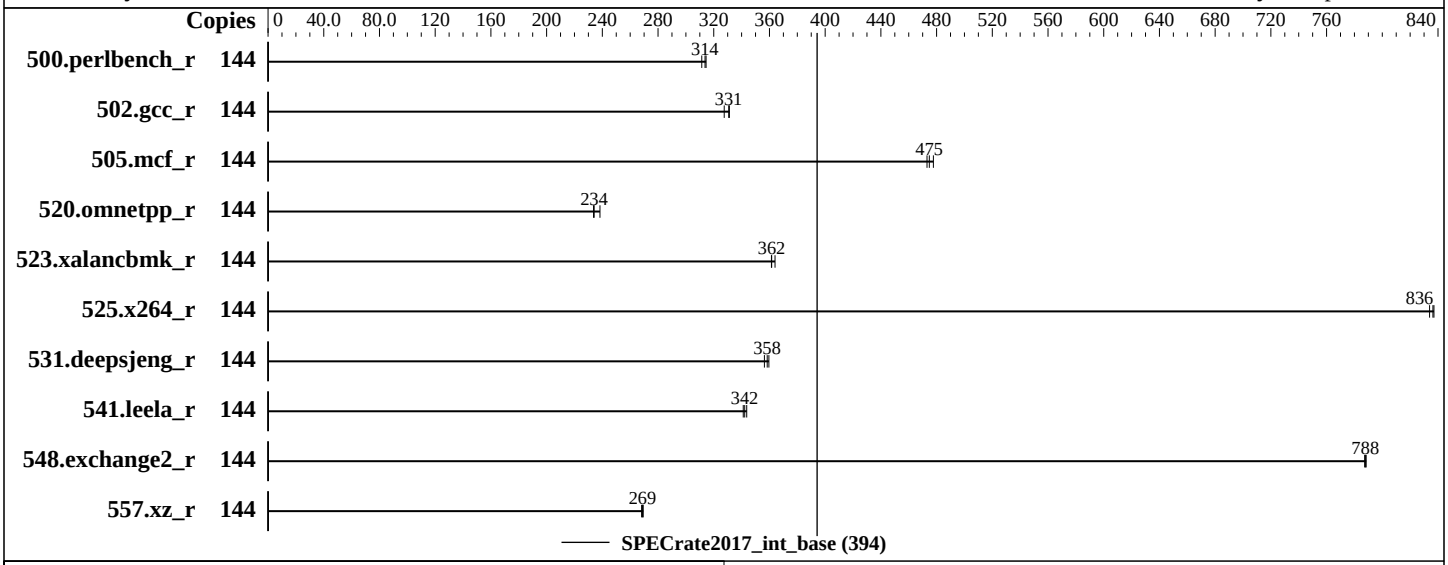
ProLiant DL560 Gen10
(2.70 GHz, Intel Xeon Gold 6150)

SPECrate2017_int_base = 394

SPECrate2017_int_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017



Hardware

CPU Name:	Intel Xeon Gold 6150
Max MHz.:	3700
Nominal:	2700
Enabled:	72 cores, 4 chips, 2 threads/core
Orderable:	1, 2, 4 chip(s)
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	24.75 MB I+D on chip per chip
Other:	None
Memory:	768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
Storage:	1 x 480 GB SATA SSD, RAID 0
Other:	None

Software

OS:	SUSE Linux Enterprise Server 12 (x86_64) SP2 Kernel 4.4.21-69-default
Compiler:	C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
Parallel:	No
Firmware:	HPE BIOS Version U34 released Oct-2017 (tested with U34 9/29/2017)
File System:	xfs
System State:	Run level 3 (mulit-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other:	jemalloc: jemalloc memory allocator library V5.0.1; jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets; jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5; jemalloc: sources available via jemalloc.net

SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL560 Gen10
(2.70 GHz, Intel Xeon Gold 6150)

SPECrate2017_int_base = 394

SPECrate2017_int_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	144	**731**	**314**	729	314	736	311							
502.gcc_r	144	616	331	**616**	**331**	623	328							
505.mcf_r	144	487	478	492	473	**490**	**475**							
520.omnetpp_r	144	793	238	**807**	**234**	808	234							
523.xalancbmk_r	144	418	364	**421**	**362**	421	361							
525.x264_r	144	301	837	**301**	**836**	302	834							
531.deepsjeng_r	144	459	360	463	356	**460**	**358**							
541.leela_r	144	**697**	**342**	694	344	699	341							
548.exchange2_r	144	**479**	**788**	479	787	479	788							
557.xz_r	144	577	269	580	268	**578**	**269**							

SPECrate2017_int_base = 394

SPECrate2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"
tuned profile set wtih "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"
```

General Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.4
```

Platform Notes

```
BIOS Configuration:
 Thermal Configuration set to Maximum Cooling
 Memory Patrol Scrubbing set to Disabled
 LLC Prefetch set to Enabled
 LLC Dead Line Allocation set to Disabled
 Stale A to S set to Enabled
 Workload Profile set to General Throughput Compute
  Minimum Processor Idle Power Core C-State set to C1E State
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-z88l Fri Dec  1 13:54:23 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6150 CPU @ 2.70GHz
      4  "physical id"s (chips)
      144 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 18
      siblings  : 36
      physical 0: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 1: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 2: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 3: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:
     Architecture:          x86_64
     CPU op-mode(s):        32-bit, 64-bit
     Byte Order:            Little Endian
     CPU(s):                144
     On-line CPU(s) list:   0-143
     Thread(s) per core:    2
     Core(s) per socket:    18
     Socket(s):             4
     NUMA node(s):          8
     Vendor ID:             GenuineIntel
     CPU family:            6
     Model:                 85
     Model name:            Intel(R) Xeon(R) Gold 6150 CPU @ 2.70GHz
     Stepping:              4
     CPU MHz:               2693.696
     BogoMIPS:              5387.39
     Virtualization:        VT-x
```

(Continued on next page)

Platform Notes (Continued)

```
      L1d cache:             32K
      L1i cache:             32K
      L2 cache:              1024K
      L3 cache:              25344K
      NUMA node0 CPU(s):     0-8,72-80
      NUMA node1 CPU(s):     9-17,81-89
      NUMA node2 CPU(s):     18-26,90-98
      NUMA node3 CPU(s):     27-35,99-107
      NUMA node4 CPU(s):     36-44,108-116
      NUMA node5 CPU(s):     45-53,117-125
      NUMA node6 CPU(s):     54-62,126-134
      NUMA node7 CPU(s):     63-71,135-143
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
      pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
      aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
      fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
      xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
      tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
      erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
      avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

  /proc/cpuinfo cache data
     cache size : 25344 KB

  From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
    available: 8 nodes (0-7)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 72 73 74 75 76 77 78 79 80
    node 0 size: 96348 MB
    node 0 free: 84032 MB
    node 1 cpus: 9 10 11 12 13 14 15 16 17 81 82 83 84 85 86 87 88 89
    node 1 size: 96765 MB
    node 1 free: 80635 MB
    node 2 cpus: 18 19 20 21 22 23 24 25 26 90 91 92 93 94 95 96 97 98
    node 2 size: 96765 MB
    node 2 free: 86190 MB
    node 3 cpus: 27 28 29 30 31 32 33 34 35 99 100 101 102 103 104 105 106 107
    node 3 size: 96765 MB
    node 3 free: 84433 MB
    node 4 cpus: 36 37 38 39 40 41 42 43 44 108 109 110 111 112 113 114 115 116
    node 4 size: 96765 MB
    node 4 free: 85909 MB
    node 5 cpus: 45 46 47 48 49 50 51 52 53 117 118 119 120 121 122 123 124 125
    node 5 size: 96765 MB
    node 5 free: 86809 MB
    node 6 cpus: 54 55 56 57 58 59 60 61 62 126 127 128 129 130 131 132 133 134
```

(Continued on next page)

Platform Notes (Continued)

```
   node 6 size: 96765 MB
   node 6 free: 86769 MB
   node 7 cpus: 63 64 65 66 67 68 69 70 71 135 136 137 138 139 140 141 142 143
   node 7 size: 96763 MB
   node 7 free: 86795 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  21  31  31  31  31  31  31
     1:  21  10  31  31  31  31  31  31
     2:  31  31  10  21  31  31  31  31
     3:  31  31  21  10  31  31  31  31
     4:  31  31  31  31  10  21  31  31
     5:  31  31  31  31  21  10  31  31
     6:  31  31  31  31  31  31  10  21
     7:  31  31  31  31  31  31  21  10

  From /proc/meminfo
     MemTotal:       792273344 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     SUSE Linux Enterprise Server 12 SP2

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 2
        # This file is deprecated and will be removed in a future service pack or release.
        # Please check /etc/os-release for details about this release.
     os-release:
        NAME="SLES"
        VERSION="12-SP2"
        VERSION_ID="12.2"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp2"

  uname -a:
     Linux linux-z88l 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Nov 29 14:56

  SPEC is set to: /home/cpu2017
```

(Continued on next page)

Platform Notes (Continued)

```
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sdb4      xfs   400G  152G  249G  38% /home

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS HPE U34 09/29/2017
   Memory:
    48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
 CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
     557.xz_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
     541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  548.exchange2_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

(Continued on next page)

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

(Continued on next page)

Base Other Flags (Continued)

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-12-01 14:54:22-0500.
Report generated on 2018-10-31 17:12:10 by CPU2017 PS formatter v6067.
Originally published on 2018-01-14.