**Lenovo Global Technology**  
ThinkSystem SR950  
(2.10 GHz, Intel Xeon Gold 6130T)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>153</td>
<td>153</td>
</tr>
</tbody>
</table>

**Hardware**

<table>
<thead>
<tr>
<th>Spec</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6130T</td>
</tr>
<tr>
<td>Max MHz.</td>
<td>3700</td>
</tr>
<tr>
<td>Nominal</td>
<td>2100</td>
</tr>
<tr>
<td>Enabled</td>
<td>64 cores, 4 chips</td>
</tr>
<tr>
<td>Orderable</td>
<td>2,4 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>22 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Memory</td>
<td>1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 800 GB SAS SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

**Software**

<table>
<thead>
<tr>
<th>Spec</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 12 SP2 (x86_64)</td>
</tr>
<tr>
<td>Kernel</td>
<td>4.4.21-69-default</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++</td>
</tr>
<tr>
<td>Fortran</td>
<td>Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran</td>
</tr>
<tr>
<td>Firmware</td>
<td>Lenovo BIOS Version PSE105X 1.00 released Aug-2017</td>
</tr>
<tr>
<td>File System</td>
<td>btrfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>
SPEC CPU2017 Floating Point Speed Result

Lenovo Global Technology
ThinkSystem SR950
(2.10 GHz, Intel Xeon Gold 6130T)

SPECspeed2017_fp_base = 153
SPECspeed2017_fp_peak = 153

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>64</td>
<td>65.9</td>
<td>896</td>
<td>66.3</td>
<td>890</td>
<td>65.6</td>
<td>900</td>
<td>66.2</td>
<td>891</td>
<td>66.2</td>
<td>893</td>
<td>66.2</td>
<td>891</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>64</td>
<td>87.9</td>
<td>190</td>
<td>87.9</td>
<td>190</td>
<td>87.8</td>
<td>190</td>
<td>87.8</td>
<td>190</td>
<td>87.8</td>
<td>190</td>
<td>87.8</td>
<td>190</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>64</td>
<td>66.7</td>
<td>78.5</td>
<td>66.3</td>
<td>79.0</td>
<td>66.0</td>
<td>79.4</td>
<td>66.7</td>
<td>78.6</td>
<td>66.7</td>
<td>78.6</td>
<td>66.7</td>
<td>78.6</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>64</td>
<td>181</td>
<td>73.1</td>
<td>183</td>
<td>72.3</td>
<td>184</td>
<td>72.0</td>
<td>183</td>
<td>72.4</td>
<td>183</td>
<td>72.4</td>
<td>183</td>
<td>72.4</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>64</td>
<td>79.7</td>
<td>111</td>
<td>79.4</td>
<td>112</td>
<td>84.2</td>
<td>105</td>
<td>80.5</td>
<td>110</td>
<td>80.5</td>
<td>110</td>
<td>80.5</td>
<td>110</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>64</td>
<td>219</td>
<td>54.2</td>
<td>216</td>
<td>55.0</td>
<td>216</td>
<td>54.9</td>
<td>219</td>
<td>54.3</td>
<td>219</td>
<td>54.3</td>
<td>219</td>
<td>54.3</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>64</td>
<td>89.9</td>
<td>161</td>
<td>89.4</td>
<td>161</td>
<td>92.7</td>
<td>156</td>
<td>94.8</td>
<td>152</td>
<td>94.3</td>
<td>153</td>
<td>90.4</td>
<td>160</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>64</td>
<td>61.5</td>
<td>284</td>
<td>61.6</td>
<td>284</td>
<td>61.4</td>
<td>284</td>
<td>61.7</td>
<td>283</td>
<td>61.7</td>
<td>283</td>
<td>61.7</td>
<td>283</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>64</td>
<td>80.5</td>
<td>113</td>
<td>85.6</td>
<td>106</td>
<td>82.0</td>
<td>111</td>
<td>81.6</td>
<td>112</td>
<td>81.1</td>
<td>112</td>
<td>81.1</td>
<td>112</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>64</td>
<td>68.7</td>
<td>229</td>
<td>67.2</td>
<td>234</td>
<td>70.8</td>
<td>223</td>
<td>65.3</td>
<td>241</td>
<td>65.3</td>
<td>241</td>
<td>65.3</td>
<td>241</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 153
SPECspeed2017_fp_peak = 153

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
MONITORMWAIT set to Enable
DCU Streamer Prefetcher set to Disable
XPT Prefetcher set to Enable
Stale AtoS set to Enable
DCA set to Enable
Trusted Execution Technology set to Enable

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology
ThinkSystem SR950
(2.10 GHz, Intel Xeon Gold 6130T)

SPECspeed2017_fp_base = 153
SPECspeed2017_fp_peak = 153

CPU2017 License: 9017
Test Date: Dec-2017
Test Sponsor: Lenovo Global Technology
Hardware Availability: Sep-2017
Tested by: Lenovo Global Technology
Software Availability: Sep-2017

Platform Notes (Continued)

LLC Deadline Alloc set to Disable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on Proton4S-SUSE12SP2 Tue Dec 19 00:11:12 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6130T CPU @ 2.10GHz
 4 "physical id"s (chips)
 64 "processors"
core, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6130T CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2095.078
BogoMIPS: 4190.15
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
NUMA node2 CPU(s): 32-47

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
(2.10 GHz, Intel Xeon Gold 6130T)

SPECspeed2017_fp_base = 153
SPECspeed2017_fp_peak = 153

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Dec-2017
Tested by: Lenovo Global Technology
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Platform Notes (Continued)

NUMA node3 CPU(s): 48-63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfp u pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcdm dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
ermv invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occupa llc

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 385107 MB
node 0 free: 385107 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 size: 387042 MB
node 1 free: 387042 MB
node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
node 2 size: 387042 MB
node 2 free: 387042 MB
node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
node 3 size: 387038 MB
node 3 free: 385837 MB
node distances:
node 0 1 2 3
0: 10 21 21 21
1: 21 10 21 21
2: 21 21 10 21
3: 21 21 21 10

From /proc/meminfo
MemTotal: 1584766852 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
(2.10 GHz, Intel Xeon Gold 6130T)

SPECspeed2017_fp_base = 153
SPECspeed2017_fp_peak = 153

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Dec-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Platform Notes (Continued)
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux Proton4S-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 18 19:06

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 btrfs 743G 193G 550G 26% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[PSE105X-1.00]- 08/17/2017
Memory:
48x NO DIMM NO DIMM
48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  619.lbm_s(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
(2.10 GHz, Intel Xeon Gold 6130T)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
</tr>
</tbody>
</table>

**SPEC CPU2017 Floating Point Speed Result**

**SPECspeed2017_fp_base** = 153
**SPECspeed2017_fp_peak** = 153

---

**Compiler Version Notes (Continued)**

---

FC 607.cactuBSSN_s(base)
!
---

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

FC 607.cactuBSSN_s(peak)
!
---

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
!
---

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
!
---

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
!
---

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
(2.10 GHz, Intel Xeon Gold 6130T)

SPECspeed2017_fp_base = 153
SPECspeed2017_fp_peak = 153

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Compiler Version Notes (Continued)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactusBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

(Continued on next page)
**Lenovo Global Technology**

ThinkSystem SR950  
(2.10 GHz, Intel Xeon Gold 6130T)  

**SPECspeed2017_fp_base = 153**  
**SPECspeed2017_fp_peak = 153**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
<th>Test Date:</th>
<th>Dec-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
<td>Hardware Availability:</td>
<td>Sep-2017</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

---

### Base Optimization Flags (Continued)

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -gopt-prefetch  
-ffinite-math-only -gopt-mem-layout-trans=3 -qopenmp  
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -gopt-prefetch  
-ffinite-math-only -gopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -gopt-prefetch  
-ffinite-math-only -gopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs -align array32byte

---

### Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

---

### Peak Compiler Invocation

C benchmarks:
```
icc
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
ifort icc
```
Lenovo Global Technology
ThinkSystem SR950
(2.10 GHz, Intel Xeon Gold 6130T)

SPECspeed2017_fp_base = 153
SPECspeed2017_fp_peak = 153

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Dec-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:

-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
(2.10 GHz, Intel Xeon Gold 6130T)

SPECspeed2017_fp_peak = 153
SPECspeed2017_fp_base = 153

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml