## SPEC CPU®2017 Floating Point Speed Result

**Cisco Systems**

Cisco UCS B200 M5 (Intel Xeon Gold 6136, 3.00 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>104</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>105</td>
</tr>
</tbody>
</table>

CPU2017 License: 9019  
Test Sponsor: Cisco Systems  
Test Date: Dec-2017  
Hardware Availability: Aug-2017

<table>
<thead>
<tr>
<th>Software</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 12 SP2 (x86_64)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>Yes</td>
</tr>
<tr>
<td>Firmware</td>
<td>Version 3.2.1d released Jul-2017</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Power Management</td>
<td>--</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_fp_base (104)</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>24</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>24</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>24</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>24</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>24</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>24</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>24</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>24</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>24</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>24</td>
</tr>
</tbody>
</table>

CPU Name: Intel Xeon Gold 6136  
Max MHz: 3700  
Nominal: 3000  
Enabled: 24 cores, 2 chips  
Orderable: 1.2 Chips  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 24.75 MB I+D on chip per chip  
Other: None  
Memory: 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R)  
Storage: 1 x 600 GB SAS HDD, 10K RPM  
Other: None
Cisco Systems
Cisco UCS B200 M5 (Intel Xeon Gold 6136, 3.00 GHz)

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>Base</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>603.bwaves_s</td>
<td>24</td>
<td>123</td>
<td>481</td>
<td>123</td>
<td>479</td>
<td>123</td>
<td>479</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>24</td>
<td>128</td>
<td>130</td>
<td>128</td>
<td>130</td>
<td>128</td>
<td>130</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>24</td>
<td>119</td>
<td>44.0</td>
<td>119</td>
<td>44.2</td>
<td>119</td>
<td>44.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>24</td>
<td>155</td>
<td>85.3</td>
<td>156</td>
<td>84.7</td>
<td>156</td>
<td>85.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>24</td>
<td>132</td>
<td>67.1</td>
<td>132</td>
<td>67.0</td>
<td>132</td>
<td>67.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>24</td>
<td>176</td>
<td>67.3</td>
<td>178</td>
<td>66.6</td>
<td>174</td>
<td>68.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>24</td>
<td>164</td>
<td>88.2</td>
<td>163</td>
<td>88.7</td>
<td>163</td>
<td>88.8</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td>24</td>
<td>108</td>
<td>162</td>
<td>108</td>
<td>162</td>
<td>108</td>
<td>162</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>24</td>
<td>111</td>
<td>81.4</td>
<td>112</td>
<td>81.3</td>
<td>113</td>
<td>80.7</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>24</td>
<td>134</td>
<td>118</td>
<td>134</td>
<td>117</td>
<td>134</td>
<td>117</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Peak</th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>24</td>
<td>124</td>
<td>477</td>
<td>124</td>
<td>477</td>
<td>124</td>
<td>476</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>24</td>
<td>127</td>
<td>131</td>
<td>127</td>
<td>132</td>
<td>127</td>
<td>132</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
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<td>24</td>
<td>119</td>
<td>44.2</td>
<td>119</td>
<td>44.0</td>
<td>119</td>
<td>44.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>24</td>
<td>148</td>
<td>89.3</td>
<td>148</td>
<td>89.9</td>
<td>148</td>
<td>89.4</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>24</td>
<td>132</td>
<td>67.1</td>
<td>132</td>
<td>67.0</td>
<td>132</td>
<td>67.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td>24</td>
<td>175</td>
<td>67.9</td>
<td>175</td>
<td>68.1</td>
<td>173</td>
<td>68.6</td>
<td>171</td>
<td>69.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>24</td>
<td>161</td>
<td>89.5</td>
<td>160</td>
<td>89.5</td>
<td>163</td>
<td>88.3</td>
<td>170</td>
<td>84.9</td>
<td></td>
<td></td>
</tr>
<tr>
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<td>162</td>
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<td></td>
<td></td>
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</tr>
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<td>24</td>
<td>114</td>
<td>80.0</td>
<td>115</td>
<td>79.6</td>
<td>114</td>
<td>79.8</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>24</td>
<td>129</td>
<td>122</td>
<td>129</td>
<td>122</td>
<td>129</td>
<td>122</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4 Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
```
sync; echo 3> /proc/sys/vm/drop_caches
```
No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly

(Continued on next page)
Cisco Systems
Cisco UCS B200 M5 (Intel Xeon Gold 6136, 3.00 GHz)

SPECspeed\textsuperscript{\textregistered}2017\_fp\_base = 104
SPECspeed\textsuperscript{\textregistered}2017\_fp\_peak = 105

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Dec-2017
Hardware Availability: Aug-2017
Software Availability: Sep-2017

General Notes (Continued)

generally available. At the time of this publication, it may
not be shipping, and/or may not be supported, and/or may fail
to meet other tests of General Availability described in the

This measured result may not be representative of the result
that would be measured were this benchmark run with hardware
and software available as of the publication date.

Platform Notes

BIOS Settings:
Intel HyperThreading Technology set to Disabled
CPU performance set to Enterprise
Power Performance Tuning set to OS Controls
SNC set to Disabled
Patrol Scrub set to Disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-mys2 Mon Dec 18 21:33:22 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz
  2 "physical id"s (chips)
  24 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 12
  siblings : 12
  physical 0: cores 0 1 2 3 4 8 9 11 17 18 19 20
  physical 1: cores 0 1 4 9 10 11 17 18 24 25 26 27

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 24
  On-line CPU(s) list: 0-23
  Thread(s) per core: 1
  Core(s) per socket: 12
  Socket(s): 2

(Continued on next page)
Platform Notes (Continued)

NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz
Stepping:              4
CPU MHz:               1452.114
CPU max MHz:           3700.0000
CPU min MHz:           1200.0000
BogoMIPS:              5999.99
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              25344K
NUMA node0 CPU(s):     0-11
NUMA node1 CPU(s):     12-23
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
                       lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
                       aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
                       fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
                       xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm hwp
                       hwp_act_window hwp_epp hwp_pkg_req intel_pt tpr_shadow vnmi flexpriority ept vpid
                       fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3dnow invpcid rtm cqm mpx avx512f
                       avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec
                       xgetbv1 cqm_llc cqm_occup_llc

/proc/cpuinfo cache data
cache size : 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
   available: 2 nodes (0-1)
   node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
   node 0 size: 192074 MB
   node 0 free: 185932 MB
   node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23
   node 1 size: 193504 MB
   node 1 free: 191113 MB
   node distances:
      node 0: 0 1
      node 1: 10 21

From /proc/meminfo
   MemTotal:    394832468 kB

(Continued on next page)
Cisco Systems
Cisco UCS B200 M5 (Intel Xeon Gold 6136, 3.00 GHz)

SPECspeed®2017_fp_base = 104
SPECspeed®2017_fp_peak = 105

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems
Test Date: Dec-2017
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Platform Notes (Continued)

```
HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release*/etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 2
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
    os-release:
        NAME="SLES"
        VERSION="12-SP2"
        VERSION_ID="12.2"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
    Linux linux-mys2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
    x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 13 14:24

SPEC is set to: /home/cpu2017
    Filesystem Type Size Used Avail Use% Mounted on
    /dev/sda3 xfs 182G 61G 121G 34% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS Cisco Systems, Inc. B200M5.3.2.1d.5.0727171353 07/27/2017
    Memory:
        24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)
```

Compiler Version Notes

```
C        619.lbm_s(base, peak) 638.imagick_s(base, peak)
```

(Continued on next page)
## Cisco Systems

Cisco UCS B200 M5 (Intel Xeon Gold 6136, 3.00 GHz)

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<thead>
<tr>
<th>SPECspeed®2017_fp_base = 104</th>
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<tbody>
<tr>
<td>SPECspeed®2017_fp_peak = 105</td>
</tr>
</tbody>
</table>

### Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>644.nab_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>icc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>607.cactuBSSN_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>C++, C, Fortran</td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>603.bwaves_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>icpc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>icc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>ifort (IFORT) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>649.fotonik3d_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fortran</td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>654.roms_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>ifort (IFORT) 18.0.0 20170811</td>
</tr>
<tr>
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</tr>
</tbody>
</table>

---

### Base Compiler Invocation

<table>
<thead>
<tr>
<th>C benchmarks:</th>
</tr>
</thead>
<tbody>
<tr>
<td>icc</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Fortran benchmarks:</th>
</tr>
</thead>
<tbody>
<tr>
<td>ifort</td>
</tr>
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</table>

Benchmarks using both Fortran and C:
| ifort icc |

(Continued on next page)
Cisco Systems
Cisco UCS B200 M5 (Intel Xeon Gold 6136, 3.00 GHz)

| SPECspeed®2017_fp_base = 104 |
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| Test Date: Dec-2017 |
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| Software Availability: Sep-2017 |

Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte
Cisco Systems

Cisco UCS B200 M5 (Intel Xeon Gold 6136, 3.00 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base = 104</th>
</tr>
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</table>

**Base Other Flags**

C benchmarks:
- `-m64 -std=c11`

Fortran benchmarks:
- `-m64`

Benchmarks using both Fortran and C:
- `-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11`

**Peak Compiler Invocation**

C benchmarks:
- `icc`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:
- `619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP`

(Continued on next page)
Cisco Systems
Cisco UCS B200 M5 (Intel Xeon Gold 6136, 3.00 GHz)

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Peak Optimization Flags (Continued)

638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:

-m64

Benchmarks using both Fortran and C:

-m64 -std=c11

Benchmarks using Fortran, C, and C++:

-m64 -std=c11
## Cisco Systems

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The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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