## SPEC CPU2017 Floating Point Rate Result

<table>
<thead>
<tr>
<th>Lenovo Global Technology</th>
<th>SPECrate2017_fp_base  =  80.9</th>
</tr>
</thead>
<tbody>
<tr>
<td>ThinkSystem SR590 (2.00 GHz, Intel Xeon Silver 4109T)</td>
<td>SPECrate2017_fp_peak  =  82.9</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>Lenovo Global Technology</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Date:</td>
<td>Dec-2017</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Nov-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon Silver 4109T</td>
</tr>
<tr>
<td>Max MHz.:</td>
<td>3000</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2000</td>
</tr>
<tr>
<td>Enabled:</td>
<td>16 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>11 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 800 GB SAS SSD</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS:</td>
<td>SUSE Linux Enterprise Server 12 SP3 (x86_64)</td>
</tr>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;</td>
</tr>
<tr>
<td></td>
<td>Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux;</td>
</tr>
<tr>
<td>Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Lenovo BIOS Version TEE119J 1.20 released Sep-2017</td>
</tr>
<tr>
<td>File System:</td>
<td>btrfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
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<thead>
<tr>
<th>SPECrate2017_fp_base (80.9)</th>
<th>SPECrate2017_fp_peak (82.9)</th>
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<tbody>
<tr>
<td>503.bwaves_r 32</td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r 32</td>
<td></td>
</tr>
<tr>
<td>508.namd_r 32</td>
<td></td>
</tr>
<tr>
<td>510.parest_r 32</td>
<td></td>
</tr>
<tr>
<td>511.povray_r 32</td>
<td></td>
</tr>
<tr>
<td>519.lbm_r 32</td>
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<tr>
<td>521.wrf_r 32</td>
<td></td>
</tr>
<tr>
<td>526.blender_r 32</td>
<td></td>
</tr>
<tr>
<td>527.cam4_r 32</td>
<td></td>
</tr>
<tr>
<td>538.imagick_r 32</td>
<td></td>
</tr>
<tr>
<td>544.nab_r 32</td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r 32</td>
<td></td>
</tr>
<tr>
<td>554.roms_r 32</td>
<td></td>
</tr>
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---

**Test Sponsor:** Lenovo Global Technology
---

**SPEC CPU2017 Floating Point Rate Result**

Lenovo Global Technology  
ThinkSystem SR590  
(2.00 GHz, Intel Xeon Silver 4109T)

**CPU2017 License:** 9017  
**Test Date:** Dec-2017  
**Test Sponsor:** Lenovo Global Technology  
**Hardware Availability:** Nov-2017  
**Tested by:** Lenovo Global Technology  
**Software Availability:** Sep-2017

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
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<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
<td>1053</td>
<td>305</td>
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<td><strong>1055 304</strong></td>
<td>32</td>
<td>1053</td>
<td>305</td>
<td>1053</td>
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<td>604</td>
<td>67.0</td>
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<td>65.4</td>
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<td>53.6</td>
<td></td>
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<tr>
<td>510.parest_r</td>
<td>32</td>
<td>1448</td>
<td>57.8</td>
<td>1452</td>
<td>57.7</td>
<td><strong>1449 57.8</strong></td>
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<td>1446</td>
<td>57.9</td>
<td>1450</td>
<td>57.7</td>
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<tr>
<td>511.povray_r</td>
<td>32</td>
<td>862</td>
<td>86.7</td>
<td><strong>863 86.6</strong></td>
<td>868</td>
<td>86.1</td>
<td>32</td>
<td><strong>750 99.6</strong></td>
<td>741</td>
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<tr>
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<tr>
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<td>73.7</td>
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<tr>
<td>527.cam4_r</td>
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<td>32</td>
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<td>852</td>
<td>65.7</td>
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<tr>
<td>538.imagick_r</td>
<td>32</td>
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<td>105</td>
<td>757</td>
<td>105</td>
<td><strong>757 105</strong></td>
<td>32</td>
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<td>105</td>
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<td>105</td>
<td>757</td>
<td>105</td>
</tr>
<tr>
<td>544.nab_r</td>
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<td>93.9</td>
<td>577</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
<td>1424</td>
<td>87.6</td>
<td><strong>1424 87.6</strong></td>
<td>1425</td>
<td>87.5</td>
<td>32</td>
<td>1423</td>
<td>87.6</td>
<td>1425</td>
<td>87.5</td>
<td><strong>1424 87.6</strong></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
<td>1004</td>
<td>50.6</td>
<td>1001</td>
<td><strong>50.8</strong></td>
<td>1001</td>
<td>50.8</td>
<td>32</td>
<td>968</td>
<td>52.6</td>
<td><strong>969 52.5</strong></td>
<td>970</td>
<td>52.4</td>
</tr>
</tbody>
</table>

**SPECrate2017_fp_base =** 80.9  
**SPECrate2017_fp_peak =** 82.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

- `LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"`
- `LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"`
- Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
- memory using Redhat Enterprise Linux 7.4
- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with:
  - `sync; echo 3 > /proc/sys/vm/drop_caches`
- runcpu command invoked through numactl i.e.:
  - `numactl --interleave=all runcpu <etc>`

---

Page 2  
Standard Performance Evaluation Corporation (info@spec.org)  
https://www.spec.org/
Lenovo Global Technology
ThinkSystem SR590
(2.00 GHz, Intel Xeon Silver 4109T)

SPECrate2017_fp_base = 80.9
SPECrate2017_fp_peak = 82.9

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
DCU Streamer Prefetcher set to Disable
MONITORMWAIT set to Enable
XTT Prefetcher set to Enable
Stale AtoS set to Enable
LLC Deadline Alloc set to Disable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b0c91c0f
running on SR590-1 Thu Dec 14 20:30:00 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
           2 "physical id"s (chips)
            32 "processors"
 cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
 cpu cores : 8
 siblings : 16
 physical 0: cores 0 1 2 3 4 5 6 7
 physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 32
  On-line CPU(s) list: 0-31
  Thread(s) per core: 2
  Core(s) per socket: 8
  Socket(s): 2
  NUMA node(s): 2
  Vendor ID: GenuineIntel
  CPU family: 6
  Model: 85
  Model name: Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
  Stepping: 4
  CPU MHz: 1995.326
  BogoMIPS: 3990.65
  Virtualization: VT-x
  L1d cache: 32K
  L1i cache: 32K
  L2 cache: 1024K

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR590
(2.00 GHz, Intel Xeon Silver 4109T)

SPEC CPU2017 Floating Point Rate Result
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SPECrate2017_fp_base = 80.9
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Dec-2017
Hardware Availability: Nov-2017
Software Availability: Sep-2017

Platform Notes (Continued)

L3 cache: 11264K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpre pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pti
msr_flag vmsacq perf_tsc dpms ept vpid cmovunts cqm mpx avx512cd avx512bw avx512vl
avx2 smep bmi2 erms invpcid rtm cqo mpx avx512f vfp16 vfp2 vfp3 vfp4 avx16f xsaveopt
xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

/proc/cpuinfo cache data
    cache size: 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
    node 0 size: 193123 MB
    node 0 free: 192100 MB
    node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
    node 1 size: 193516 MB
    node 1 free: 192638 MB
    node distances:
        node 0 1
        0: 10 21
        1: 21 10

From /proc/meminfo
    MemTotal: 395919828 kB
    HugePages_Total: 0
    Hugepagesize: 2048 kB

From /etc/*release*/ /etc/*version*
    SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 3
        # This file is deprecated and will be removed in a future service pack or release.
        # Please check /etc/os-release for details about this release.
    os-release:
        NAME="SLES"
        VERSION="12-SP3"
        VERSION_ID="12.3"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"

(Continued on next page)
Platform Notes (Continued)

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
    Linux SR590-1 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017 (b7ce4e4) x86_64
    x86_64 x86_64 GNU/Linux

run-level 3 Dec 14 09:05

SPEC is set to: /home/cpu2017.1.0.2.ic18.0

Filesystem     Type   Size  Used Avail Use% Mounted on
/dev/sdb2      btrfs  744G  130G  615G  18% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS Lenovo -[TEE119J-1.20]- 09/06/2017
    Memory:
        12x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666, configured at 2400
        4x NO DIMM NO DIMM
```

Compiler Version Notes

```
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
-----------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

CC  519.lbm_r(peak) 544.nab_r(peak)
-----------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

CXXC 508.namd_r(base) 510.parest_r(base)
-----------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
```

(Continued on next page)
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ThinkSystem SR590
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Compiler Version Notes (Continued)

==============================================================================
CXXC 508.namd_r(peak) 510.parest_r(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 511.povray_r(base) 526.blender_r(base)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 511.povray_r(peak) 526.blender_r(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
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FC 507.cactuBSSN_r(base)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 507.cactuBSSN_r(peak)
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(Continued on next page)
## Lenovo Global Technology

**ThinkSystem SR590**  
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### Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------
 FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
```

```
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```
------------------------------------------------------------------------------
 FC  554.roms_r(peak)
```

```
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```
------------------------------------------------------------------------------
 CC  521.wrf_r(base) 527.cam4_r(base)
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```
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icc (ICC) 18.0.0 20170811
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```
------------------------------------------------------------------------------
 CC  521.wrf_r(peak) 527.cam4_r(peak)
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Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

**C benchmarks:**

- icc

**C++ benchmarks:**

- icpc

**Fortran benchmarks:**

- ifort

(Continued on next page)
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<td>Software Availability: Sep-2017</td>
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</table>

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR590
(2.00 GHz, Intel Xeon Silver 4109T)

SPECrate2017_fp_base = 80.9
SPECrate2017_fp_peak = 82.9

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Dec-2017
Hardware Availability: Nov-2017
Software Availability: Sep-2017

Base Optimization Flags (Continued)

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

(Continued on next page)
Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 519.lbm_r

C++ benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs

(Continued on next page)
Lenovo Global Technology
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Peak Optimization Flags (Continued)

554.roms_r (continued):
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
## Lenovo Global Technology

**ThinkSystem SR590**  
(2.00 GHz, Intel Xeon Silver 4109T)

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You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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