## SPEC® CPU2017 Floating Point Speed Result

**Huawei**

**Huawei CH121 V5 (Intel Xeon Gold 5120)**

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei  
**Test Date:** Jan-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Sep-2017

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>28</td>
<td>38.0</td>
<td>97.0</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>28</td>
<td>116</td>
<td>134</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>28</td>
<td>75.0</td>
<td>102</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>28</td>
<td>60.6</td>
<td>71.2</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>28</td>
<td>60.8</td>
<td>52.1</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>28</td>
<td>54.8</td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>28</td>
<td>75.0</td>
<td>134</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>28</td>
<td>72.7</td>
<td>134</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>28</td>
<td>97.0</td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>28</td>
<td>102</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5120  
- **Max MHz.:** 3200  
- **Nominal:** 2200  
- **Enabled:** 28 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 19.25 MB I+D on chip per core  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 1200 GB SAS, 10000 RPM  
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
- **Fortran:** Version 18.0.0.128 of Intel Fortran  
- **Parallel:** Yes  
- **Firmware:** Version 0.31 Released Sep-2017  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
Huawei

Huawei CH121 V5 (Intel Xeon Gold 5120)

SPECspeed2017_fp_base = 87.7
SPECspeed2017_fp_peak = 89.6

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>28</td>
<td>139</td>
<td>426</td>
<td>140</td>
<td>423</td>
<td>139</td>
<td>424</td>
<td>28</td>
<td>138</td>
<td>427</td>
<td>140</td>
<td>423</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>28</td>
<td>146</td>
<td>114</td>
<td>146</td>
<td>114</td>
<td>28</td>
<td>143</td>
<td>116</td>
<td>143</td>
<td>117</td>
<td>143</td>
<td>116</td>
</tr>
<tr>
<td>619.ibm_s</td>
<td>28</td>
<td>138</td>
<td>37.9</td>
<td>138</td>
<td>38.0</td>
<td>138</td>
<td>38.0</td>
<td>28</td>
<td>138</td>
<td>37.9</td>
<td>138</td>
<td>38.0</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>28</td>
<td>201</td>
<td>65.9</td>
<td>202</td>
<td>65.6</td>
<td>201</td>
<td>65.8</td>
<td>28</td>
<td>186</td>
<td>71.2</td>
<td>185</td>
<td>71.4</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>28</td>
<td>146</td>
<td>60.6</td>
<td>146</td>
<td>60.6</td>
<td>145</td>
<td>61.1</td>
<td>28</td>
<td>146</td>
<td>60.7</td>
<td>146</td>
<td>60.8</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>28</td>
<td>228</td>
<td>52.0</td>
<td>226</td>
<td>52.6</td>
<td>228</td>
<td>52.1</td>
<td>28</td>
<td>216</td>
<td>54.9</td>
<td>217</td>
<td>54.8</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>28</td>
<td>192</td>
<td>75.1</td>
<td>193</td>
<td>74.7</td>
<td>192</td>
<td>75.0</td>
<td>28</td>
<td>192</td>
<td>75.0</td>
<td>192</td>
<td>75.2</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>28</td>
<td>131</td>
<td>134</td>
<td>130</td>
<td>134</td>
<td>130</td>
<td>134</td>
<td>28</td>
<td>131</td>
<td>134</td>
<td>131</td>
<td>134</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>28</td>
<td>125</td>
<td>72.8</td>
<td>125</td>
<td>72.7</td>
<td>126</td>
<td>72.2</td>
<td>28</td>
<td>125</td>
<td>72.8</td>
<td>125</td>
<td>72.7</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>28</td>
<td>162</td>
<td>97.1</td>
<td>164</td>
<td>95.9</td>
<td>162</td>
<td>97.0</td>
<td>28</td>
<td>155</td>
<td>102</td>
<td>154</td>
<td>102</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

(Continued on next page)
Huawei

Huawei CH121 V5 (Intel Xeon Gold 5120)

| SPECspeed2017_fp_base = 87.7 |
| SPECspeed2017_fp_peak = 89.6 |

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

General Notes (Continued)

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS configuration:
Power Efficiency Mode Set to Custom
Hyper-Threading Set to Disable
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-hyg4 Mon Jan 22 11:14:57 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 5120 CPU @ 2.20GHz
  2 "physical id"s (chips)
  28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 14
siblings : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

From lscpu:
  Architecture:       x86_64
  CPU op-mode(s):     32-bit, 64-bit
  Byte Order:         Little Endian
  CPU(s):             28
  On-line CPU(s) list: 0-27
  Thread(s) per core: 1
  Core(s) per socket: 14
  Socket(s):          2
  NUMA node(s):       2
  Vendor ID:          GenuineIntel
  CPU family:         6

(Continued on next page)
Huawei CH121 V5 (Intel Xeon Gold 5120)

CPU2017 License: 3175
Test Sponsor: Huawei
Hardware Availability: Jul-2017
Test Date: Jan-2018
Software Availability: Sep-2017

Platform Notes (Continued)

Model: 85
Model name: Intel(R) Xeon(R) Gold 5120 CPU @ 2.20GHz
Stepping: 4
CPU MHz: 1000.000
CPU max MHz: 2201.0000
CPU min MHz: 1000.0000
BogoMIPS: 4399.98
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 19712K
NUMA node0 CPU(s): 0-13
NUMA node1 CPU(s): 14-27
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pearch perfblr bts rep_good nopl xtopology nonstop_tsc
aperfperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vmm灵活priority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

/proc/cpuinfo cache data

cache size : 19712 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13
node 0 size: 191498 MB
node 0 free: 189444 MB
node 1 cpus: 14 15 16 17 18 19 20 21 22 23 24 25 26 27
node 1 size: 193412 MB
node 1 free: 191984 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo

MemTotal: 394148704 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Huawei
Huawei CH121 V5 (Intel Xeon Gold 5120)

SPECspeed2017_fp_base = 87.7
SPECspeed2017_fp_peak = 89.6

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jan-2018
Tested by: Huawei
Hardware Availability: Jul-2017
Software Availability: Sep-2017

Platform Notes (Continued)

SuSE-release:
   SUSE Linux Enterprise Server 12 (x86_64)
   VERSION = 12
   PATCHLEVEL = 2
   # This file is deprecated and will be removed in a future service pack or release.
   # Please check /etc/os-release for details about this release.
   os-release:
   NAME="SLES"
   VERSION="12-SP2"
   VERSION_ID="12.2"
   PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
   ID="sles"
   ANSI_COLOR="0;32"
   CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-hyq4 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
   x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jan 21 12:00
SPEC is set to: /spec2017
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      xfs   828G   57G  772G   7% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS INSYDE Corp. 0.31 09/29/2017
   Memory:
      24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
  CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
==============================================================================
  icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
  CC  619.lbm_s(peak)
(Continued on next page)
Huawei
Huawei CH121 V5 (Intel Xeon Gold 5120)

SPECspeed2017_fp_base = 87.7
SPECspeed2017_fp_peak = 89.6

CPU2017 License: 3175
Test Sponsor: Huawei
Software Availability: Sep-2017

Test Date: Jan-2018
Hardware Availability: Jul-2017

Tested by: Huawei

Compiler Version Notes (Continued)

-------------------------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------------------------------------------------------------------------------------
FC 607.cactuBSSN_s(base)

-------------------------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------------------------------------------------------------------------------------
FC 607.cactuBSSN_s(peak)

-------------------------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------------------------------------------------------------------------------------
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)

-------------------------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------------------------------------------------------------------------------------
FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)

-------------------------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------------------------------------------------------------------------------------
CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)

-------------------------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811

(Continued on next page)
Huawei

Huawei CH121 V5 (Intel Xeon Gold 5120)

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_base</td>
<td>87.7</td>
</tr>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>89.6</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jan-2018
Hardware Availability: Jul-2017
Software Availability: Sep-2017

Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

CC 621.wrf_s(peak) 628.pop2_s(peak)

---

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
## SPEC CPU2017 Floating Point Speed Result

<table>
<thead>
<tr>
<th>Huawei CH121 V5 (Intel Xeon Gold 5120)</th>
<th>SPECspeed2017_fp_base = 87.7</th>
<th>SPECspeed2017_fp_peak = 89.6</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License: 3175</td>
<td>Test Date: Jan-2018</td>
<td>Hardware Availability: Jul-2017</td>
</tr>
<tr>
<td>Test Sponsor: Huawei</td>
<td>Tested by: Huawei</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

### Base Optimization Flags

**C benchmarks:**
- `-xCORE-AVX2` -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- `-qopt-mem-layout-trans=3` -qopenmp -DSPEC_OPENMP

**Fortran benchmarks:**
- `-DSPEC_OPENMP` `-xCORE-AVX2` -ipo -O3 -no-prec-div -qopt-prefetch
- `-ffinite-math-only` `-qopt-mem-layout-trans=3` -qopenmp
- `-nostandard-realloc-lhs` -align array32byte

**Benchmarks using both Fortran and C:**
- `-xCORE-AVX2` -ipo -O3 -no-prec-div -qopt-prefetch `-ffinite-math-only`
- `-qopt-mem-layout-trans=3` -qopenmp -DSPEC_OPENMP
- `-nostandard-realloc-lhs` -align array32byte

**Benchmarks using Fortran, C, and C++:**
- `-xCORE-AVX2` -ipo -O3 -no-prec-div -qopt-prefetch `-ffinite-math-only`
- `-qopt-mem-layout-trans=3` -qopenmp -DSPEC_OPENMP
- `-nostandard-realloc-lhs` -align array32byte

### Base Other Flags

**C benchmarks:**
- `-m64` `-std=c11`

**Fortran benchmarks:**
- `-m64`

**Benchmarks using both Fortran and C:**
- `-m64` `-std=c11`

**Benchmarks using Fortran, C, and C++:**
- `-m64` `-std=c11`

### Peak Compiler Invocation

**C benchmarks:**
- `icc`

**Fortran benchmarks:**
- `ifort`

(Continued on next page)
Huawei CH121 V5 (Intel Xeon Gold 5120)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>87.7</td>
<td>89.6</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jan-2018
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Sep-2017

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

- 619.lbm_s: basepeak = yes
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

- 638.imagick_s: basepeak = yes
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

- 644.nab_s: Same as 638.imagick_s

- 649.fotonik3d_s: basepeak = yes

Fortran benchmarks:

- 603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
  -DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
  -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
  -qopenmp -nostandard-realloc-lhs -align array32byte

- 649.fotonik3d_s: basepeak = yes

- 654.roms_s: Same as 603.bwaves_s

Benchmarks using both Fortran and C:

- 621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
  -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
  -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
  -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

- 627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp

(Continued on next page)
Peak Optimization Flags (Continued)

627.cam4_s (continued):
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-01-21 22:14:56-0500.
Originally published on 2018-02-27.