**Huawei**

**Huawei CH121 V5 (Intel Xeon Bronze 3104)**

- **SPECrat2017_fp_base** = 46.0
- **SPECrat2017_fp_peak** = 47.0

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<td>554.roms_r</td>
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**Hardware**

- **CPU Name**: Intel Xeon Bronze 3104
- **Max MHz.**: 1700
- **Nominal**: 1700
- **Enabled**: 12 cores, 2 chips
- **Orderable**: 1.2 chips
- **Cache L1**: 32 KB I + 32 KB D on chip per core
- **L2**: 1 MB I+D on chip per core
- **L3**: 8.25 MB I+D on chip per chip
- **Memory**: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)
- **Storage**: 1 x 1200 GB SAS, 10000 RPM
- **Other**: None

**Software**

- **OS**: Red Hat Enterprise Linux Server release 7.3 (Maipo)
- **Compiler**: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;

- **Parallel**: No
- **Firmware**: Version 0.31 Released Sep-2017
- **File System**: xfs
- **System State**: Run level 3 (multi-user)
- **Base Pointers**: 64-bit
- **Peak Pointers**: 64-bit
- **Other**: None
Huawei CH121 V5 (Intel Xeon Bronze 3104)

**Results Table**

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</table>

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:

```
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

numactl --interleave=all runcpu <etc>

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

(Continued on next page)
### Huawei CH121 V5 (Intel Xeon Bronze 3104)

<table>
<thead>
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<th>SPECrate2017_fp_peak</th>
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<td>47.0</td>
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</tbody>
</table>

| Copyright 2017-2018 Standard Performance Evaluation Corporation |

#### General Notes (Continued)

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, [http://www.spec.org/osg/policy.html](http://www.spec.org/osg/policy.html)

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

#### Platform Notes

BIOS configuration:
- Power Policy Set to Performance
- XPT Prefetch Set to Enabled
- Sysinfo program `/spec2017/bin/sysinfo`
- Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f running on localhost.localdomain Thu Jan 11 23:09:28 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see [https://www.spec.org/cpu2017/Docs/config.html#sysinfo](https://www.spec.org/cpu2017/Docs/config.html#sysinfo)

From `/proc/cpuinfo`

- model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
- 2 "physical id"s (chips)
- 12 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from `/proc/cpuinfo` might not be reliable. Use with caution.)
  - cpu cores : 6
  - siblings : 6
  - physical 0: cores 0 1 2 3 4 5
  - physical 1: cores 0 1 2 3 4 5

From `lscpu`:
- Architecture: x86_64

(Continued on next page)
Huawei
Huawei CH121 V5 (Intel Xeon Bronze 3104)

SPECrate2017_fp_peak = 47.0
SPECrate2017_fp_base = 46.0

CPU2017 License: 3175  Test Date: Jan-2018
Test Sponsor: Huawei  Hardware Availability: Jul-2017
Tested by: Huawei  Software Availability: Sep-2017

Platform Notes (Continued)

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
Stepping: 4
CPU MHz: 1700.000
BogoMIPS: 3405.02
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-5
NUMA node1 CPU(s): 6-11

/cache/cpustat cache data
cache size : 8448 KB

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5
node 0 size: 194709 MB
node 0 free: 189403 MB
node 1 cpus: 6 7 8 9 10 11
node 1 size: 196608 MB
node 1 free: 191561 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 394145208 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:

(Continued on next page)
Huawei
Huawei CH121 V5 (Intel Xeon Bronze 3104)

SPECrate2017_fp_base = 46.0
SPECrate2017_fp_peak = 47.0

Platform Notes (Continued)

NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 10 04:01

SPEC is set to: /spec2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 859G 50G 810G 6% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMI BIOS" standard.
BIOS INSYDE Corp. 0.31 09/29/2017
Memory:
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2133

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
  CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
==============================================================================
  icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
  CC  519.lbm_r(peak) 544.nab_r(peak)
==============================================================================
  icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
### Huawei CH121 V5 (Intel Xeon Bronze 3104)

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**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Test Date:** Jan-2018  
**Tested by:** Huawei  
**Hardware Availability:** Jul-2017  
**Software Availability:** Sep-2017

#### Compiler Version Notes (Continued)

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Huawei
Huawei CH121 V5 (Intel Xeon Bronze 3104)

**Compiler Version Notes (Continued)**

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**Base Compiler Invocation**

C benchmarks:
- icc

(Continued on next page)
Huawei

Huawei CH121 V5 (Intel Xeon Bronze 3104)

 SPEC CPU2017 Floating Point Rate Result

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Huawei

Huawei CH121 V5 (Intel Xeon Bronze 3104)

SPECrate2017_fp_base = 46.0
SPECrate2017_fp_peak = 47.0

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jan-2018
Hardware Availability: Jul-2017
Software Availability: Sep-2017

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.libm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

(Continued on next page)
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### Base Optimization Flags (Continued)

Fortran benchmarks:
-`-xCORE-AVX2` `-ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
-`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`

Benchmarks using both Fortran and C:
-`-xCORE-AVX2` `-ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
-`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`

Benchmarks using both C and C++:
-`-xCORE-AVX2` `-ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
-`-qopt-mem-layout-trans=3`

Benchmarks using Fortran, C, and C++:
-`-xCORE-AVX2` `-ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
-`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`

### Base Other Flags

C benchmarks:
-`-m64 -std=c11`

C++ benchmarks:
-`-m64`

Fortran benchmarks:
-`-m64`

Benchmarks using both Fortran and C:
-`-m64 -std=c11`

Benchmarks using both C and C++:
-`-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
-`-m64 -std=c11`

### Peak Compiler Invocation

C benchmarks:
-`icc`

(Continued on next page)
Huawei

Huawei CH121 V5 (Intel Xeon Bronze 3104)

| SPECrate2017_fp_base = 46.0 |
| SPECrate2017_fp_peak = 47.0 |

CPU2017 License: 3175  Test Date: Jan-2018
Test Sponsor: Huawei  Hardware Availability: Jul-2017
Tested by: Huawei  Software Availability: Sep-2017

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbmr_r:basepeak = yes

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

C++ benchmarks:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

-nostandard-realloc-lhs -align array32byte

(Continued on next page)
Huawei CH121 V5 (Intel Xeon Bronze 3104)

Huawei

SPECrate2017_fp_base = 46.0

SPECrate2017_fp_peak = 47.0

CPU2017 License: 3175
Test Date: Jan-2018
Test Sponsor: Huawei
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Sep-2017

Peak Optimization Flags (Continued)

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:

521.wrf_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

(Continued on next page)
Huawei

Huawei CH121 V5 (Intel Xeon Bronze 3104)  

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SPECrate2017_fp_peak = 47.0

CPU2017 License: 3175  
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Tested by: Huawei  
Hardware Availability: Jul-2017

Software Availability: Sep-2017

Peak Other Flags (Continued)

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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