# SPEC® CPU2017 Floating Point Rate Result

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## Lenovo Global Technology

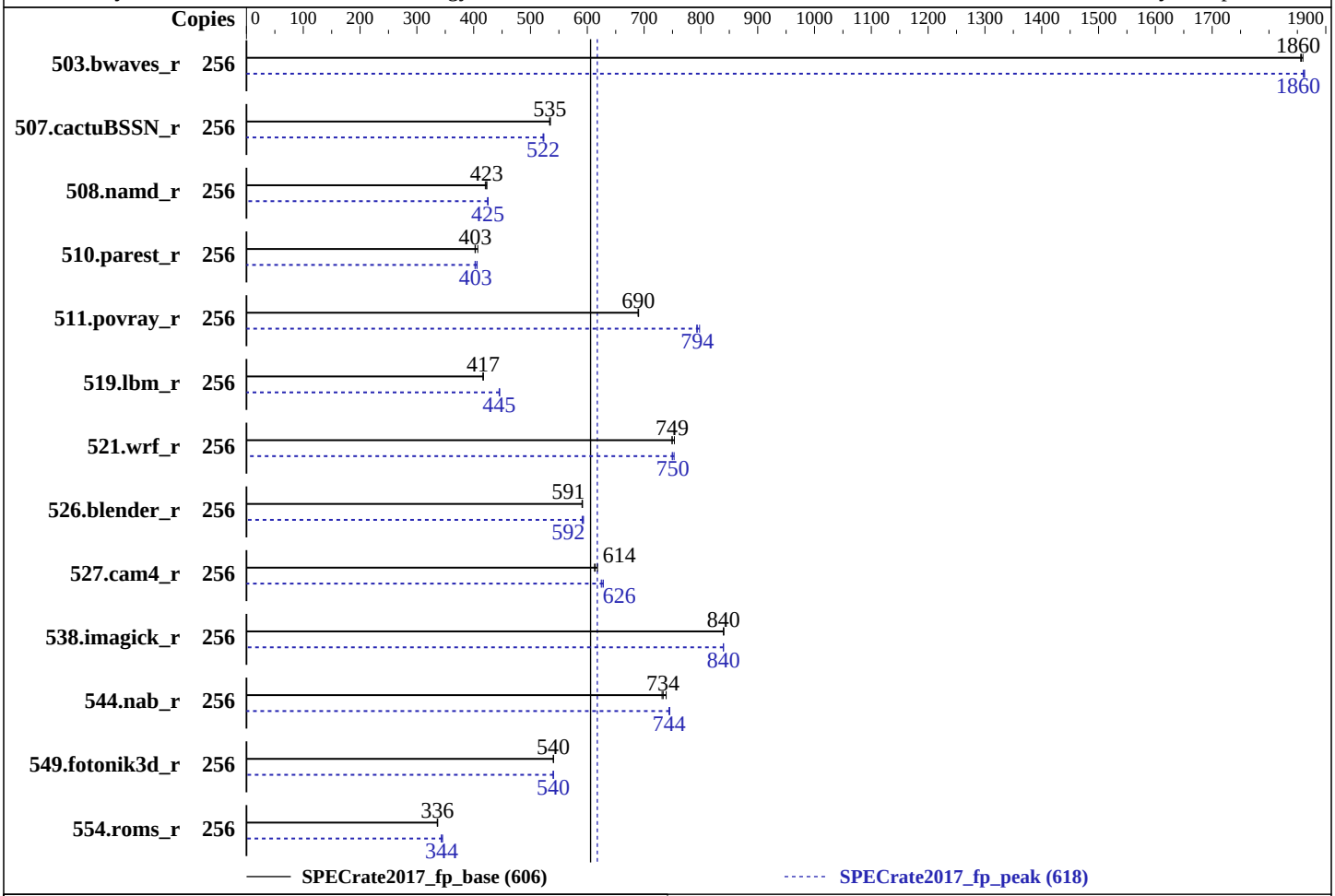
ThinkSystem SR950
(2.00 GHz, Intel Xeon Platinum 8153)

SPECrate2017_fp_base = 606

SPECrate2017_fp_peak = 618

**CPU2017 License:** 9017
**Test Sponsor:** Lenovo Global Technology
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018
**Hardware Availability:** Sep-2017
**Software Availability:** Sep-2017




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon Platinum 8153 |
| Max MHz.: | 2800 |
| Nominal: | 2000 |
| Enabled: | 128 cores, 8 chips, 2 threads/core |
| Orderable: | 2,4,8 chips |
| Cache L1: | 32 KB I + 32 KB D on chip per core |
| L2: | 1 MB I+D on chip per core |
| L3: | 22 MB I+D on chip per chip |
| Other: | None |
| Memory: | 3 TB (96 x 32 GB 2Rx4 PC4-2666V-R) |
| Storage: | 800 GB tmpfs |
| Other: | None |

### Software

| | |
|---|---|
| OS: | SUSE Linux Enterprise Server 12 SP2 (x86_64) Kernel 4.4.21-69-default |
| Compiler: | C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux |
| Parallel: | No |
| Firmware: | Lenovo BIOS Version PSE105X 1.00 released Aug-2017 |
| File System: | tmpfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 64-bit |
| Other: | None |

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**Software Availability:** Sep-2017

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r | 256 | 1383 | 1860 | **1382** | **1860** | 1380 | 1860 | 256 | 1378 | 1860 | 1380 | 1860 | **1378** | **1860** |
| 507.cactuBSSN_r | 256 | 607 | 534 | **606** | **535** | 606 | 535 | 256 | 620 | 522 | **620** | **522** | 619 | 524 |
| 508.namd_r | 256 | **575** | **423** | 578 | 420 | 575 | 423 | 256 | 573 | 424 | 572 | 425 | **572** | **425** |
| 510.parest_r | 256 | 1645 | 407 | 1664 | 402 | **1662** | **403** | 256 | **1660** | **403** | 1664 | 402 | 1649 | 406 |
| 511.povray_r | 256 | **866** | **690** | 867 | 689 | 866 | 690 | 256 | 754 | 793 | **753** | **794** | 749 | 798 |
| 519.lbm_r | 256 | 648 | 417 | 647 | 417 | **648** | **417** | 256 | 607 | 445 | **606** | **445** | 605 | 446 |
| 521.wrf_r | 256 | 761 | 753 | **765** | **749** | 765 | 749 | 256 | 765 | 749 | 762 | 753 | **764** | **750** |
| 526.blender_r | 256 | 660 | 591 | 659 | 592 | **660** | **591** | 256 | **658** | **592** | 657 | 593 | 660 | 591 |
| 527.cam4_r | 256 | 731 | 613 | 725 | 618 | **729** | **614** | 256 | 713 | 628 | 717 | 625 | **715** | **626** |
| 538.imagick_r | 256 | 758 | 840 | 758 | 840 | **758** | **840** | 256 | **758** | **840** | 758 | 840 | 759 | 839 |
| 544.nab_r | 256 | 583 | 739 | **587** | **734** | 589 | 732 | 256 | **579** | **744** | 578 | 745 | 579 | 744 |
| 549.fotonik3d_r | 256 | 1846 | 540 | 1845 | 541 | **1846** | **540** | 256 | **1847** | **540** | 1846 | 541 | 1848 | 540 |
| 554.roms_r | 256 | **1210** | **336** | 1208 | 337 | 1211 | 336 | 256 | **1184** | **344** | 1178 | 345 | 1186 | 343 |

**SPECrate2017_fp_base = 606**

**SPECrate2017_fp_peak = 618**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Tmpfs filesystem can be set with:
 mount -t tmpfs -o size=800g tmpfs /home
Process tuning setting:
 echo 50000     > /proc/sys/kernel/sched_cfs_bandwidth_slice_us
 echo 240000000 > /proc/sys/kernel/sched_latency_ns
 echo 5000000   > /proc/sys/kernel/sched_migration_cost_ns
 echo 100000000 > /proc/sys/kernel/sched_min_granularity_ns
 echo 150000000 > /proc/sys/kernel/sched_wakeup_granularity_ns
```

## General Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.4
```

**(Continued on next page)**

## General Notes (Continued)

```
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>
No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on
past performance using the historical hardware and/or
software described on this result page.

The system as described on this result page was formerly
generally available.  At the time of this publication, it may
not be shipping, and/or may not be supported, and/or may fail
to meet other tests of General Availability described in the
SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result
that would be measured were this benchmark run with hardware
and software available as of the publication date.
```

## Platform Notes

```
BIOS configuration:
Choose Operating Mode set to Maximum Performance
SNC set to Enable
DCU Streamer Prefetcher set to Disable
MONITORM/WAIT set to Enable
Execute Disable Bit set to Disable
Trusted Execution Technology set to Enable
Per Core Pstate set to Disable
XPT Prefetcher set to Enable
Stale AtoS set to Enable
LLC Deadline Alloc set to Enable
 Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on linux-boxi Thu Jan 11 00:20:11 2018

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
```

**(Continued on next page)**

## Platform Notes (Continued)

```
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

  From /proc/cpuinfo
     model name : Intel(R) Xeon(R) Platinum 8153 CPU @ 2.00GHz
        8  "physical id"s (chips)
        256 "processors"
     cores, siblings (Caution: counting these is hw and system dependent. The following
     excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
        cpu cores : 16
        siblings  : 32
        physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 4: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 5: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 6: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 7: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

  From lscpu:
       Architecture:          x86_64
       CPU op-mode(s):        32-bit, 64-bit
       Byte Order:            Little Endian
       CPU(s):                256
       On-line CPU(s) list:   0-255
       Thread(s) per core:    2
       Core(s) per socket:    16
       Socket(s):             8
       NUMA node(s):          16
       Vendor ID:             GenuineIntel
       CPU family:            6
       Model:                 85
       Model name:            Intel(R) Xeon(R) Platinum 8153 CPU @ 2.00GHz
       Stepping:              4
       CPU MHz:               1995.327
       BogoMIPS:              3990.65
       Virtualization:        VT-x
       L1d cache:             32K
       L1i cache:             32K
       L2 cache:              1024K
       L3 cache:              22528K
       NUMA node0 CPU(s):     0-3,8-11,128-131,136-139
       NUMA node1 CPU(s):     4-7,12-15,132-135,140-143
       NUMA node2 CPU(s):     16-19,24-27,144-147,152-155
       NUMA node3 CPU(s):     20-23,28-31,148-151,156-159
       NUMA node4 CPU(s):     32-35,40-43,160-163,168-171
       NUMA node5 CPU(s):     36-39,44-47,164-167,172-175
```

**(Continued on next page)**

## Platform Notes (Continued)

```
      NUMA node6 CPU(s):     48-51,56-59,176-179,184-187
      NUMA node7 CPU(s):     52-55,60-63,180-183,188-191
      NUMA node8 CPU(s):     64-67,72-75,192-195,200-203
      NUMA node9 CPU(s):     68-71,76-79,196-199,204-207
      NUMA node10 CPU(s):    80-83,88-91,208-211,216-219
      NUMA node11 CPU(s):    84-87,92-95,212-215,220-223
      NUMA node12 CPU(s):    96-99,104-107,224-227,232-235
      NUMA node13 CPU(s):    100-103,108-111,228-231,236-239
      NUMA node14 CPU(s):    112-115,120-123,240-243,248-251
      NUMA node15 CPU(s):    116-119,124-127,244-247,252-255
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
      pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
      aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
      fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
      xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
      tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
      erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
      avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

  /proc/cpuinfo cache data
     cache size : 22528 KB

  From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
    available: 16 nodes (0-15)
    node 0 cpus: 0 1 2 3 8 9 10 11 128 129 130 131 136 137 138 139
    node 0 size: 192984 MB
    node 0 free: 192366 MB
    node 1 cpus: 4 5 6 7 12 13 14 15 132 133 134 135 140 141 142 143
    node 1 size: 193528 MB
    node 1 free: 192932 MB
    node 2 cpus: 16 17 18 19 24 25 26 27 144 145 146 147 152 153 154 155
    node 2 size: 193528 MB
    node 2 free: 193002 MB
    node 3 cpus: 20 21 22 23 28 29 30 31 148 149 150 151 156 157 158 159
    node 3 size: 193528 MB
    node 3 free: 188018 MB
    node 4 cpus: 32 33 34 35 40 41 42 43 160 161 162 163 168 169 170 171
    node 4 size: 193528 MB
    node 4 free: 192962 MB
    node 5 cpus: 36 37 38 39 44 45 46 47 164 165 166 167 172 173 174 175
    node 5 size: 193528 MB
    node 5 free: 193012 MB
    node 6 cpus: 48 49 50 51 56 57 58 59 176 177 178 179 184 185 186 187
    node 6 size: 193528 MB
    node 6 free: 193001 MB
```

**(Continued on next page)**

## Platform Notes (Continued)

```
   node 7 cpus: 52 53 54 55 60 61 62 63 180 181 182 183 188 189 190 191
   node 7 size: 193528 MB
   node 7 free: 193012 MB
   node 8 cpus: 64 65 66 67 72 73 74 75 192 193 194 195 200 201 202 203
   node 8 size: 193528 MB
   node 8 free: 184376 MB
   node 9 cpus: 68 69 70 71 76 77 78 79 196 197 198 199 204 205 206 207
   node 9 size: 193528 MB
   node 9 free: 193009 MB
   node 10 cpus: 80 81 82 83 88 89 90 91 208 209 210 211 216 217 218 219
   node 10 size: 193528 MB
   node 10 free: 192989 MB
   node 11 cpus: 84 85 86 87 92 93 94 95 212 213 214 215 220 221 222 223
   node 11 size: 193528 MB
   node 11 free: 192991 MB
   node 12 cpus: 96 97 98 99 104 105 106 107 224 225 226 227 232 233 234 235
   node 12 size: 193528 MB
   node 12 free: 192991 MB
   node 13 cpus: 100 101 102 103 108 109 110 111 228 229 230 231 236 237 238 239
   node 13 size: 193528 MB
   node 13 free: 193000 MB
   node 14 cpus: 112 113 114 115 120 121 122 123 240 241 242 243 248 249 250 251
   node 14 size: 193528 MB
   node 14 free: 193011 MB
   node 15 cpus: 116 117 118 119 124 125 126 127 244 245 246 247 252 253 254 255
   node 15 size: 193523 MB
   node 15 free: 193010 MB
   node distances:
   node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15
     0:  10  20  20  20  20  20  20  20  20  20  20  20  20  20  20  20
     1:  20  10  20  20  20  20  20  20  20  20  20  20  20  20  20  20
     2:  20  20  10  20  20  20  20  20  20  20  20  20  20  20  20  20
     3:  20  20  20  10  20  20  20  20  20  20  20  20  20  20  20  20
     4:  20  20  20  20  10  20  20  20  20  20  20  20  20  20  20  20
     5:  20  20  20  20  20  10  20  20  20  20  20  20  20  20  20  20
     6:  20  20  20  20  20  20  10  20  20  20  20  20  20  20  20  20
     7:  20  20  20  20  20  20  20  10  20  20  20  20  20  20  20  20
     8:  20  20  20  20  20  20  20  20  10  20  20  20  20  20  20  20
     9:  20  20  20  20  20  20  20  20  20  10  20  20  20  20  20  20
    10:  20  20  20  20  20  20  20  20  20  20  10  20  20  20  20  20
    11:  20  20  20  20  20  20  20  20  20  20  20  10  20  20  20  20
    12:  20  20  20  20  20  20  20  20  20  20  20  20  10  20  20  20
    13:  20  20  20  20  20  20  20  20  20  20  20  20  20  10  20  20
    14:  20  20  20  20  20  20  20  20  20  20  20  20  20  20  10  20
    15:  20  20  20  20  20  20  20  20  20  20  20  20  20  20  20  10

  From /proc/meminfo
```

**(Continued on next page)**

## Platform Notes (Continued)

```
    MemTotal:       3170207908 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
    SuSE-release:
       SUSE Linux Enterprise Server 12 (x86_64)
       VERSION = 12
       PATCHLEVEL = 2
       # This file is deprecated and will be removed in a future service pack or release.
       # Please check /etc/os-release for details about this release.
    os-release:
       NAME="SLES"
       VERSION="12-SP2"
       VERSION_ID="12.2"
       PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
       ID="sles"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:12:sp2"

  uname -a:
    Linux linux-boxi 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
    x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jan 10 19:26

  SPEC is set to: /home/cpu2017.1.0.2.ic18.0
    Filesystem     Type   Size  Used Avail Use% Mounted on
    tmpfs          tmpfs  800G   11G  790G   2% /home

  Additional information from dmidecode follows.  WARNING: Use caution when you interpret
  this section. The 'dmidecode' program reads system data which is "intended to allow
  hardware to be accurately determined", but the intent may not be met, as there are
  frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS Lenovo -[PSE105X-1.00]- 08/17/2017
    Memory:
     96x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

  (End of data from sysinfo program)
```

## Compiler Version Notes

```
==============================================================================
 CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
```


**(Continued on next page)**

## Compiler Version Notes (Continued)

```
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC   519.lbm_r(peak) 544.nab_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CXXC 508.namd_r(peak) 510.parest_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CC  511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC   511.povray_r(peak) 526.blender_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  507.cactuBSSN_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
```

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## Compiler Version Notes (Continued)

```
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC   507.cactuBSSN_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC   554.roms_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CC  521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC   521.wrf_r(peak) 527.cam4_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
```

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## Compiler Version Notes (Continued)

---

## Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

## Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

## Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

## Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 519.lbm_r

C++ benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:

**(Continued on next page)**

## Peak Optimization Flags (Continued)

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

## Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

**(Continued on next page)**

## Peak Other Flags (Continued)

Benchmarks using Fortran, C, and C++:
`-m64 -std=c11`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html`
`http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml`
`http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml`


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